



# CINT2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

**SGI**  
SGI Altix 3000 (1300MHz, Itanium 2)

SPECint\_rate2000 = 79.4  
SPECint\_rate\_base2000 = 79.4

SPEC license #: 4 | Tested by: SGI | Test date: Jun-2003 | Hardware Avail: Jun-2003 | Software Avail: Jun-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	179	72.7	8	179	72.7
175.vpr	8	171	76.0	8	171	76.0
176.gcc	8	108	94.3	8	108	94.3
181.mcf	8	287	58.3	8	287	58.3
186.crafty	8	111	83.8	8	111	83.8
197.parser	8	295	56.7	8	295	56.7
252.eon	8	120	100	8	120	100
253.perlbnk	8	206	81.3	8	206	81.3
254.gap	8	162	63.0	8	162	63.0
255.vortex	8	152	116	8	152	116
256.bzip2	8	179	77.8	8	179	77.8
300.twolf	8	297	93.6	8	297	93.6

### Hardware

CPU: Intel Itanium 2  
 CPU MHz: 1300  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 8 chips, 1 core/chip  
 CPU(s) orderable: 4-64  
 Parallel: No  
 Primary Cache: 16KBI + 16KBD (on chip) per CPU  
 Secondary Cache: 256KB (on chip) per CPU  
 L3 Cache: 3.0MB (on chip) per CPU  
 Other Cache: N/A  
 Memory: 16 GB (16\*512MB DIMMS per 4cpu module)  
 Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)  
 Other Hardware: None

### Software

Operating System: SGI ProPack(TM) v2.2  
 Compiler: Intel(R) C++ Compiler for Linux 7.1 (Build 20030507)  
 File System: xfs  
 System State: Single-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

### Baseline optimization flags:

C programs: -ipo -O3 +FDO  
 C++ programs: -ipo -O2 +FDO -ansi\_alias

### Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca -D\_LIBC  
 186.crafty: -DLINUX\_i386  
 252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST  
 253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
 -DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
 254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
 -DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
 255.vortex: -DSPEC\_CPU2000\_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).