



CINT2000 Result

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Dell
PowerEdge 6650 (2.8 GHz Xeon MP)

SPECint2000 = **1234**
SPECint_base2000 = **1204**

SPEC license #: 55 Tested by: Dell, Round Rock, TX Test date: Jun-2003 Hardware Avail: Jun-2003 Software Avail: Apr-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	500 1000 1500 2000			
164.gzip	1400	129	1089	128	1091	[Bar chart showing ratio 1091]			
175.vpr	1400	146	959	146	958	[Bar chart showing ratio 958]			
176.gcc	1100	75.8	1452	76.3	1442	[Bar chart showing ratio 1442]			
181.mcf	1800	204	882	199	905	[Bar chart showing ratio 905]			
186.crafty	1000	91.3	1096	91.2	1096	[Bar chart showing ratio 1096]			
197.parser	1800	160	1125	160	1126	[Bar chart showing ratio 1126]			
252.eon	1300	105	1236	90.7	1433	[Bar chart showing ratio 1433]			
253.perlbmk	1800	143	1262	138	1304	[Bar chart showing ratio 1304]			
254.gap	1100	77.7	1416	77.7	1416	[Bar chart showing ratio 1416]			
255.vortex	1900	106	1794	106	1794	[Bar chart showing ratio 1794]			
256.bzip2	1500	147	1024	140	1071	[Bar chart showing ratio 1071]			
300.twolf	3000	217	1383	206	1460	[Bar chart showing ratio 1460]			

Hardware

CPU: Intel Xeon MP (400 MHz system bus)
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
 CPU(s) orderable: 1,2,4
 Parallel: No
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
 Secondary Cache: 512KB(I+D) on chip
 L3 Cache: 2048KB(I+D) on chip
 Other Cache: N/A
 Memory: 4 x 1024MB DDR200
 Disk Subsystem: 1 x 36GB SEAGATE ST336706LC
 Other Hardware:

Software

Operating System: Windows 2000 Advanced Server (SP2)
 Compiler: Intel C++ Compiler 7.1 (20030402Z)
 Microsoft Visual Studio .NET (7.0.9466)
 MicroQuill SmartHeap Library 6.01
 NTFS
 System State: Hyper-Threading Technology disabled using BIOS switch. Additional CPUs disabled using /noexec flag in Windows boot.ini file.

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
 186.crafy: -DNT_i386
 253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK DIRECTED OPTIMIZATION

ONESTEP=yes
 FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

BASE TUNING

C: -QxW -Qipo -O3 +FDO
 C++: -QxW -Qipo -GX -GR

PEAK TUNING

164.gzip: -QxW -Qipo -Oa -O3 +FDO
 175.vpr: -QxW -Qipo -O3 +FDO
 176.gcc: -QxW -Qipo -O3 +FDO
 181.mcf: -QxW -Qipo -O3 +FDO shlw32M.lib
 186.crafty: -QxW -Qipo -O3 +FDO
 197.parser: -QxW -Qipo -O3 +FDO
 252.eon: -QxW -Qipo -O3 +FDO
 253.perlbmk: -QxW -Qipo -O3 +FDO shlw32M.lib



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Notes/Tuning Information (Continued)

```
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:    -Qipo -Oa -Qunroll11 +FDO
300.twolf:    -Qipo -QxW -O3 -Oa      shlW32M.lib
EXTRA LIBRARIES
shlW32M.lib:  MicroQuill SmartHeap Library 6.01
              www.microquill.com
```