



# CFP2000 Result

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## Fujitsu Siemens Computers CELSIUS V810

SPECfp2000 = 1420  
SPECfp\_base2000 = 1408

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: Nov-2003 Hardware Avail: Dec-2003 Software Avail: Nov-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	102	1569	102	1569	
171.swim	3100	159	1944	159	1944	
172.mgrid	1800	139	1298	135	1338	
173.applu	2100	170	1233	170	1238	
177.mesa	1400	89.0	1574	84.8	1650	
178.galgel	2900	116	2502	116	2502	
179.art	2600	204	1277	204	1277	
183.quake	1300	102	1272	102	1272	
187.facerec	1900	98.0	1940	98.0	1940	
188.amp	2200	175	1258	174	1263	
189.lucas	2000	151	1328	146	1370	
191.fma3d	2100	154	1360	154	1360	
200.sixtrack	1100	164	672	164	672	
301.apsi	2600	199	1306	199	1306	

### Hardware

CPU: AMD Opteron (TM) 248  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
 CPU(s) orderable: 1,2  
 Parallel: No  
 Primary Cache: 64KBI + 64KBD on chip  
 Secondary Cache: 1024KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: --  
 Memory: 4 x 1 GB PC2700 DDR SDRAM, ECC, Registered  
 Disk Subsystem: Maxtor ST020H2  
 Other Hardware: None

### Software

Operating System: SuSE Linux 9.0 (AMD64)  
 2.4.21-102-default  
 Compiler: PGI Fortran 5.1-2,  
 SuSE gcc 3.3.1 compiler  
 (from SuSE Linux 9.0)  
 File System: Linux/reiserfs  
 System State: Multi-user SuSE Run level 3

## Notes/Tuning Information

+FDO: PASS1=-fprofile-arcs PASS2=-fbranch-probabilities  
 pgf90 is the PGI Fortran 5.1-2 compiler  
 gcc is the SuSE gcc 3.3.1 compiler  
 Portability:

PORTABILITY=-DSPEC\_CPU2000\_LP64 is applied to all the C benchmarks.

178.galgel: -Mfixed  
 Baseline: C gcc -O3 -funroll-all-loops +FDO  
 Baseline: Fortran pgf90 -fastsse -Mipa=fast  
 Peak tuning:

168.wupwise: basepeak=true  
 171.swim: basepeak=true  
 172.mgrid: pgf90 -fast -Mipa=fast  
 173.applu: pgf90 -fast -Mipa=fast  
 177.mesa: gcc -O3 -funroll-all-loops -finline-limit=2000 +FDO  
 178.galgel: basepeak=true  
 179.art: basepeak=true  
 183.quake: basepeak=true



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## Notes/Tuning Information (Continued)

```
187.facerec:    basepeak=true
188.ammf:      gcc -O3 -funroll-all-loops -ffast-math -finline-limit=2000 +FDO
189.lucas:     pgf90 -fastsse -Mipa=fast -Mnosmart
191.fma3d:     basepeak=true
200.sixtrack:  basepeak=true
301.apsi:      basepeak=true
```

ONESTEP is used for all base and peak runs

System tested with only one CPU present.  
BIOS default settings have been used. These include:  
ECC memory checking disabled,  
node interleave disabled, SRAT enabled.

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<http://www.fujitsu-siemens.com/countries>