



# CINT2000 Result

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## SGI

SGI Altix 3700 Bx2 (1600MHz 6M L3, Itanium 2)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 1956

SPEC license #: 4 | Tested by: SGI | Test date: Nov-2004 | Hardware Avail: Nov-2004 | Software Avail: Nov-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	128	135	1544			
175.vpr	128	125	1662			
176.gcc	128	93.5	1748			
181.mcf	128	102	2625			
186.crafty	128	78.6	1890			
197.parser	128	181	1477			
252.eon	128	67.0	2882			
253.perlbnk	128	138	1932			
254.gap	128	122	1340			
255.vortex	128	89.7	3144			
256.bzip2	128	122	1826			
300.twolf	128	200	2227			

### Hardware

CPU: Intel Itanium 2  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 128 cores, 128 chips, 1 core/chip  
CPU(s) orderable: 16-256  
Parallel: No  
Primary Cache: 16KBI + 16KBD on chip, per core  
Secondary Cache: 256KB (I+D) on chip, per core  
L3 Cache: 6.0MB (I+D) on chip, per core  
Other Cache: N/A  
Memory: 256 GB (32 512MB PC2700 DIMMS per 8 core module)  
Disk Subsystem: 1 x 73 GB SCSI (Seagate Cheetah 10k rpm)  
Other Hardware: None

### Software

Operating System: SGI ProPack(TM) v3.0 Service Pack 1  
Compiler: Intel(R) C++ Compiler for Linux 8.1 (Build 20041021)  
MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
File System: xfs  
System State: Multi-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
C++ programs: -fast -ansi\_alias +FDO  
Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca  
186.crafty: -DLINUX\_i386  
252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
-DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
-DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
255.vortex: -DSPEC\_CPU2000\_LP64 srcalt=closed\_files

Processes were bound to CPUs using dplace.