



# CFP2000 Result

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Acer Incorporated  
Altos R710 (3.6 GHz Intel Xeon)

SPECfp\_rate2000 = 32.2

SPECfp\_rate\_base2000 = 31.8

SPEC license #: 97 Tested by: Acer Incorporated Test date: Apr-2005 Hardware Avail: Apr-2005 Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	72.9	50.9	2	73.1	50.8
171.swim	2	276	26.1	2	276	26.1
172.mgrid	2	172	24.3	2	172	24.3
173.applu	2	216	22.6	2	210	23.2
177.mesa	2	89.0	36.5	2	89.0	36.5
178.galgel	2	100	67.1	2	101	66.8
179.art	2	82.4	73.2	2	82.4	73.2
183.quake	2	82.5	36.6	2	82.4	36.6
187.facerec	2	119	37.0	2	119	37.0
188.amp	2	206	24.8	2	177	28.8
189.lucas	2	186	24.9	2	187	24.9
191.fma3d	2	188	25.9	2	188	25.9
200.sixtrack	2	170	15.0	2	168	15.2
301.apsi	2	235	25.7	2	233	25.9

### Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 512MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 36GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran Programs : -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.quake:Base for C Program: -fast +FDO shlw32M.lib
188.amp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: -fast -Qscalar_rep- -Qauto +FDO
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```



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## Notes/Tuning Information (Continued)

```
187.facerec: basepeak=yes
188.ammf: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
191.fma3d: basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi: -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```