



# CFP2000 Result

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## Acer Incorporated

Acer Altos R710 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECfp2000 = 1916

SPECfp\_base2000 = 1915

SPEC license #: 97 Tested by: Acer Incorporated Test date: Oct-2005 Hardware Avail: Oct-2005 Software Avail: May-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	58.8	2723	58.9	2717	
171.swim	3100	134	2314	135	2296	
172.mgrid	1800	114	1575	114	1573	
173.applu	2100	133	1584	133	1584	
177.mesa	1400	84.7	1652	84.7	1652	
178.galgel	2900	76.6	3787	76.8	3777	
179.art	2600	58.4	4449	58.4	4449	
183.earthquake	1300	47.4	2744	47.3	2750	
187.facerec	1900	97.3	1953	97.3	1953	
188.amp	2200	183	1200	180	1223	
189.lucas	2000	97.3	2056	97.5	2051	
191.fma3d	2100	138	1527	138	1527	
200.sixtrack	1100	145	756	145	756	
301.apsi	2600	195	1333	194	1339	

### Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3800  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disable)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 1024MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs : -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.earthquake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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## Notes/Tuning Information (Continued)

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>