



CINT2000 Result

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Hewlett-Packard Company
ProLiant BL20P G4 (3.0GHz, Intel Xeon processor 5160)

SPECint_rate2000 = 34.9
SPECint_rate_base2000 = 34.8

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	1	75.7	21.4	1	75.7	21.5
175.vpr	1	65.6	24.8	1	63.1	25.7
176.gcc	1	35.5	35.9	1	35.5	35.9
181.mcf	1	43.3	48.2	1	43.3	48.2
186.crafty	1	36.3	31.9	1	36.3	31.9
197.parser	1	73.5	28.4	1	73.1	28.5
252.eon	1	33.1	45.5	1	33.2	45.4
253.perlbnk	1	50.1	41.7	1	49.7	42.0
254.gap	1	38.1	33.5	1	38.0	33.5
255.vortex	1	38.6	57.1	1	38.6	57.2
256.bzip2	1	66.6	26.1	1	66.6	26.1
300.twolf	1	87.3	39.9	1	87.3	39.9

Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Parallel: No
 Primary Cache: 32KBI + 32KBD (on chip) per core
 Secondary Cache: 4096KB(I+D) (on chip) shared
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8x1024MB PC2-5300F
 Disk Subsystem: 1x36GB 10K SAS
 Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
 Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.zip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode