



CINT2000 Result

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Hewlett-Packard Company

ProLiant DL380 G5 (2.33GHz, Intel Xeon processor 5140)

SPECint_rate2000 = 100

SPECint_rate_base2000 = 99.5

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	99.4	65.3	4	99.4	65.4
175.vpr	4	92.8	70.0	4	88.4	73.5
176.gcc	4	45.7	112	4	45.7	112
181.mcf	4	89.9	92.9	4	89.9	92.9
186.crafty	4	46.8	99.1	4	46.9	98.9
197.parser	4	102	81.7	4	102	82.2
252.eon	4	42.6	141	4	42.7	141
253.perlbnk	4	66.7	125	4	66.4	126
254.gap	4	52.6	97.0	4	51.9	98.3
255.vortex	4	54.1	163	4	54.1	163
256.bzip2	4	98.1	70.9	4	98.1	70.9
300.twolf	4	112	124	4	112	124

Hardware

CPU: Intel Xeon processor 5140 (2.33GHz, 4MB L2 shared, 1333MHz bus)
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Parallel: No
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 16 GB (8x2048 MB PC2-5300F)
 Disk Subsystem: 1x36 GB 10 K SAS
 Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
 Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERL DLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org



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Notes/Tuning Information (Continued)

256.bzip2: basepeak=1
300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled