



CINT2000 Result

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ASUS Computer International
Asus M2N32-SLI Deluxe, AMD Athlon (TM) 64 FX-62

SPECint2000 = **2061**
SPECint_base2000 = **1923**

SPEC license #: 13 Tested by: Intel Corporation Test date: Jul-2006 Hardware Avail: Jun-2006 Software Avail: Jun-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	89.4	1567	89.4	1567	
175.vpr	1400	92.4	1515	91.0	1538	
176.gcc	1100	50.9	2159	50.9	2159	
181.mcf	1800	136	1321	111	1625	
186.crafty	1000	54.9	1822	49.3	2030	
197.parser	1800	101	1780	102	1768	
252.eon	1300	38.3	3393	37.6	3455	
253.perlbnk	1800	84.7	2126	77.5	2321	
254.gap	1100	54.1	2033	51.4	2140	
255.vortex	1900	66.7	2850	58.4	3253	
256.bzip2	1500	103	1456	96.8	1549	
300.twolf	3000	156	1918	134	2238	

Hardware

CPU: AMD Athlon (TM) 64 FX-62
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1
Parallel: no
Primary Cache: 64KBI + 64KBD on chip, per core
Secondary Cache: 1MB (I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 2x1 GB, Corsair CM2X1024-8500C5 DDR2-1066 (manually set to DDR2-800 5-5-5-15 in the bios)
Disk Subsystem: SATA, Western Digital WD740GD, 10000 rpm
Other Hardware: None

Software

Operating System: Microsoft Windows XP SP2
Compiler: Intel C++ 9.1 build 20060519Z for IA32
Microsoft Visual Studio .NET 7.0.9466 (libraries)
MicroQuill Smartheap Library 8.0
File System: NTFS
System State: default

Notes/Tuning Information

GENERAL

+FDO: PASS1= -Qprof_gen PASS2= -Qprof_use
shlw32M-80.lib is the SmartHeap library V8.0 from MicroQuill www.microquill.com

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast -arch:SSE2 -Oi- +FDO
for C++ programs: -fast -arch:SSE2 -Qcxx-features +FDO

164.gzip: basepeak=yes
175.vpr: -O3 -Qipo -QxW -Qoption,c,-ip_ninl_max_stats=2000,
-ip_ninl_max_total_stats=4500 +FDO
176.gcc: basepeak=yes
181.mcf: -O3 -Qipo -QaxN +FDO shlw32M-80.lib



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SPECint2000 = 2061
SPECint_base2000 = 1923

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Notes/Tuning Information (Continued)

```
186.crafty: -O3 -Qipo -QxW +FDO shlw32M-80.lib
197.parser: -Oi -Qipo -QxW +FDO shlw32M-80.lib
252.eon: -O3 -Qipo -QxW -Qansi_alias -Qoption,c,-ip_ninl_max_stats=2000,
        -ip_ninl_max_total_stats=4500 +FDO shlw32M-80.lib
253.perlbnk: -O3 -Qipo -QxW +FDO shlw32M-80.lib
254.gap: -O3 -Qipo -QxW +FDO shlw32M-80.lib
255.vortex: -O3 -Qipo -arch:SSE -Oi- -Qoption,c,-ip_ninl_max_stats=2000,
        -ip_ninl_max_total_stats=4500 +FDO shlw32M-80.lib
```

The SUT was built with a discrete PCIe graphics card (ATI Radeon X1900 XTX) and an ATX 550W power supply. The 2 DIMMs were configured in dual-channel mode.