



# CINT2000 Result

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## Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor E5320, 1.86 GHz

SPECint\_rate2000 = 148

SPECint\_rate\_base2000 = 148

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Sep-2006 | Hardware Avail: Nov-2006 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	8	127	103	8	127	103
175.vpr	8	122	107	8	122	107
176.gcc	8	59.9	170	8	59.9	170
181.mcf	8	153	109	8	153	109
186.crafty	8	58.8	158	8	58.8	158
197.parser	8	131	127	8	131	127
252.eon	8	53.9	224	8	53.9	224
253.perlbnk	8	83.4	200	8	83.4	200
254.gap	8	92.1	111	8	92.1	111
255.vortex	8	69.4	254	8	69.4	254
256.bzip2	8	125	111	8	125	111
300.twolf	8	141	197	8	141	197

### Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Parallel: No  
 Primary Cache: 32KB(I) + 32KB(D) on chip, per core  
 Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)  
 Disk Subsystem: Seagate ST373454SS (SAS, 15.4krpm, 73GB)  
 Other Hardware: none

### Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
 Compiler: Intel C++ Compiler 9.1  
 Build 20060323Z (for 32-bit applications),  
 Microsoft Visual Studio .Net 2003  
 7.1.3088 (for libraries),  
 SmartHeap Library Version 8.0  
 from <http://www.microquill.com>  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### GENERAL

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### Portability flags

176.gcc: -Dalloca=\_alloca /F10000000  
 186.crafty: -DNT\_i386  
 253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
 254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### Base tuning flags

for C programs: -fast +FDO shlw32m.lib  
 for C++ programs: -fast -Qcxx\_features +FDO shlw32m.lib

### Peak tuning flags

same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of the operating system.



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## Notes/Tuning Information (Continued)

BIOS configuration:

Adjacent Sector Prefetch = disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3  
are electronically equivalent.

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<http://www.fujitsu-siemens.com/countries>