



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = Not Run

IBM System x3250 (Intel Xeon X3210)

SPECint_base2006 = 13.8

CPU2006 license: 11

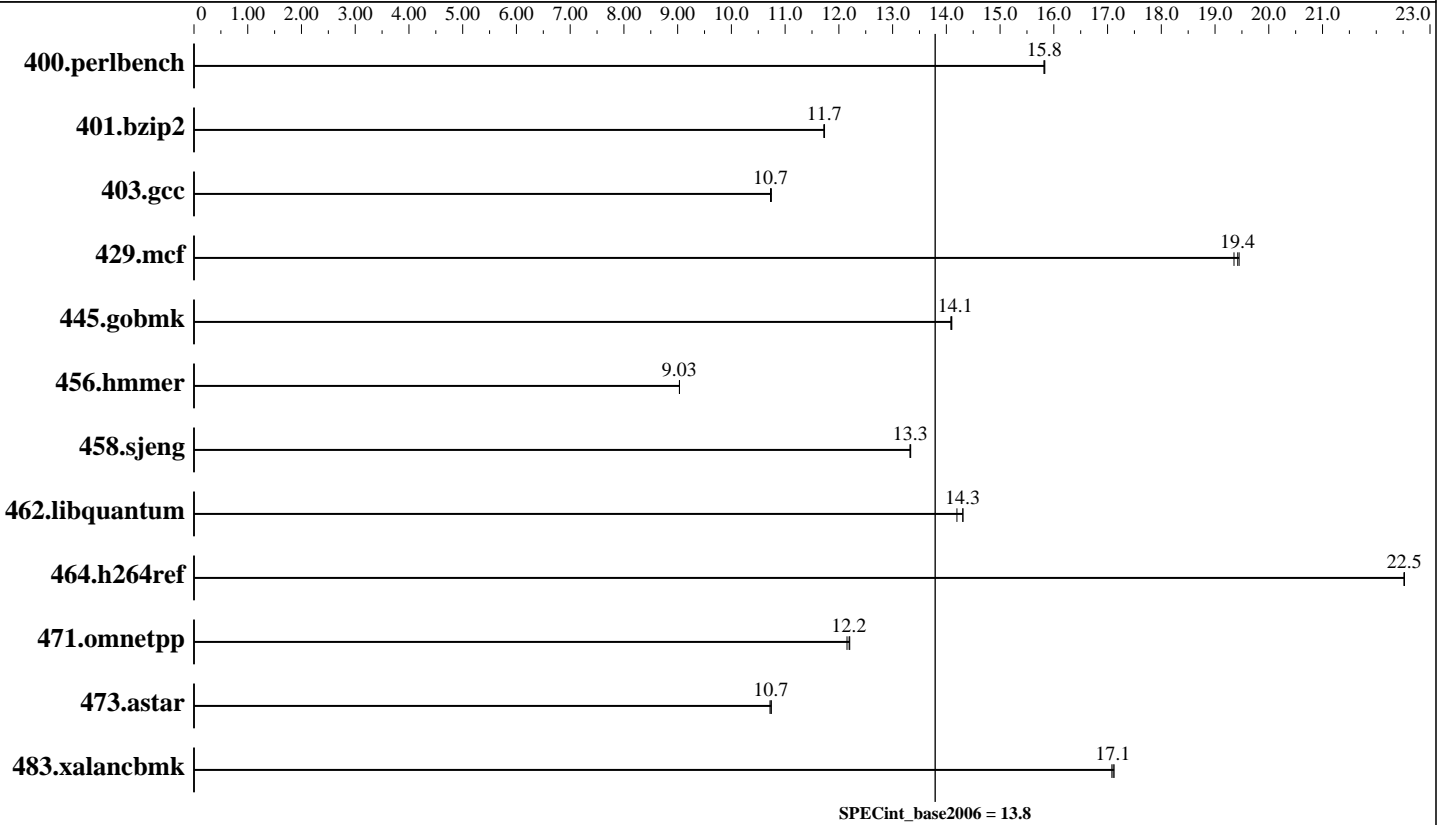
Test date: Jan-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon X3210
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB PC2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	618	15.8	618	15.8	617	15.8						
401.bzip2	823	11.7	823	11.7	822	11.7						
403.gcc	750	10.7	749	10.7	750	10.7						
429.mcf	471	19.4	469	19.4	470	19.4						
445.gobmk	745	14.1	744	14.1	744	14.1						
456.hmmr	1033	9.03	1033	9.03	1033	9.03						
458.sjeng	908	13.3	907	13.3	908	13.3						
462.libquantum	1459	14.2	1448	14.3	1448	14.3						
464.h264ref	982	22.5	983	22.5	982	22.5						
471.omnetpp	514	12.2	513	12.2	512	12.2						
473.astar	655	10.7	654	10.7	654	10.7						
483.xalancbmk	404	17.1	403	17.1	403	17.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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