



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = Not Run

IBM System x3400 (Intel Xeon E5335)

SPECint_base2006 = 12.5

CPU2006 license: 11

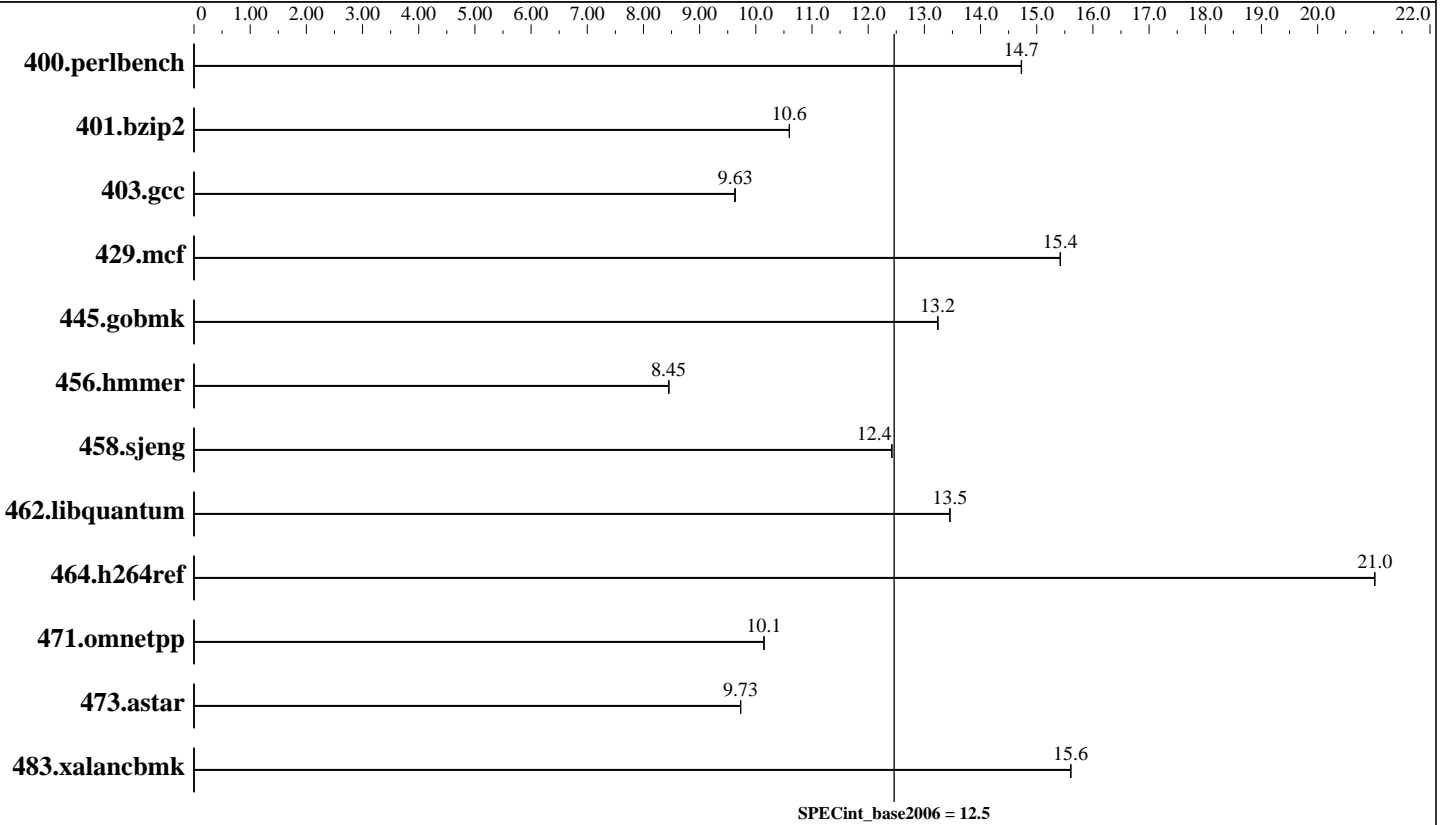
Test date: Jan-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	663	14.7	663	14.7	664	14.7						
401.bzip2	911	10.6	911	10.6	911	10.6						
403.gcc	836	9.63	836	9.63	836	9.62						
429.mcf	591	15.4	591	15.4	591	15.4						
445.gobmk	792	13.2	792	13.2	792	13.2						
456.hammer	1104	8.45	1104	8.45	1104	8.45						
458.sjeng	974	12.4	974	12.4	974	12.4						
462.libquantum	1540	13.5	1540	13.5	1540	13.5						
464.h264ref	1053	21.0	1053	21.0	1053	21.0						
471.omnetpp	616	10.1	616	10.1	616	10.1						
473.astar	721	9.73	722	9.73	721	9.73						
483.xalancbmk	442	15.6	442	15.6	442	15.6						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on an IBM System x3400. IBM System X 3400 and IBM System X 3500 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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