



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

SPECint®\_rate2006 = 45.8

ProLiant BL480c  
(2.66 GHz, Intel Xeon processor X5355)

SPECint\_rate\_base2006 = 44.4

CPU2006 license: 3

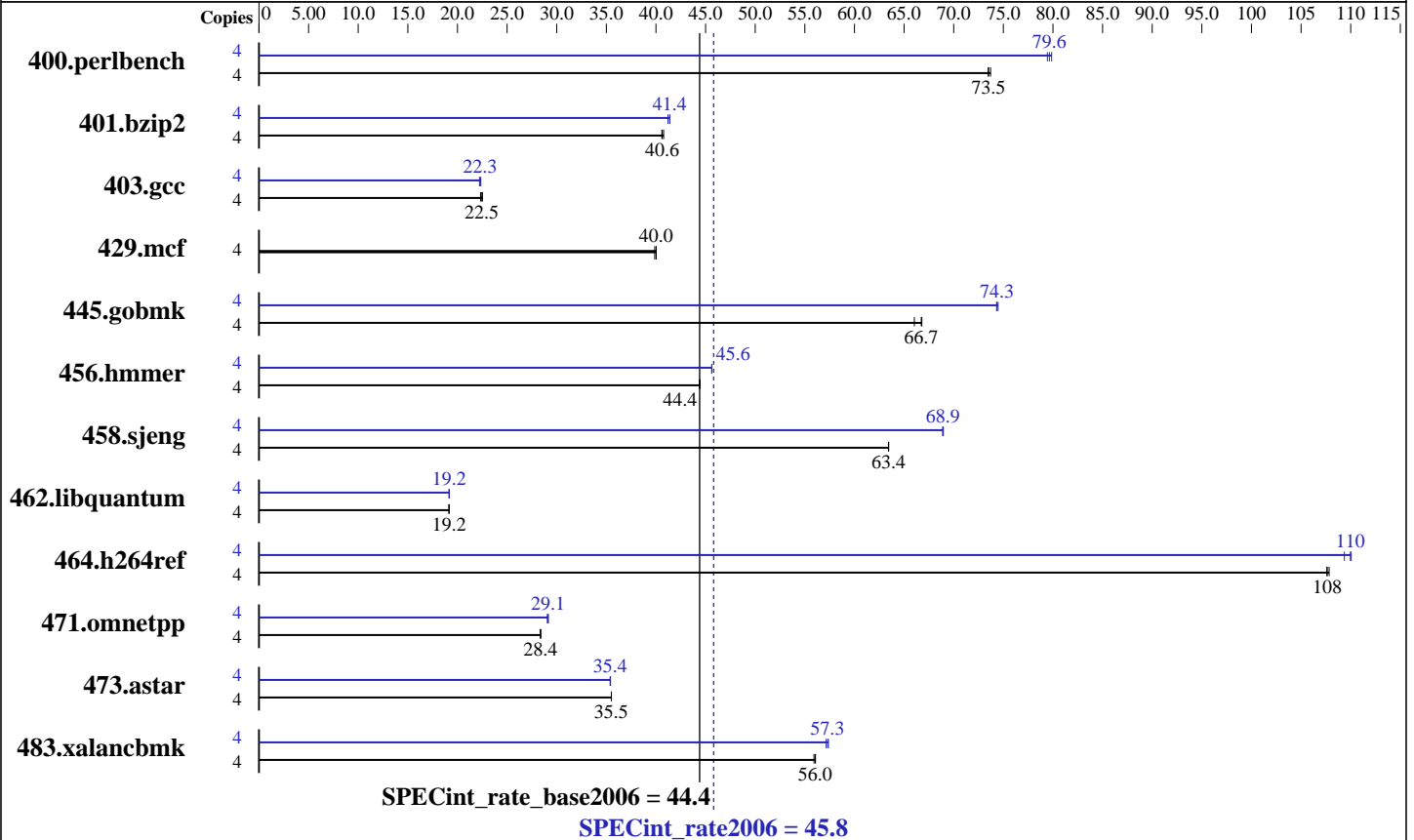
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus  
 CPU MHz: 2660  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise X64 Edition  
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z  
 Package ID: W\_CC\_P\_9.1.020  
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	530	73.7	<u>532</u>	<u>73.5</u>	532	73.5	4	489	79.9	<u>491</u>	<u>79.6</u>	492	79.4
401.bzip2	4	951	40.6	<u>950</u>	<u>40.6</u>	947	40.8	4	932	41.4	937	41.2	<u>933</u>	<u>41.4</u>
403.gcc	4	1442	22.3	1430	22.5	<u>1434</u>	<u>22.5</u>	4	1449	22.2	<u>1445</u>	<u>22.3</u>	1441	22.4
429.mcf	4	911	40.0	915	39.9	<u>912</u>	<u>40.0</u>	4	911	40.0	915	39.9	<u>912</u>	<u>40.0</u>
445.gobmk	4	<u>629</u>	<u>66.7</u>	635	66.0	628	66.8	4	<u>564</u>	<u>74.3</u>	563	74.5	565	74.3
456.hammer	4	840	44.4	<u>840</u>	<u>44.4</u>	840	44.4	4	818	45.6	<u>818</u>	<u>45.6</u>	818	45.6
458.sjeng	4	763	63.4	763	63.4	<u>763</u>	<u>63.4</u>	4	<u>702</u>	<u>68.9</u>	702	69.0	703	68.9
462.libquantum	4	4328	19.2	<u>4327</u>	<u>19.2</u>	4326	19.2	4	<u>4325</u>	<u>19.2</u>	4322	19.2	4327	19.2
464.h264ref	4	823	108	<u>822</u>	<u>108</u>	821	108	4	<u>805</u>	<u>110</u>	804	110	809	109
471.omnetpp	4	881	28.4	881	28.4	<u>881</u>	<u>28.4</u>	4	<u>858</u>	<u>29.1</u>	858	29.2	861	29.0
473.astar	4	<u>791</u>	<u>35.5</u>	791	35.5	791	35.5	4	793	35.4	<u>793</u>	<u>35.4</u>	793	35.4
483.xalancbmk	4	<u>493</u>	<u>56.0</u>	492	56.1	494	55.9	4	481	57.4	<u>482</u>	<u>57.3</u>	483	57.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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## Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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