# SPEC® CINT2006 Result
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## Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor L5320, 1.86 GHz

$SPECint^{®}\_rate2006 =$ 65.5

SPECint_rate_base2006 = 60.8

| | |
|---|---|
| **CPU2006 license:** 22 | **Test date:** Mar-2007 |
| **Test sponsor:** Fujitsu Siemens Computers | **Hardware Availability:** Mar-2007 |
| **Tested by:** Fujitsu Siemens Computers | **Software Availability:** Jan-2007 |



| Copies | |
|---|---|
| 400.perlbench | 8 — 112 / 8 — 102 |
| 401.bzip2 | 8 — 58.3 / 8 — 56.2 |
| 403.gcc | 8 — 31.8 / 8 — 28.0 |
| 429.mcf | 8 — 53.6 |
| 445.gobmk | 8 — 105 / 8 — 92.9 |
| 456.hmmer | 8 — 70.3 / 8 — 60.5 |
| 458.sjeng | 8 — 100 / 8 — 91.2 |
| 462.libquantum | 8 — 23.4 / 8 — 23.1 |
| 464.h264ref | 8 — 185 / 8 — 150 |
| 471.omnetpp | 8 — 46.6 / 8 — 44.4 |
| 473.astar | 8 — 51.1 |
| 483.xalancbmk | 8 — 73.7 / 8 — 73.2 |

**SPECint_rate_base2006 = 60.8**

**SPECint_rate2006 = 65.5**

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon L5320 |
| CPU Characteristics: | L5320 |
| CPU MHz: | 1867 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per chip, 4 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC) |
| Disk Subsystem: | SAS (73GB 15400 rpm) |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Windows Server 2003 x64 Enterprise Edition + SP1 (64-bit) |
| Compiler: | Intel C++ Compiler for IA32-based applications, Version 9.1 - built 20070109Z, Package-ID: W_CC_C_9.1.034 |
| | Intel C++ Compiler for EM64T-based applications, Version 9.1 - built 20070109, Package-ID: W_CC_C_9.1.034 |
| | Microsoft Visual Studio 2005 (libraries & linker) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Smart Heap Library, Version 8.1 |
| | Microsoft Platform SDK (libraries) |

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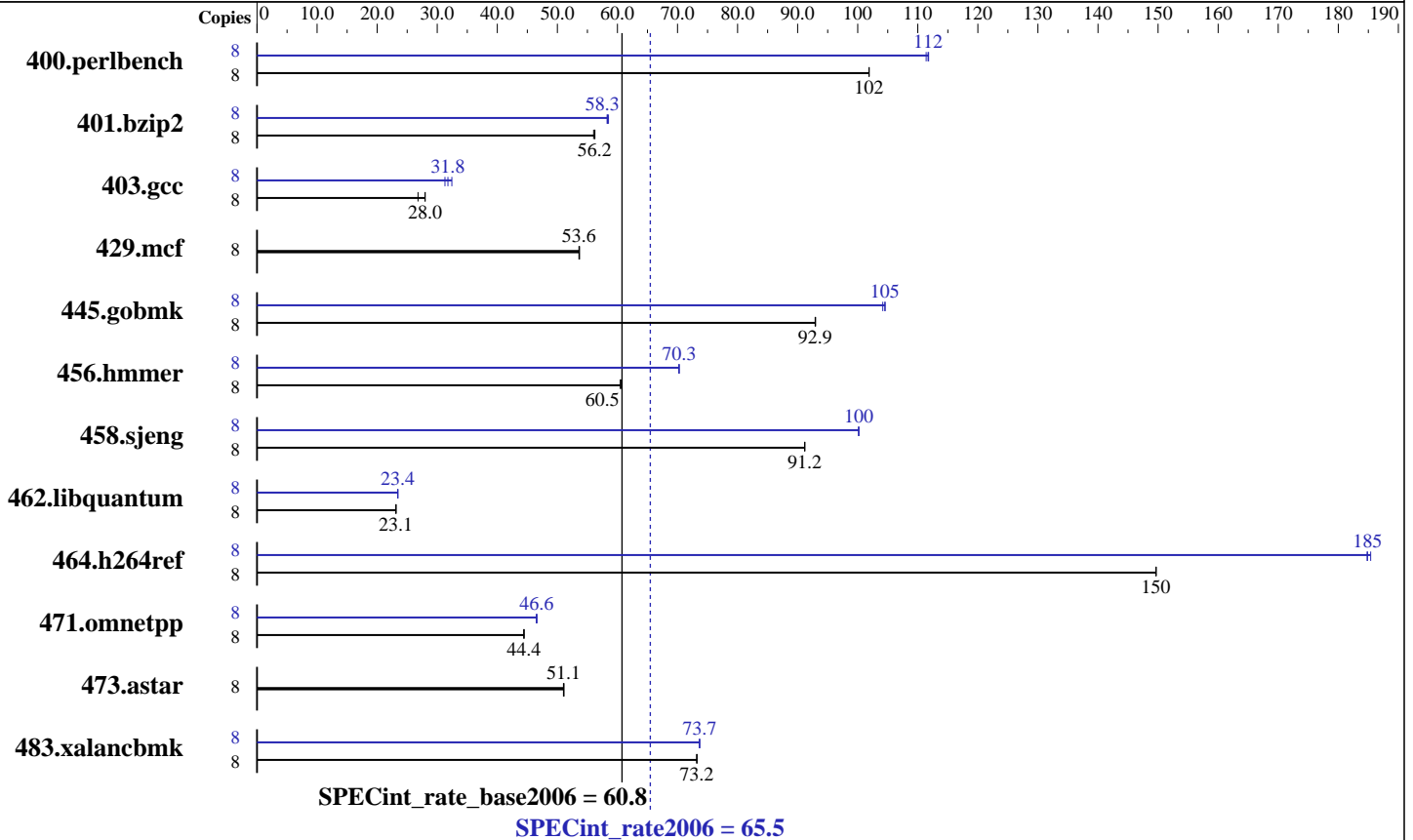
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## Results Table

| Benchmark | Copies | Base Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Peak Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 8 | 767 | 102 | 767 | 102 | **767** | **102** | 8 | 702 | 111 | **700** | **112** | 699 | 112 |
| 401.bzip2 | 8 | **1373** | **56.2** | 1378 | 56.0 | 1372 | 56.3 | 8 | 1325 | 58.3 | **1323** | **58.3** | 1319 | 58.5 |
| 403.gcc | 8 | **2302** | **28.0** | 2403 | 26.8 | 2301 | 28.0 | 8 | 1986 | 32.4 | **2026** | **31.8** | 2058 | 31.3 |
| 429.mcf | 8 | 1359 | 53.7 | 1361 | 53.6 | **1361** | **53.6** | 8 | 1359 | 53.7 | 1361 | 53.6 | **1361** | **53.6** |
| 445.gobmk | 8 | 902 | 93.0 | 903 | 92.9 | **903** | **92.9** | 8 | **803** | **105** | 806 | 104 | 802 | 105 |
| 456.hmmer | 8 | 1234 | 60.5 | 1233 | 60.5 | **1233** | **60.5** | 8 | 1062 | 70.3 | 1063 | 70.2 | **1062** | **70.3** |
| 458.sjeng | 8 | 1062 | 91.2 | 1062 | 91.2 | **1062** | **91.2** | 8 | **966** | **100** | 966 | 100 | 967 | 100 |
| 462.libquantum | 8 | 7172 | 23.1 | **7168** | **23.1** | 7163 | 23.1 | 8 | 7072 | 23.4 | 7071 | 23.4 | **7072** | **23.4** |
| 464.h264ref | 8 | 1183 | 150 | 1183 | 150 | **1183** | **150** | 8 | 955 | 185 | **958** | **185** | 958 | 185 |
| 471.omnetpp | 8 | 1124 | 44.5 | **1125** | **44.4** | 1126 | 44.4 | 8 | 1075 | 46.5 | 1073 | 46.6 | **1074** | **46.6** |
| 473.astar | 8 | **1100** | **51.1** | 1101 | 51.0 | 1099 | 51.1 | 8 | **1100** | **51.1** | 1101 | 51.0 | 1099 | 51.1 |
| 483.xalancbmk | 8 | **754** | **73.2** | 754 | 73.2 | 754 | 73.2 | 8 | 749 | 73.7 | 750 | 73.6 | **749** | **73.7** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer, 462.libquantum and 464.h264ref in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

## Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

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## Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

## Base Optimization Flags

C benchmarks:

```
-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icl -Qvc8 -Qc99
```

```
    401.bzip2: icl -Qvc8 -Qc99
            -link  -LIBPATH:"C:\\Program Files (x86)\\Intel\\Compiler\\C++\\9.1\\EM64T\\Li
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64"
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64\\atlmfc

    456.hmmer: icl -Qvc8 -Qc99
            -link  -LIBPATH:"C:\\Program Files (x86)\\Intel\\Compiler\\C++\\9.1\\EM64T\\Li
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64"
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64\\atlmfc

462.libquantum: icl -Qvc8 -Qc99
            -link  -LIBPATH:"C:\\Program Files (x86)\\Intel\\Compiler\\C++\\9.1\\EM64T\\Li
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64"
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64\\atlmfc

  464.h264ref: icl -Qvc8 -Qc99
            -link  -LIBPATH:"C:\\Program Files (x86)\\Intel\\Compiler\\C++\\9.1\\EM64T\\Li
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64"
            -link  -LIBPATH:"C:\\Program Files\\Microsoft Platform SDK\\Lib\\AMD64\\atlmfc
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# Peak Compiler Invocation (Continued)

C++ benchmarks:
```
icl -Qvc8
```

# Peak Portability Flags

```
    401.bzip2: -DSPEC_CPU_P64
      403.gcc: -DSPEC_CPU_WIN32
    456.hmmer: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
    464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES -DSPEC_CPU_P64
  483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

# Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
                 shlW32M.lib -link -FORCE:MULTIPLE

      401.bzip2: -fast -F512000000 shlSMP64Mt.lib bufferoverflowu.lib
                 -link -FORCE:MULTIPLE

        403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
                 -link -FORCE:MULTIPLE

        429.mcf: basepeak = yes

      445.gobmk: Same as 400.perlbench

      456.hmmer: Same as 401.bzip2

      458.sjeng: Same as 400.perlbench

  462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
                 bufferoverflowu.lib -link -FORCE:MULTIPLE

    464.h264ref: Same as 462.libquantum
```

C++ benchmarks:

```
    471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
                 -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

      473.astar: basepeak = yes
```

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## Peak Optimization Flags (Continued)

483.xalancbmk: Same as 471.omnetpp

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.24.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.24.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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