



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 28.5

PRIMERGY TX150 S5, Intel Xeon processor 3060,
2.40 GHz

SPECint_rate_base2006 = 27.2

CPU2006 license: 22

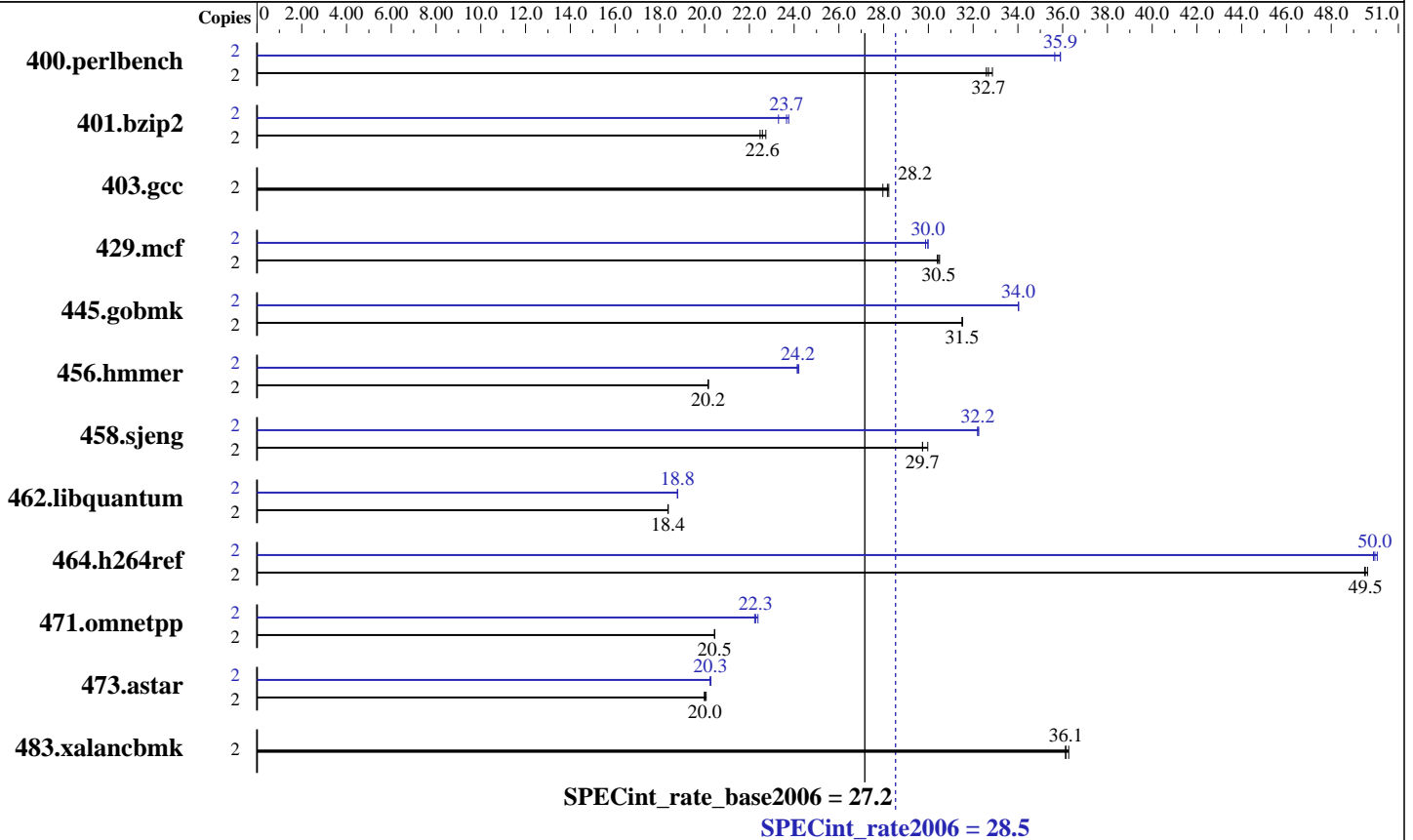
Test date: May-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Sep-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon 3060
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
 Disk Subsystem: SATA (160 GB 7200 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	595	32.9	600	32.6	598	32.7	2	544	35.9	544	35.9	548	35.6
401.bzip2	2	859	22.5	855	22.6	849	22.7	2	828	23.3	816	23.7	812	23.8
403.gcc	2	576	28.0	572	28.2	570	28.2	2	576	28.0	572	28.2	570	28.2
429.mcf	2	600	30.4	599	30.5	598	30.5	2	611	29.9	608	30.0	608	30.0
445.gobmk	2	666	31.5	666	31.5	666	31.5	2	617	34.0	617	34.0	617	34.0
456.hmmer	2	926	20.2	926	20.2	925	20.2	2	773	24.1	772	24.2	771	24.2
458.sjeng	2	814	29.7	814	29.7	807	30.0	2	750	32.3	751	32.2	752	32.2
462.libquantum	2	2257	18.4	2255	18.4	2256	18.4	2	2205	18.8	2205	18.8	2205	18.8
464.h264ref	2	894	49.5	894	49.5	892	49.6	2	886	50.0	887	49.9	884	50.1
471.omnetpp	2	611	20.5	611	20.5	612	20.4	2	562	22.2	558	22.4	561	22.3
473.astar	2	701	20.0	699	20.1	702	20.0	2	693	20.3	693	20.2	692	20.3
483.xalancbmk	2	380	36.3	382	36.1	382	36.1	2	380	36.3	382	36.1	382	36.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

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