



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## HITACHI

**SPECint®2006 = 17.3**

BladeSymphony BS1000 (Intel Xeon 5160)

**SPECint\_base2006 = 16.6**

CPU2006 license: 872

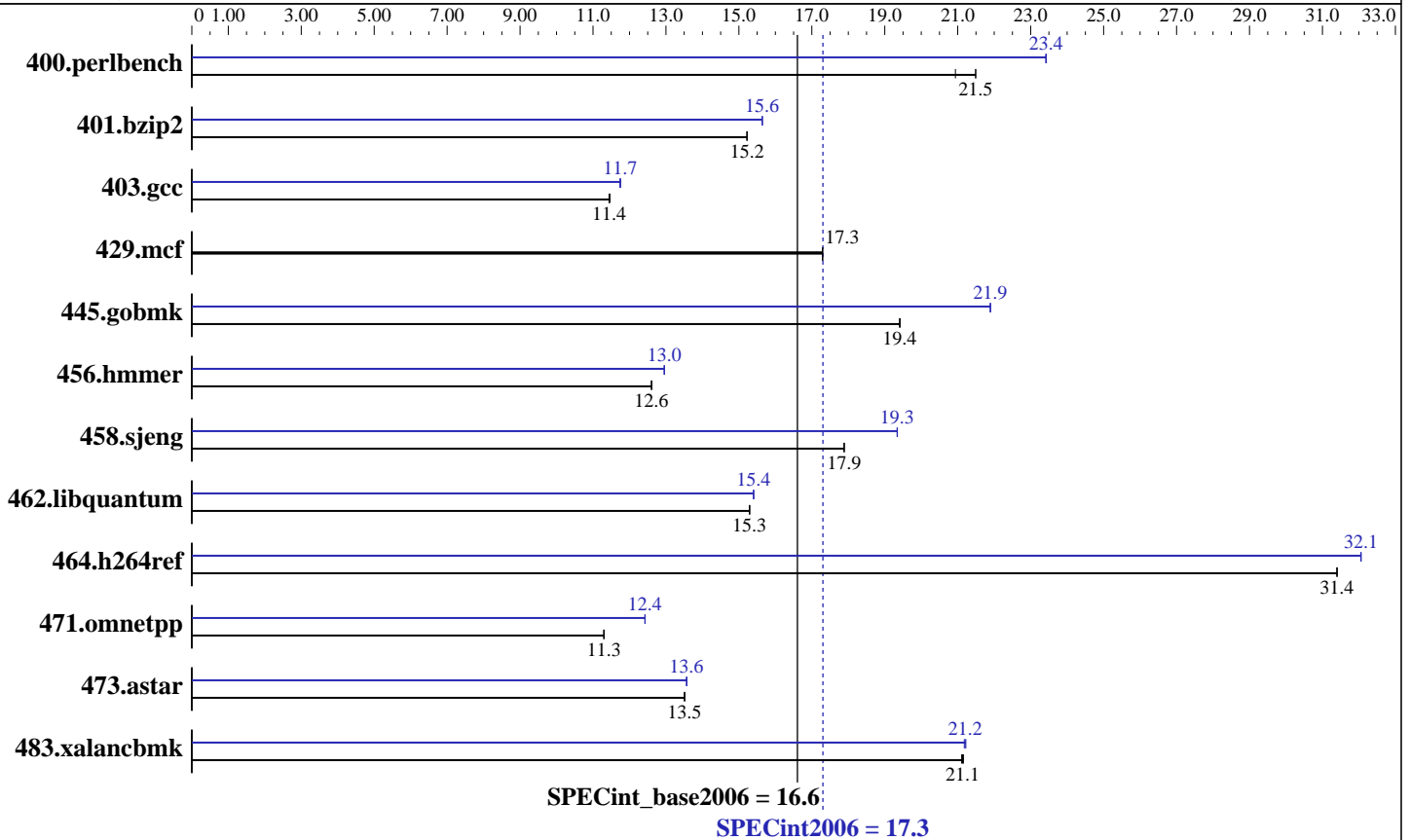
Test sponsor: HITACHI

Tested by: HITACHI

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Dec-2006



### Hardware

CPU Name: Intel Xeon 5160  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB(4 x 2 GB PC2-4200F)  
 Disk Subsystem: 1 x 73GB 10000rpm SAS  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 R2, Enterprise x64 Edition  
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build 20061103Z  
 Microsoft Visual Studio .NET 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## HITACHI

SPECint2006 = 17.3

BladeSymphony BS1000 (Intel Xeon 5160)

SPECint\_base2006 = 16.6

CPU2006 license: 872

Test sponsor: HITACHI

Tested by: HITACHI

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Dec-2006

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	467	20.9	455	21.5	<u>455</u>	<u>21.5</u>	417	23.4	417	23.4	<u>417</u>	<u>23.4</u>
401.bzip2	634	15.2	634	15.2	<u>634</u>	<u>15.2</u>	617	15.6	<u>617</u>	<u>15.6</u>	617	15.6
403.gcc	<b>704</b>	<b>11.4</b>	704	11.4	702	11.5	685	11.8	<b>685</b>	<b>11.7</b>	686	11.7
429.mcf	527	17.3	527	17.3	<u>527</u>	<u>17.3</u>	527	17.3	527	17.3	<u>527</u>	<u>17.3</u>
445.gobmk	541	19.4	540	19.4	<u>540</u>	<u>19.4</u>	<u>479</u>	<u>21.9</u>	479	21.9	479	21.9
456.hammer	740	12.6	<u>740</u>	<u>12.6</u>	740	12.6	720	13.0	720	13.0	<u>720</u>	<u>13.0</u>
458.sjeng	676	17.9	<u>677</u>	<u>17.9</u>	677	17.9	<u>626</u>	<u>19.3</u>	626	19.3	626	19.3
462.libquantum	1355	15.3	1355	15.3	<u>1355</u>	<u>15.3</u>	1345	15.4	1345	15.4	<u>1345</u>	<u>15.4</u>
464.h264ref	<u>705</u>	<u>31.4</u>	705	31.4	705	31.4	<u>690</u>	<u>32.1</u>	690	32.1	690	32.1
471.omnetpp	553	11.3	<u>553</u>	<u>11.3</u>	553	11.3	503	12.4	503	12.4	<u>503</u>	<u>12.4</u>
473.astar	<u>519</u>	<u>13.5</u>	520	13.5	519	13.5	517	13.6	<u>517</u>	<u>13.6</u>	517	13.6
483.xalancbmk	326	21.2	327	21.1	<u>327</u>	<u>21.1</u>	<u>325</u>	<u>21.2</u>	326	21.2	325	21.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**HITACHI**

**SPECint2006 = 17.3**

**BladeSymphony BS1000 (Intel Xeon 5160)**

**SPECint\_base2006 = 16.6**

**CPU2006 license:** 872

**Test sponsor:** HITACHI

**Tested by:** HITACHI

**Test date:** May-2007

**Hardware Availability:** Sep-2006

**Software Availability:** Dec-2006

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**HITACHI**

**SPECint2006 = 17.3**

**BladeSymphony BS1000 (Intel Xeon 5160)**

**SPECint\_base2006 = 16.6**

**CPU2006 license:** 872

**Test sponsor:** HITACHI

**Tested by:** HITACHI

**Test date:** May-2007

**Hardware Availability:** Sep-2006

**Software Availability:** Dec-2006

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/ic91.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/ic91.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 11:11:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 12 June 2007.