SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY RX600 S3, Intel Xeon processor 7120M, 3.0 GHz

SPECint®_rate2006 = 35.9

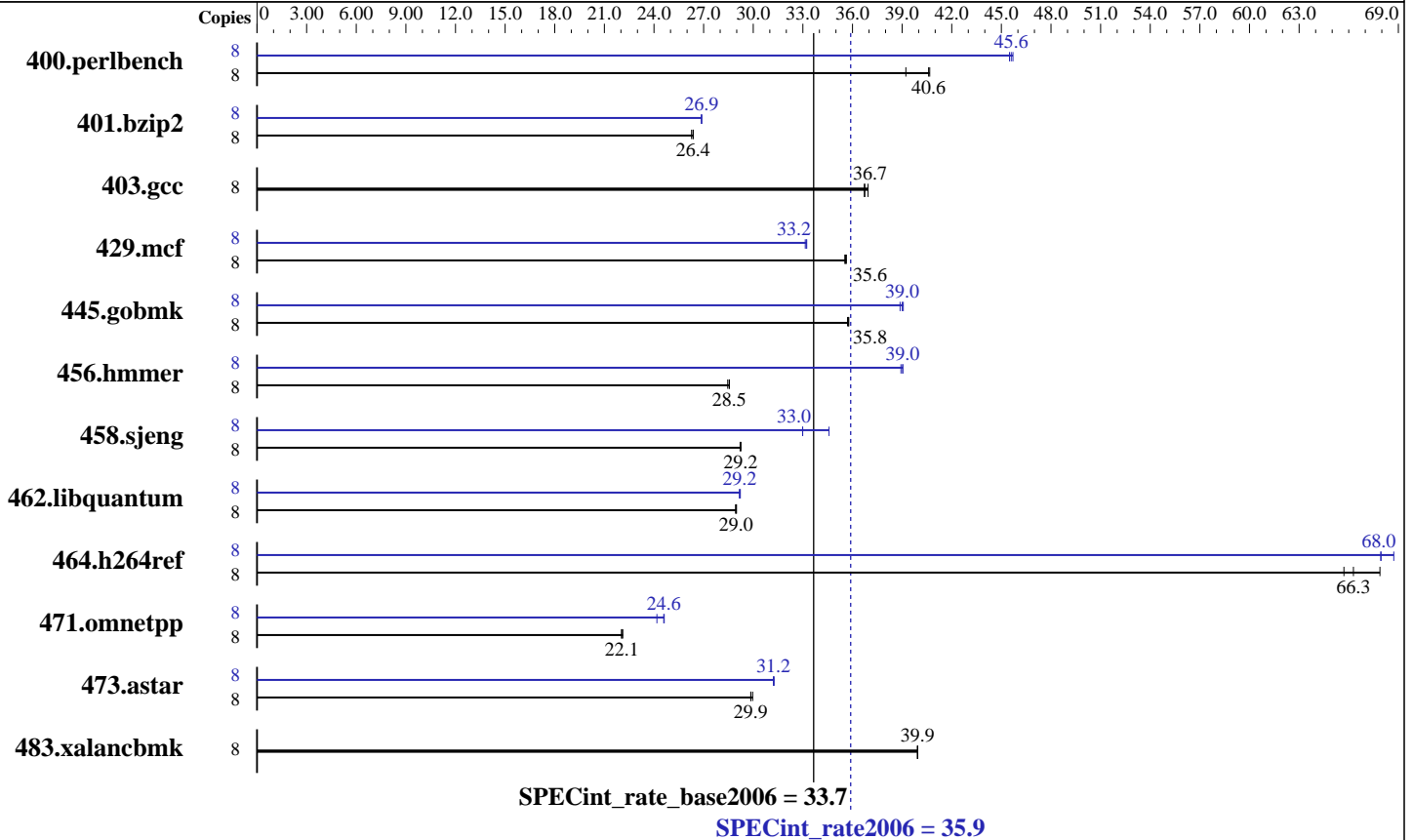SPECint_rate_base2006 = 33.7

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jun-2007

Hardware Availability: Aug-2006

Software Availability: Mar-2007

Benchmark	Copies	Value
400.perlbench	8	45.6
	8	40.6
401.bzip2	8	26.9
	8	26.4
403.gcc	8	36.7
429.mcf	8	33.2
	8	35.6
445.gobmk	8	39.0
	8	35.8
456.hmmer	8	39.0
	8	28.5
458.sjeng	8	33.0
	8	29.2
462.libquantum	8	29.2
	8	29.0
464.h264ref	8	68.0
	8	66.3
471.omnetpp	8	24.6
	8	22.1
473.astar	8	31.2
	8	29.9
483.xalancbmk	8	39.9

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Hardware

CPU Name:	Intel Xeon 7120M
CPU Characteristics:	800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)
Disk Subsystem:	Seagate ST973401SS (SAS 73GB 10 krpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**1925**	**40.6**	1992	39.2	1922	40.7	8	**1714**	**45.6**	1718	45.5	1710	45.7
401.bzip2	8	2928	26.4	2939	26.3	**2928**	**26.4**	8	2874	26.9	2872	26.9	**2873**	**26.9**
403.gcc	8	1743	36.9	1754	36.7	**1753**	**36.7**	8	1743	36.9	1754	36.7	**1753**	**36.7**
429.mcf	8	**2051**	**35.6**	2053	35.5	2048	35.6	8	2201	33.1	**2197**	**33.2**	2195	33.2
445.gobmk	8	**2346**	**35.8**	2351	35.7	2346	35.8	8	2159	38.9	2148	39.1	**2151**	**39.0**
456.hmmer	8	2623	28.5	2614	28.6	**2615**	**28.5**	8	1917	38.9	1912	39.0	**1912**	**39.0**
458.sjeng	8	**3311**	**29.2**	3314	29.2	3309	29.3	8	**2934**	**33.0**	2799	34.6	2935	33.0
462.libquantum	8	**5723**	**29.0**	5730	28.9	5718	29.0	8	**5680**	**29.2**	5684	29.2	5673	29.2
464.h264ref	8	2694	65.7	**2671**	**66.3**	2608	67.9	8	2576	68.7	**2605**	**68.0**	2606	67.9
471.omnetpp	8	2261	22.1	2269	22.0	**2267**	**22.1**	8	**2033**	**24.6**	2031	24.6	2068	24.2
473.astar	8	1874	30.0	1881	29.9	**1881**	**29.9**	8	1797	31.3	**1798**	**31.2**	1799	31.2
483.xalancbmk	8	1382	39.9	**1382**	**39.9**	1382	39.9	8	1382	39.9	**1382**	**39.9**	1382	39.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

The PRIMERGY RX600 S3 and the PRIMERGY TX600 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
        icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_X64
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -fast

C++ benchmarks:
        -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc

        401.bzip2: /opt/intel/cce/9.1.047/bin/icc
                -I/opt/intel/cce/9.1.047/include
                -L/opt/intel/cce/9.1.047/lib

        456.hmmer: /opt/intel/cce/9.1.047/bin/icc
                -I/opt/intel/cce/9.1.047/include
                -L/opt/intel/cce/9.1.047/lib

        462.libquantum: /opt/intel/cce/9.1.047/bin/icc
                -I/opt/intel/cce/9.1.047/include
                -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
        icpc

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

       401.bzip2: -fast

         403.gcc: basepeak = yes

         429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

       445.gobmk: Same as 429.mcf

       456.hmmer: Same as 400.perlbench

       458.sjeng: Same as 429.mcf

  462.libquantum: Same as 400.perlbench

      464.h264ref: Same as 429.mcf
```

C++ benchmarks:

```
     471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
                  -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

        473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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