



# SPEC® CINT2006 Result

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## IBM Corporation

**SPECint®2006 = 16.9**

IBM BladeCenter HS21 XM (Intel Xeon E5345)

**SPECint\_base2006 = 15.3**

CPU2006 license: 11

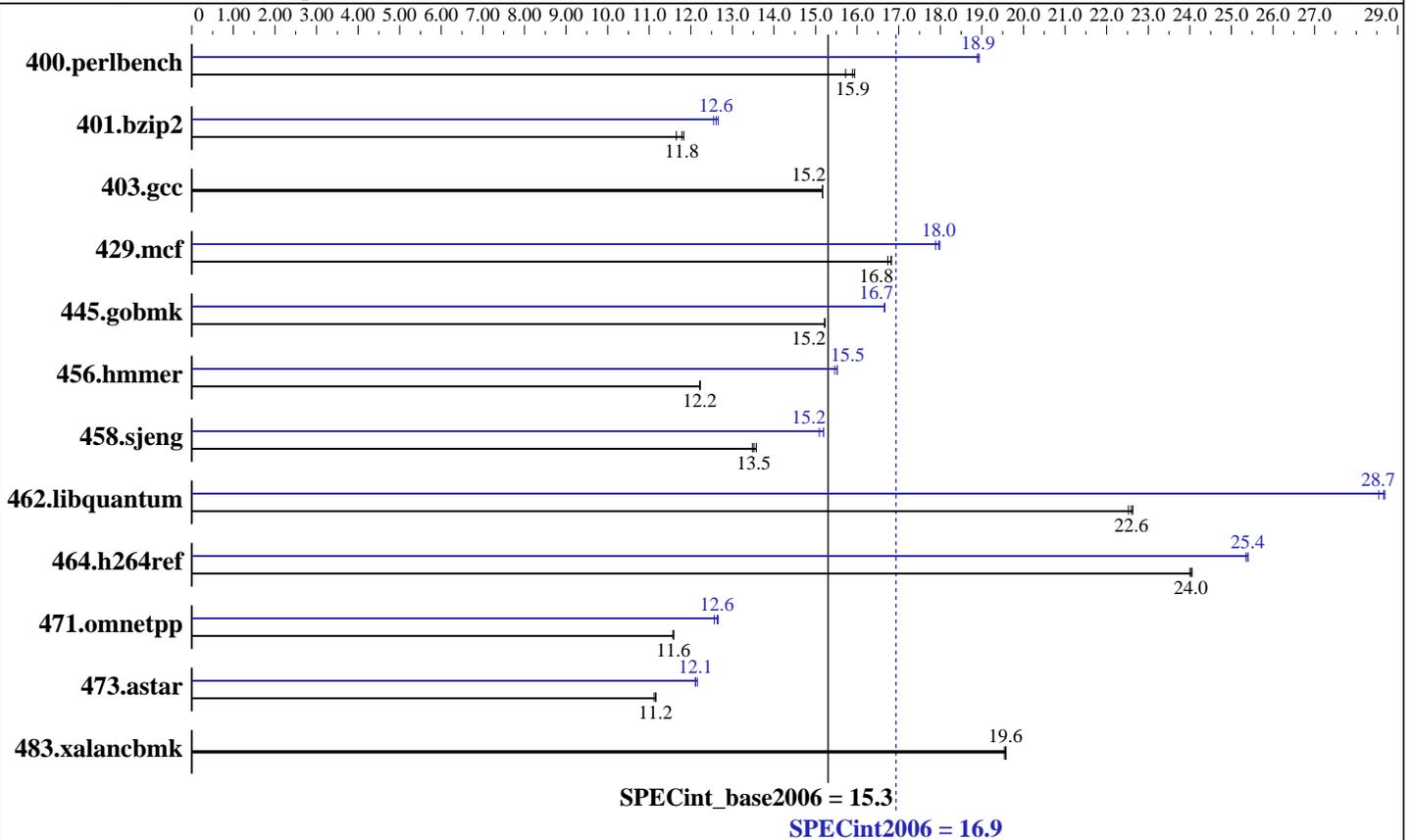
Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Jul-2007



### Hardware

CPU Name: Intel Xeon E5345  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SLES 10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux version 10.0  
 Build 20070426 Package ID: 1\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>615</u></b>	<b><u>15.9</u></b>	613	15.9	621	15.7	<b><u>517</u></b>	<b><u>18.9</u></b>	516	18.9	<b><u>517</u></b>	<b><u>18.9</u></b>
401.bzip2	828	11.7	816	11.8	<b><u>819</u></b>	<b><u>11.8</u></b>	<b><u>765</u></b>	<b><u>12.6</u></b>	762	12.7	769	12.5
403.gcc	<b><u>531</u></b>	<b><u>15.2</u></b>	531	15.2	531	15.2	<b><u>531</u></b>	<b><u>15.2</u></b>	531	15.2	531	15.2
429.mcf	<b><u>542</u></b>	<b><u>16.8</u></b>	545	16.7	542	16.8	<b><u>508</u></b>	<b><u>18.0</u></b>	507	18.0	510	17.9
445.gobmk	<b><u>689</u></b>	<b><u>15.2</u></b>	689	15.2	690	15.2	630	16.7	<b><u>630</u></b>	<b><u>16.7</u></b>	629	16.7
456.hmmmer	<b><u>764</u></b>	<b><u>12.2</u></b>	764	12.2	763	12.2	601	15.5	603	15.5	<b><u>601</u></b>	<b><u>15.5</u></b>
458.sjeng	<b><u>895</u></b>	<b><u>13.5</u></b>	892	13.6	897	13.5	<b><u>796</u></b>	<b><u>15.2</u></b>	802	15.1	796	15.2
462.libquantum	916	22.6	920	22.5	<b><u>917</u></b>	<b><u>22.6</u></b>	722	28.7	<b><u>723</u></b>	<b><u>28.7</u></b>	726	28.5
464.h264ref	920	24.1	922	24.0	<b><u>921</u></b>	<b><u>24.0</u></b>	871	25.4	873	25.3	<b><u>872</u></b>	<b><u>25.4</u></b>
471.omnetpp	540	11.6	539	11.6	<b><u>539</u></b>	<b><u>11.6</u></b>	497	12.6	<b><u>495</u></b>	<b><u>12.6</u></b>	494	12.7
473.astar	<b><u>629</u></b>	<b><u>11.2</u></b>	629	11.2	631	11.1	577	12.2	580	12.1	<b><u>579</u></b>	<b><u>12.1</u></b>
483.xalancbmk	<b><u>353</u></b>	<b><u>19.6</u></b>	353	19.5	352	19.6	<b><u>353</u></b>	<b><u>19.6</u></b>	353	19.5	352	19.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs

-L/spec/cpu2006.1.0/lib -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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