



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 33.8

PRIMERGY TX150 S6, Intel Xeon processor 3065, 2.33 GHz

### SPECint\_rate\_base2006 = 29.3

CPU2006 license: 22

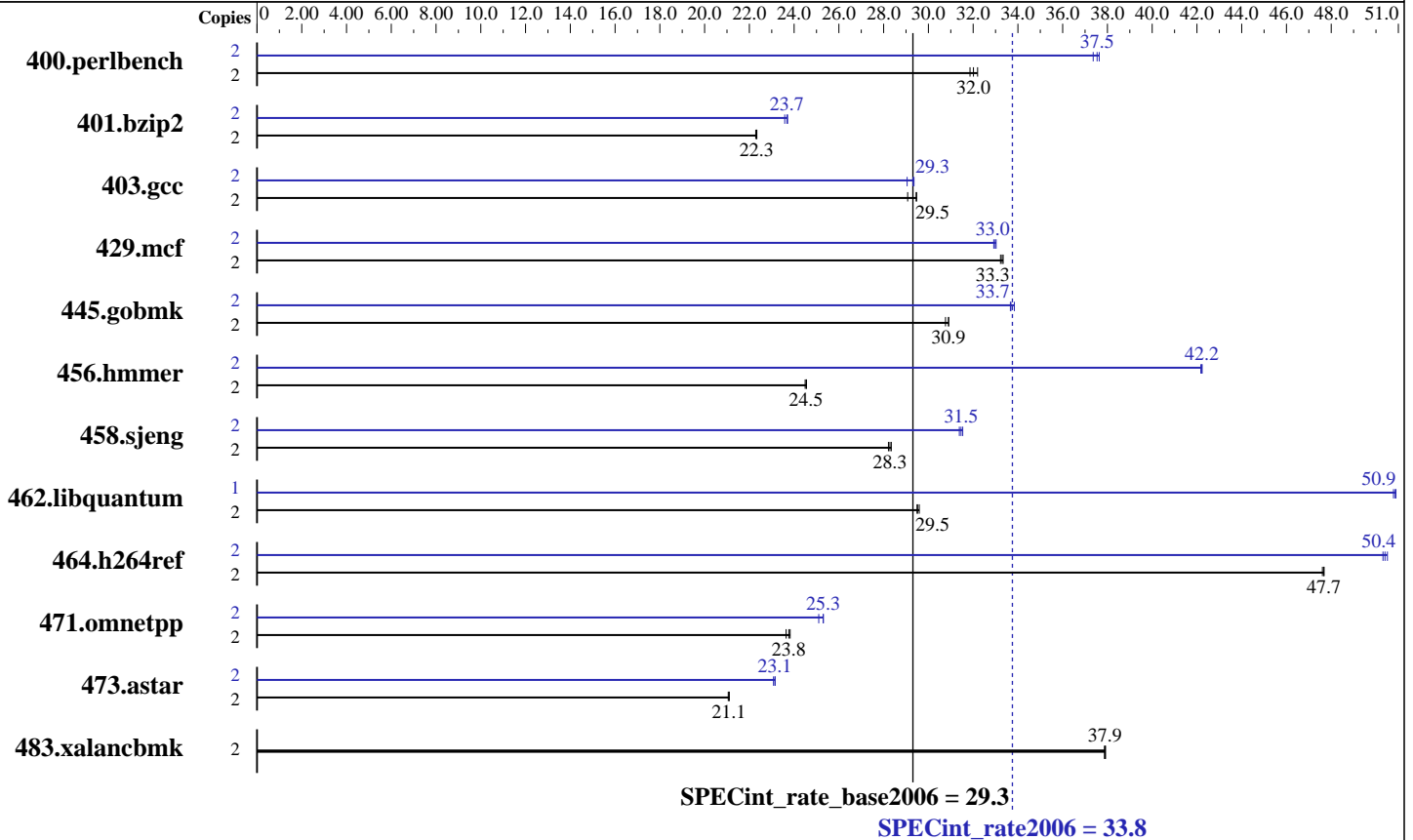
Test date: Sep-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon 3065  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1 GB DDR2 PC2-6400E, 2 rank, CAS 6-6-6, with ECC)  
 Disk Subsystem: Fujitsu MAY2036RC (SAS, 36GB, 10000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE LINUX Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	613	31.9	<b>610</b>	<b>32.0</b>	607	32.2	2	523	37.4	519	37.6	<b>521</b>	<b>37.5</b>
401.bzip2	2	864	22.3	<b>864</b>	<b>22.3</b>	866	22.3	2	814	23.7	<b>815</b>	<b>23.7</b>	818	23.6
403.gcc	2	<b>547</b>	<b>29.5</b>	546	29.5	554	29.1	2	<b>549</b>	<b>29.3</b>	554	29.0	548	29.4
429.mcf	2	<b>548</b>	<b>33.3</b>	547	33.3	549	33.2	2	554	32.9	552	33.0	<b>553</b>	<b>33.0</b>
445.gobmk	2	682	30.8	<b>679</b>	<b>30.9</b>	679	30.9	2	620	33.8	<b>623</b>	<b>33.7</b>	623	33.7
456.hammer	2	<b>760</b>	<b>24.5</b>	760	24.6	762	24.5	2	442	42.2	442	42.2	<b>442</b>	<b>42.2</b>
458.sjeng	2	854	28.3	858	28.2	<b>856</b>	<b>28.3</b>	2	767	31.5	<b>769</b>	<b>31.5</b>	771	31.4
462.libquantum	2	1405	29.5	1400	29.6	<b>1404</b>	<b>29.5</b>	1	407	50.9	<b>407</b>	<b>50.9</b>	408	50.8
464.h264ref	2	<b>928</b>	<b>47.7</b>	930	47.6	928	47.7	2	880	50.3	<b>878</b>	<b>50.4</b>	876	50.5
471.omnetpp	2	525	23.8	529	23.6	<b>526</b>	<b>23.8</b>	2	498	25.1	494	25.3	<b>494</b>	<b>25.3</b>
473.astar	2	<b>666</b>	<b>21.1</b>	665	21.1	667	21.1	2	606	23.1	608	23.1	<b>607</b>	<b>23.1</b>
483.xalanbmk	2	364	37.9	364	37.9	<b>364</b>	<b>37.9</b>	2	364	37.9	364	37.9	<b>364</b>	<b>37.9</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

`456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:

`icpc`

## Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:00:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 October 2007.