



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

SPECint[®]_rate2006 = 55.8

ProLiant DL580 G4
(2.6 GHz, Intel Xeon processor 7110M)

SPECint_rate_base2006 = 52.0

CPU2006 license: 3

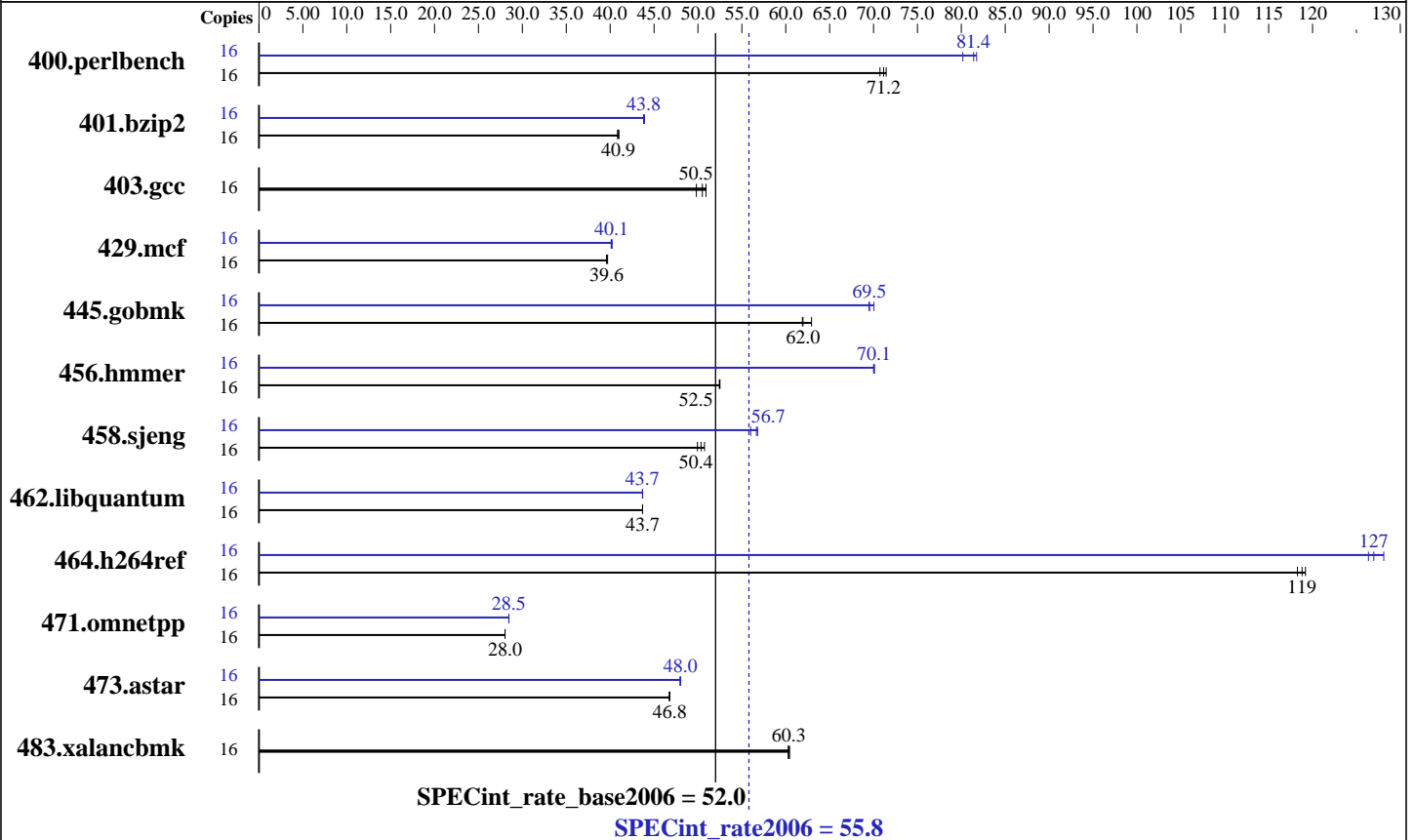
Test date: Sep-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Aug-2006

Tested by: Hewlett-Packard Company

Software Availability: May-2007



Hardware

CPU Name: Intel Xeon 7110M
 CPU Characteristics: 2.6 GHz, 800 MHz system bus
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB PC2-3200R)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T
 applications, Version 10.0
 Build 20070426 Package ID: 1_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 libraries



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>2197</u>	<u>71.2</u>	2188	71.4	2210	70.7	16	1913	81.7	<u>1920</u>	<u>81.4</u>	1950	80.2
401.bzip2	16	3766	41.0	3781	40.8	<u>3772</u>	<u>40.9</u>	16	3525	43.8	<u>3525</u>	<u>43.8</u>	3516	43.9
403.gcc	16	2530	50.9	<u>2551</u>	<u>50.5</u>	2585	49.8	16	2530	50.9	<u>2551</u>	<u>50.5</u>	2585	49.8
429.mcf	16	3675	39.7	<u>3683</u>	<u>39.6</u>	3687	39.6	16	3627	40.2	3638	40.1	<u>3635</u>	<u>40.1</u>
445.gobmk	16	2712	61.9	<u>2709</u>	<u>62.0</u>	2667	62.9	16	2396	70.0	2416	69.5	<u>2413</u>	<u>69.5</u>
456.hammer	16	2843	52.5	2847	52.4	<u>2846</u>	<u>52.5</u>	16	<u>2131</u>	<u>70.1</u>	2133	70.0	2129	70.1
458.sjeng	16	3877	49.9	<u>3845</u>	<u>50.4</u>	3815	50.7	16	3457	56.0	3409	56.8	<u>3417</u>	<u>56.7</u>
462.libquantum	16	7588	43.7	7587	43.7	<u>7587</u>	<u>43.7</u>	16	7585	43.7	<u>7588</u>	<u>43.7</u>	7590	43.7
464.h264ref	16	<u>2980</u>	<u>119</u>	2994	118	2971	119	16	<u>2788</u>	<u>127</u>	2802	126	2764	128
471.omnetpp	16	<u>3568</u>	<u>28.0</u>	3569	28.0	3568	28.0	16	<u>3513</u>	<u>28.5</u>	3512	28.5	3514	28.5
473.astar	16	2404	46.7	<u>2401</u>	<u>46.8</u>	2400	46.8	16	2338	48.0	<u>2340</u>	<u>48.0</u>	2342	48.0
483.xalancbmk	16	1832	60.3	<u>1829</u>	<u>60.3</u>	1827	60.4	16	1832	60.3	<u>1829</u>	<u>60.3</u>	1827	60.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Regulator Disabled
Adjacent Sector Prefetch Enabled
Hardware Prefetcher Enabled
Hyper-Threading Technology Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:

-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3
-no-prec-div

403.gcc: basepeak = yes

429.mcf: -xP -ipo -O3 -static -no-prec-div -prefetch
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xP -O2 -ipo
-no-prec_div -ansi-alias

456.hmmcr: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xP -ipo -O3 -static
-no-prec-div -unroll4

462.libquantum: Same as 458.sjeng

464.h264ref: Same as 456.hmmcr

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xP -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.xml>



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