



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 59.1

### Acer Altos G330 Mk2 (Intel Xeon X3220)

### SPECint\_rate\_base2006 = 51.6

CPU2006 license: 97

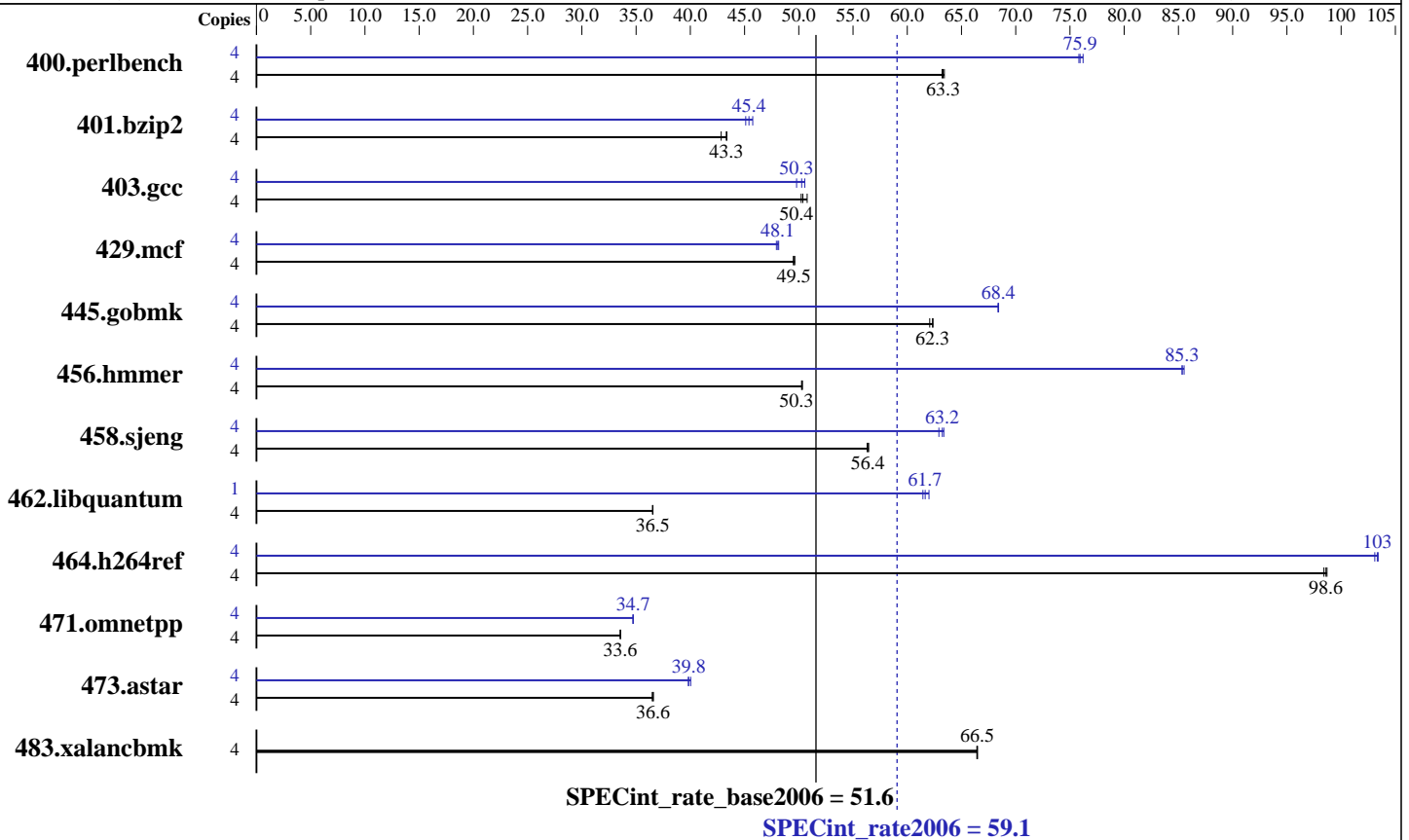
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



#### Hardware

CPU Name: Intel Xeon X3220  
 CPU Characteristics: 1066 MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)  
 Disk Subsystem: 1 x 160 GB 7200RPM SATA HDD  
 Other Hardware: None

#### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	618	63.2	616	63.4	<b>617</b>	<b>63.3</b>	4	515	75.8	513	76.2	<b>515</b>	<b>75.9</b>
401.bzip2	4	901	42.8	<b>891</b>	<b>43.3</b>	890	43.4	4	856	45.1	<b>850</b>	<b>45.4</b>	844	45.8
403.gcc	4	<b>639</b>	<b>50.4</b>	641	50.2	634	50.8	4	647	49.8	<b>641</b>	<b>50.3</b>	637	50.6
429.mcf	4	<b>736</b>	<b>49.5</b>	737	49.5	735	49.6	4	758	48.1	<b>759</b>	<b>48.1</b>	761	47.9
445.gobmk	4	673	62.4	<b>673</b>	<b>62.3</b>	676	62.1	4	613	68.4	614	68.4	<b>614</b>	<b>68.4</b>
456.hmmmer	4	743	50.3	741	50.3	<b>742</b>	<b>50.3</b>	4	<b>437</b>	<b>85.3</b>	437	85.3	436	85.5
458.sjeng	4	<b>859</b>	<b>56.4</b>	857	56.4	860	56.3	4	<b>765</b>	<b>63.2</b>	769	62.9	764	63.4
462.libquantum	4	2269	36.5	2267	36.6	<b>2269</b>	<b>36.5</b>	1	<b>336</b>	<b>61.7</b>	337	61.4	334	62.0
464.h264ref	4	899	98.4	<b>898</b>	<b>98.6</b>	897	98.7	4	859	103	856	103	<b>857</b>	<b>103</b>
471.omnetpp	4	<b>745</b>	<b>33.6</b>	745	33.5	745	33.6	4	720	34.7	<b>720</b>	<b>34.7</b>	720	34.7
473.astar	4	770	36.5	<b>768</b>	<b>36.6</b>	767	36.6	4	701	40.0	705	39.8	<b>705</b>	<b>39.8</b>
483.xalancbmk	4	<b>415</b>	<b>66.5</b>	415	66.5	416	66.4	4	<b>415</b>	<b>66.5</b>	415	66.5	416	66.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode

taskset was used to bind copies to cores, except for 462.libquantum peak  
OMP\_NUM\_THREADS was set to number of logical processors as seen by the OS  
KMP\_AFFINITY was set to physical,0

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.38.html>



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You can also download the XML flags source by saving the following link:

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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