



SPEC® CINT2006 Result

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Bull SAS

NovaScale T860 E1
(Intel Xeon X5260,3.33GHz)

SPECint®2006 = 26.9

SPECint_base2006 = 22.6

CPU2006 license: 20

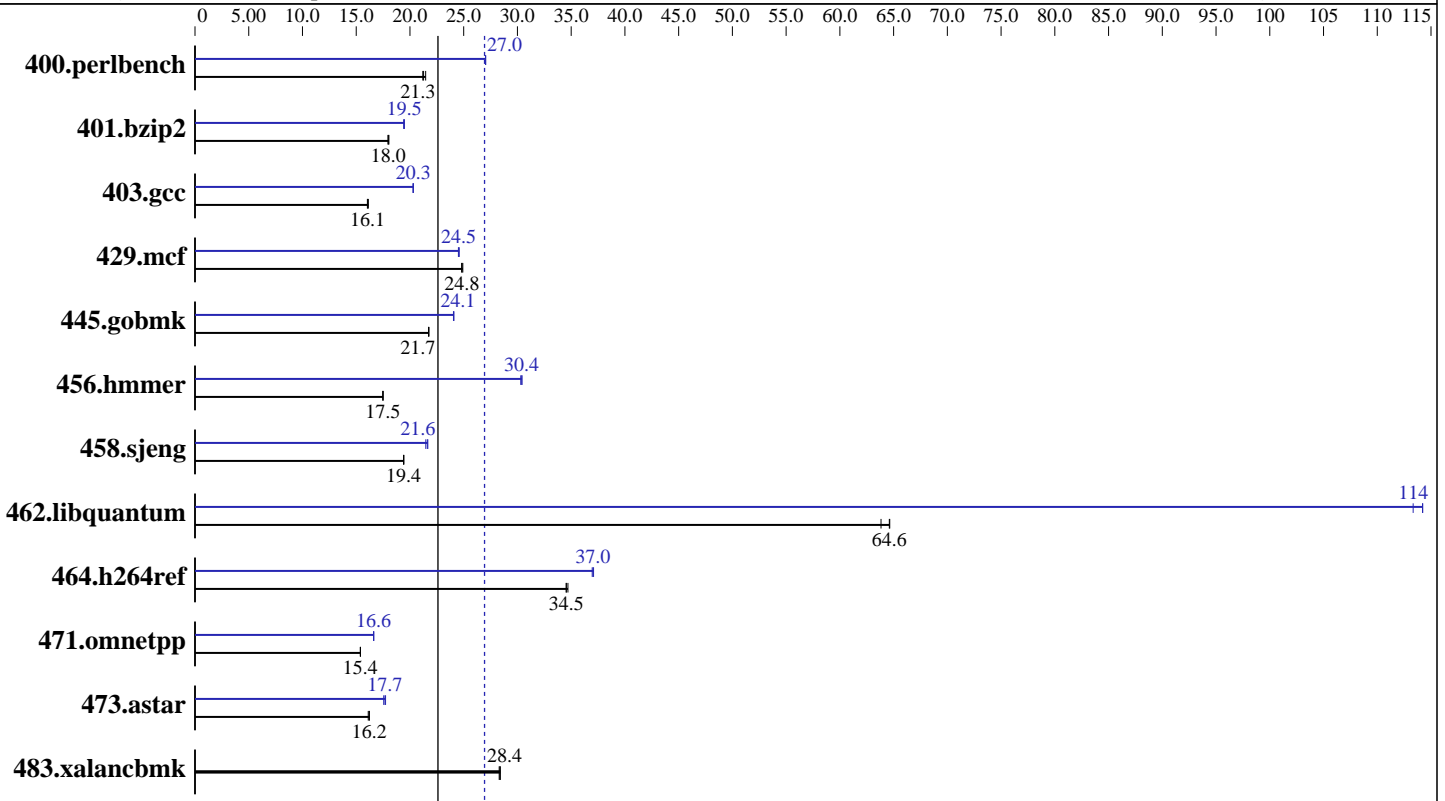
Test sponsor: Bull SAS

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2, 1333 MHz bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	461	21.2	<u>459</u>	<u>21.3</u>	455	21.5	<u>361</u>	<u>27.0</u>	361	27.0	361	27.0
401.bzip2	535	18.0	<u>536</u>	<u>18.0</u>	537	18.0	496	19.5	<u>496</u>	<u>19.5</u>	497	19.4
403.gcc	499	16.1	<u>501</u>	<u>16.1</u>	502	16.0	396	20.3	397	20.3	<u>397</u>	<u>20.3</u>
429.mcf	368	24.8	366	24.9	<u>367</u>	<u>24.8</u>	371	24.6	<u>372</u>	<u>24.5</u>	372	24.5
445.gobmk	<u>483</u>	<u>21.7</u>	483	21.7	482	21.8	<u>436</u>	<u>24.1</u>	436	24.1	436	24.0
456.hmmer	<u>534</u>	<u>17.5</u>	534	17.5	533	17.5	<u>307</u>	<u>30.4</u>	308	30.3	307	30.4
458.sjeng	<u>623</u>	<u>19.4</u>	624	19.4	623	19.4	<u>560</u>	<u>21.6</u>	564	21.5	559	21.7
462.libquantum	321	64.6	<u>321</u>	<u>64.6</u>	325	63.8	183	113	<u>181</u>	<u>114</u>	181	114
464.h264ref	638	34.7	641	34.5	<u>641</u>	<u>34.5</u>	<u>598</u>	<u>37.0</u>	599	37.0	597	37.1
471.omnetpp	406	15.4	<u>406</u>	<u>15.4</u>	406	15.4	376	16.6	376	16.6	<u>376</u>	<u>16.6</u>
473.astar	436	16.1	<u>433</u>	<u>16.2</u>	433	16.2	396	17.7	400	17.6	<u>397</u>	<u>17.7</u>
483.xalancbmk	<u>243</u>	<u>28.4</u>	243	28.4	244	28.3	<u>243</u>	<u>28.4</u>	243	28.4	244	28.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

The NEC Express5800/120Lj(Intel Xeon X5260) and the Bull NovaScale T860 E1(Intel Xeon X5260,3.33GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Lj(Intel Xeon X5260) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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