



SPEC® CINT2006 Result

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Intel Corporation

Intel Desktop Board DQ35JO (Intel Core 2 Duo Q9300)

SPECint®2006 = 19.3

SPECint_base2006 = 17.5

CPU2006 license: 13

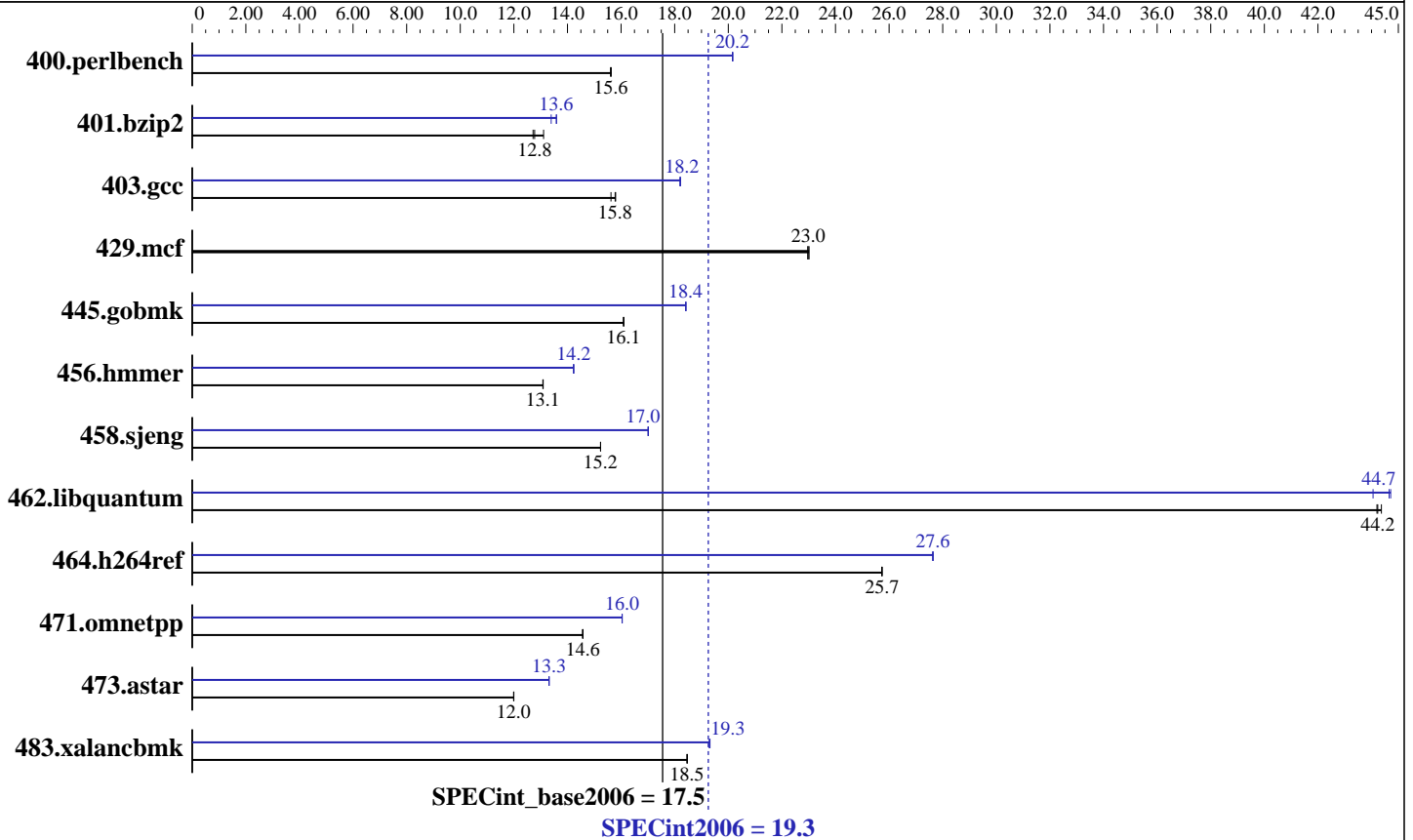
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Quad Q9300
 CPU Characteristics: 2.50 GHz, 1333 FSB
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB Micron DDR2-800 CL5)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	626	15.6	625	15.6	625	15.6	485	20.2	484	20.2	485	20.2
401.bzip2	736	13.1	759	12.7	755	12.8	721	13.4	710	13.6	710	13.6
403.gcc	510	15.8	515	15.6	510	15.8	442	18.2	442	18.2	442	18.2
429.mcf	396	23.0	397	23.0	397	23.0	396	23.0	397	23.0	397	23.0
445.gobmk	652	16.1	652	16.1	652	16.1	570	18.4	570	18.4	570	18.4
456.hammer	713	13.1	713	13.1	713	13.1	656	14.2	656	14.2	656	14.2
458.sjeng	794	15.2	794	15.2	794	15.2	711	17.0	711	17.0	711	17.0
462.libquantum	467	44.4	469	44.2	468	44.2	470	44.1	464	44.7	463	44.7
464.h264ref	860	25.7	860	25.7	860	25.7	801	27.6	801	27.6	801	27.6
471.omnetpp	429	14.6	429	14.6	429	14.6	390	16.0	390	16.0	390	16.0
473.astar	585	12.0	585	12.0	585	12.0	527	13.3	527	13.3	527	13.3
483.xalancbmk	374	18.5	374	18.5	374	18.5	358	19.3	358	19.3	357	19.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:

<http://www.intel.com/products/motherboard/DQ35JO/index.htm>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista Ultimate (32-bit)

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml>



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