



SPEC® CINT2006 Result

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Acer Incorporated

SPECint_rate2006 = 78.5

Acer Altos R920 (Intel Xeon E7320)

SPECint_rate_base2006 = 71.5

CPU2006 license: 97

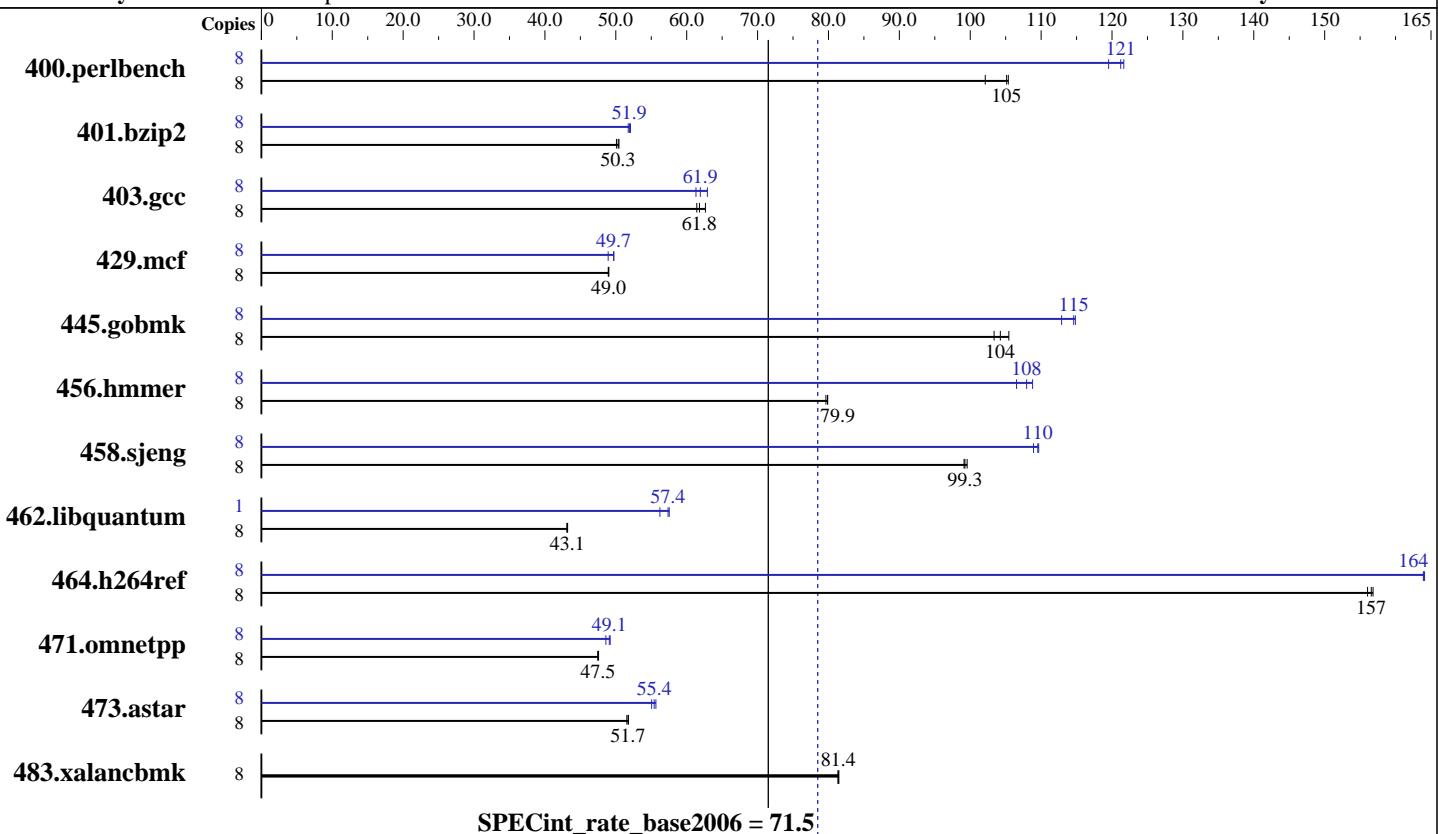
Test date: Oct-2007

Test sponsor: Acer Incorporated

Hardware Availability: Nov-2007

Tested by: Acer Incorporated

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7320
CPU Characteristics: 1066MHz system bus
CPU MHz: 2130
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2, 3, 4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (16 x 1048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64)
Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	765	102	743	105	742	105	8	642	122	654	120	645	121
401.bzip2	8	1530	50.4	1534	50.3	1541	50.1	8	1492	51.8	1487	51.9	1483	52.1
403.gcc	8	1042	61.8	1048	61.4	1028	62.6	8	1050	61.3	1040	61.9	1024	62.9
429.mcf	8	1489	49.0	1490	49.0	1491	48.9	8	1491	48.9	1468	49.7	1467	49.7
445.gobmk	8	796	105	812	103	805	104	8	743	113	732	115	731	115
456.hmmer	8	935	79.9	937	79.6	934	79.9	8	701	107	691	108	686	109
458.sjeng	8	977	99.1	975	99.3	972	99.6	8	889	109	883	110	884	110
462.libquantum	8	3837	43.2	3842	43.1	3846	43.1	1	369	56.2	361	57.4	360	57.5
464.h264ref	8	1131	157	1129	157	1134	156	8	1080	164	1079	164	1080	164
471.omnetpp	8	1051	47.6	1053	47.5	1053	47.5	8	1029	48.6	1019	49.1	1016	49.2
473.astar	8	1089	51.6	1085	51.8	1085	51.7	8	1020	55.1	1013	55.4	1009	55.7
483.xalancbmk	8	678	81.5	678	81.4	679	81.4	8	678	81.5	678	81.4	679	81.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

BIOS Settings

Hardware Prefetcher set to OFF

Adjacent Cache Line Prefetch set to OFF

All results but peak 462.libquantum used taskset to bind processes to cores.

"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:

```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 18:11:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 April 2008.