



SPEC[®] CINT2006 Result

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Hewlett-Packard Company

SPECint[®]_rate2006 = 140

ProLiant DL160 G5
(3.00 GHz, Intel Xeon X5472)

SPECint_rate_base2006 = 115

CPU2006 license: 3

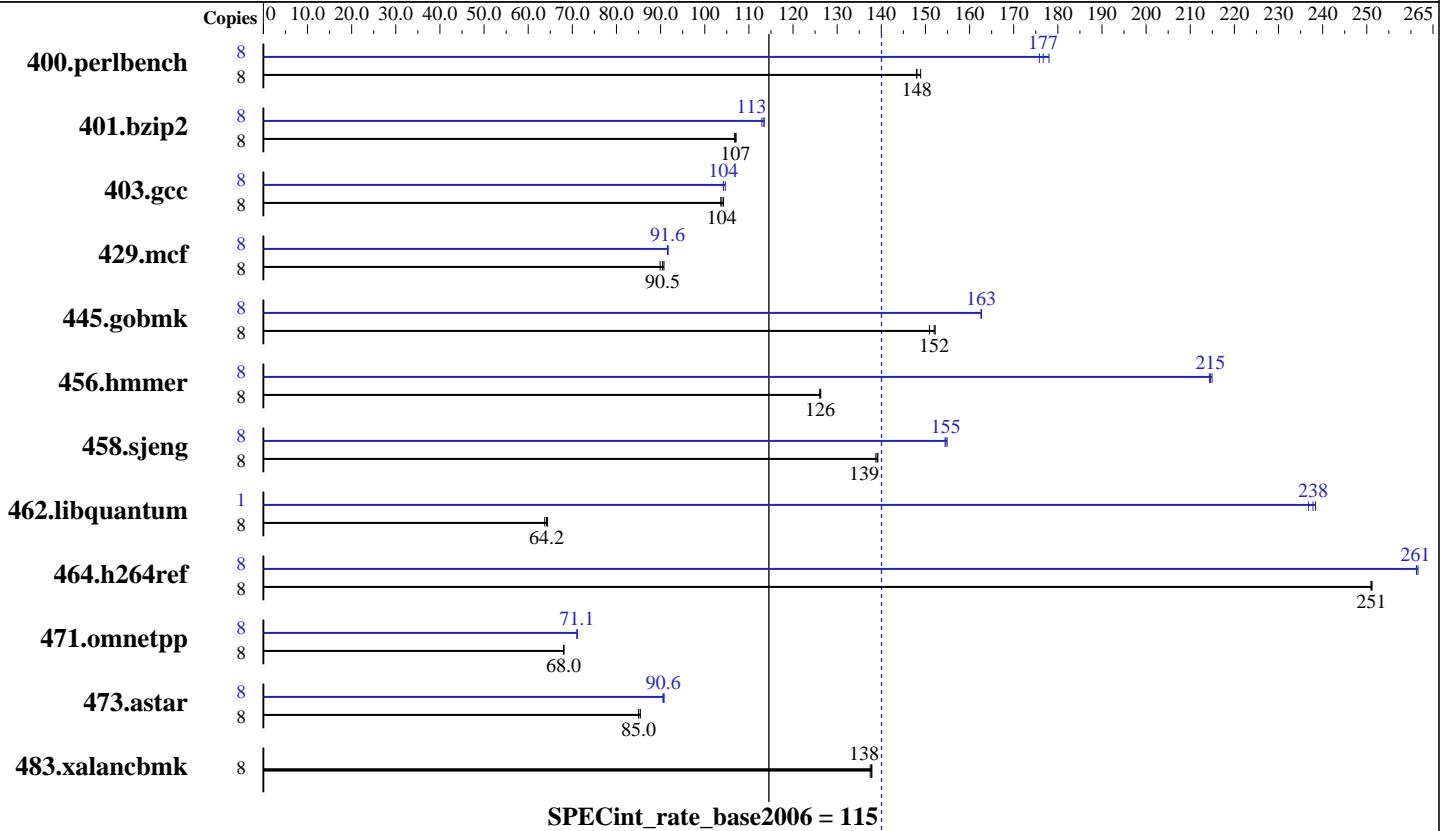
Test date: Feb-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5472
 CPU Characteristics: 3.00 GHz, 2x6 MB L2 shared, 1600 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8x4 GB PC2-5300F CL5)
 Disk Subsystem: 1x160 GB 7.2 K SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	525	149	<u>528</u>	<u>148</u>	528	148	8	<u>442</u>	<u>177</u>	439	178	445	176
401.bzip2	8	721	107	723	107	<u>722</u>	<u>107</u>	8	680	113	<u>681</u>	<u>113</u>	684	113
403.gcc	8	618	104	621	104	<u>620</u>	<u>104</u>	8	<u>618</u>	<u>104</u>	618	104	615	105
429.mcf	8	812	89.9	<u>806</u>	<u>90.5</u>	804	90.8	8	<u>797</u>	<u>91.6</u>	797	91.5	795	91.7
445.gobmk	8	<u>552</u>	<u>152</u>	556	151	551	152	8	516	163	<u>516</u>	<u>163</u>	516	163
456.hammer	8	<u>592</u>	<u>126</u>	592	126	591	126	8	<u>348</u>	<u>215</u>	347	215	348	214
458.sjeng	8	698	139	<u>696</u>	<u>139</u>	695	139	8	627	154	625	155	<u>626</u>	<u>155</u>
462.libquantum	8	2600	63.7	<u>2584</u>	<u>64.2</u>	2574	64.4	1	<u>87.1</u>	<u>238</u>	87.5	237	86.9	238
464.h264ref	8	705	251	705	251	<u>705</u>	<u>251</u>	8	<u>678</u>	<u>261</u>	677	262	678	261
471.omnetpp	8	<u>735</u>	<u>68.0</u>	734	68.1	735	68.0	8	703	71.1	<u>703</u>	<u>71.1</u>	704	71.1
473.astar	8	661	85.0	658	85.4	<u>660</u>	<u>85.0</u>	8	<u>620</u>	<u>90.6</u>	620	90.5	619	90.8
483.xalancbmk	8	400	138	401	137	<u>401</u>	<u>138</u>	8	400	138	401	137	<u>401</u>	<u>138</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.02.xml>

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