SPEC® CFP2006 Result
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IBM Corporation

IBM System x3500 (Intel Xeon E5410)

$SPECfp^®\_rate2006 =$ Not Run
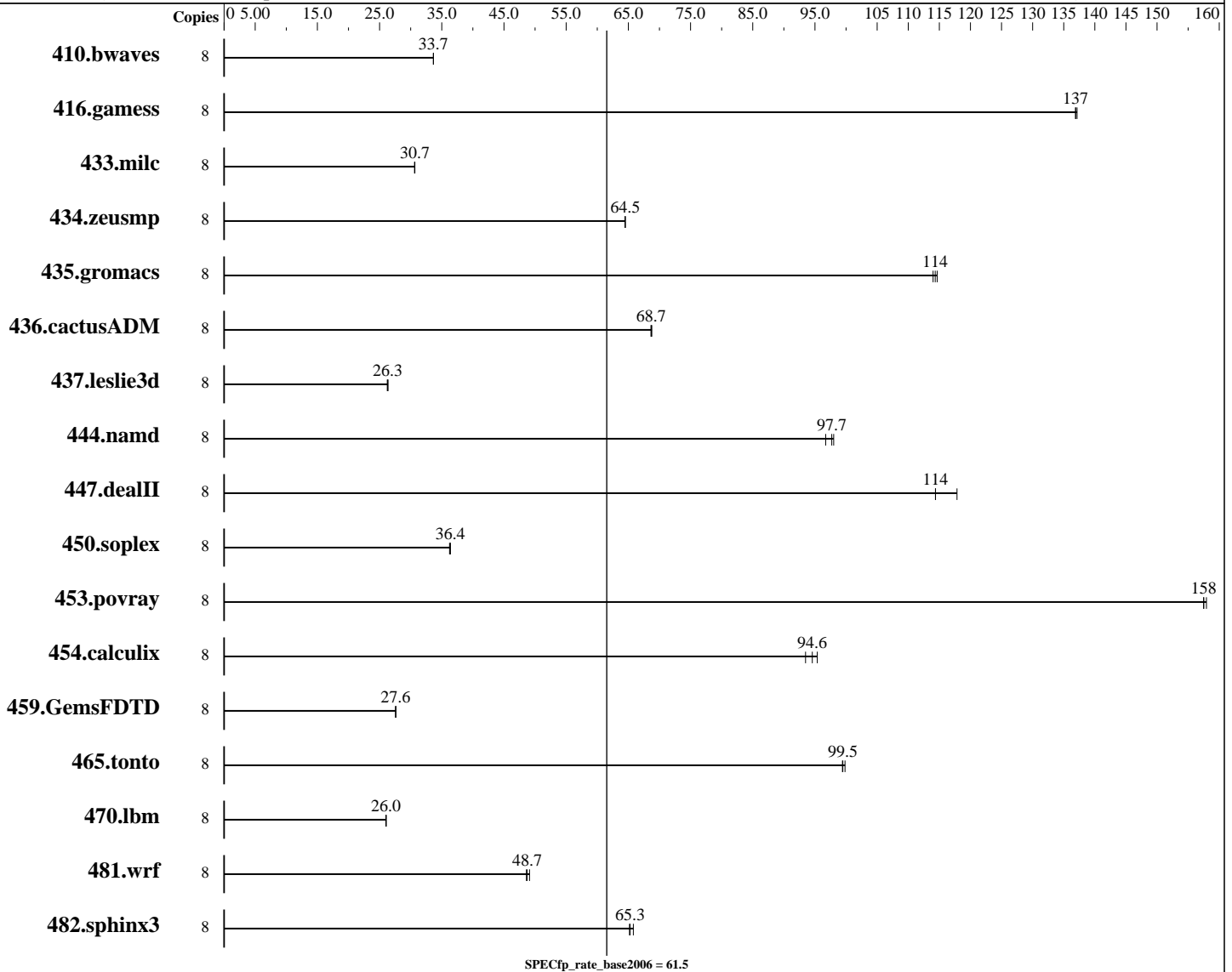
$SPECfp\_rate\_base2006 =$ 61.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Copies	Benchmark	Result
8	410.bwaves	33.7
8	416.gamess	137
8	433.milc	30.7
8	434.zeusmp	64.5
8	435.gromacs	114
8	436.cactusADM	68.7
8	437.leslie3d	26.3
8	444.namd	97.7
8	447.dealII	114
8	450.soplex	36.4
8	453.povray	158
8	454.calculix	94.6
8	459.GemsFDTD	27.6
8	465.tonto	99.5
8	470.lbm	26.0
8	481.wrf	48.7
8	482.sphinx3	65.3

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Hardware

CPU Name: Intel Xeon E5410
CPU Characteristics: 1333MHz system bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3229	33.7	3231	33.6	**3231**	**33.7**							
416.gamess	8	**1144**	**137**	1144	137	1142	137							
433.milc	8	**2395**	**30.7**	2399	30.6	2394	30.7							
434.zeusmp	8	1129	64.5	**1129**	**64.5**	1129	64.5							
435.gromacs	8	**500**	**114**	501	114	498	115							
436.cactusADM	8	**1392**	**68.7**	1390	68.8	1393	68.6							
437.leslie3d	8	2866	26.2	**2859**	**26.3**	2853	26.4							
444.namd	8	**657**	**97.7**	663	96.7	655	98.0							
447.dealII	8	777	118	**800**	**114**	800	114							
450.soplex	8	1834	36.4	**1835**	**36.4**	1841	36.2							
453.povray	8	**270**	**158**	270	157	269	158							
454.calculix	8	692	95.4	706	93.5	**698**	**94.6**							
459.GemsFDTD	8	3073	27.6	**3076**	**27.6**	3078	27.6							
465.tonto	8	789	99.8	**791**	**99.5**	792	99.4							
470.lbm	8	4222	26.0	**4221**	**26.0**	4220	26.0							
481.wrf	8	1838	48.6	**1834**	**48.7**	1820	49.1							
482.sphinx3	8	**2388**	**65.3**	2370	65.8	2392	65.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    All benchmarks compiled in 64-bit mode
    Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
    taskset utility used to bind CPU(s) to processes
    This result is measured on an IBM System x3500 Server. Note
    that the IBM System x3500 and IBM System x3400 are electrically
    equivalent.

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

          410.bwaves: -DSPEC_CPU_LP64
          416.gamess: -DSPEC_CPU_LP64
            433.milc: -DSPEC_CPU_LP64
          434.zeusmp: -DSPEC_CPU_LP64
         435.gromacs: -DSPEC_CPU_LP64 -nofor_main
       436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
         437.leslie3d: -DSPEC_CPU_LP64
            444.namd: -DSPEC_CPU_LP64
           447.dealII: -DSPEC_CPU_LP64
          450.soplex: -DSPEC_CPU_LP64
          453.povray: -DSPEC_CPU_LP64
         454.calculix: -DSPEC_CPU_LP64 -nofor_main
        459.GemsFDTD: -DSPEC_CPU_LP64
           465.tonto: -DSPEC_CPU_LP64
            470.lbm: -DSPEC_CPU_LP64
            481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
         482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -fast

Fortran benchmarks:
    -fast

Benchmarks using both Fortran and C:
    -fast

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The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.xml`

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For other inquiries, please contact webmaster@spec.org.

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