



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 21.1

ESPRIMO P5720, Intel Pentium Dual Core E2160 processor

SPECint_rate_base2006 = 19.2

CPU2006 license: 22

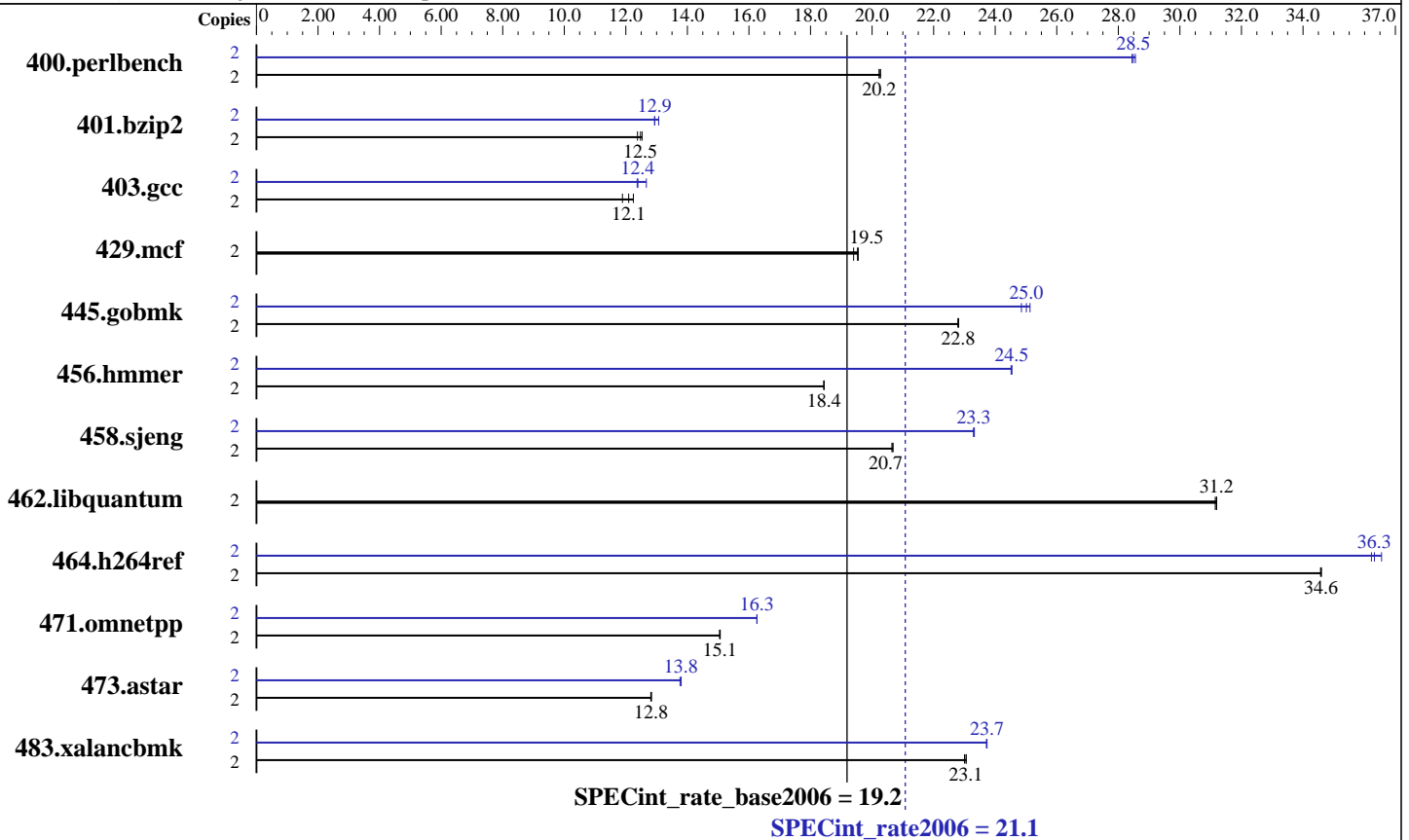
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Pentium Dual Core E2160
 CPU Characteristics: 1.80 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1 GB PC2-5300 DDR2-667 CL5)
 Disk Subsystem: Seagate 250 GB SATA, 8MB Cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional x64 Edition SP2
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	966	20.2	964	20.3	966	20.2	2	686	28.5	684	28.6	687	28.4
401.bzip2	2	1541	12.5	1559	12.4	1547	12.5	2	1492	12.9	1492	12.9	1477	13.1
403.gcc	2	1331	12.1	1354	11.9	1315	12.2	2	1299	12.4	1302	12.4	1271	12.7
429.mcf	2	940	19.4	933	19.6	934	19.5	2	940	19.4	933	19.6	934	19.5
445.gobmk	2	921	22.8	920	22.8	920	22.8	2	835	25.1	844	24.9	839	25.0
456.hmmmer	2	1012	18.4	1012	18.4	1012	18.4	2	761	24.5	761	24.5	760	24.5
458.sjeng	2	1171	20.7	1172	20.6	1170	20.7	2	1039	23.3	1039	23.3	1038	23.3
462.libquantum	2	1328	31.2	1329	31.2	1330	31.1	2	1328	31.2	1329	31.2	1330	31.1
464.h264ref	2	1280	34.6	1279	34.6	1280	34.6	2	1211	36.6	1222	36.2	1219	36.3
471.omnetpp	2	830	15.1	830	15.1	831	15.0	2	769	16.3	769	16.3	769	16.3
473.astar	2	1096	12.8	1095	12.8	1094	12.8	2	1018	13.8	1018	13.8	1020	13.8
483.xalancbmk	2	599	23.1	600	23.0	599	23.1	2	582	23.7	581	23.7	582	23.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.
Binaries were built in Windows Vista64 Ultimate.

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<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000
C++ benchmarks:
-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99
401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
C++ benchmarks:
icl -Qvc8

Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64

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Peak Portability Flags (Continued)

403.gcc: -DSPEC_CPU_WIN32
456.hmmr: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features -F512000000 shlW32M.lib
-link -FORCE:MULTIPLE



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml

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For other inquiries, please contact webmaster@spec.org.

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