



SPEC[®] CFP2006 Result

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Itautec

SPECfp[®]_rate2006 = 39.3

Servidor Itautec LX211 (Intel Xeon X5450)

SPECfp_rate_base2006 = 36.5

CPU2006 license: 9001

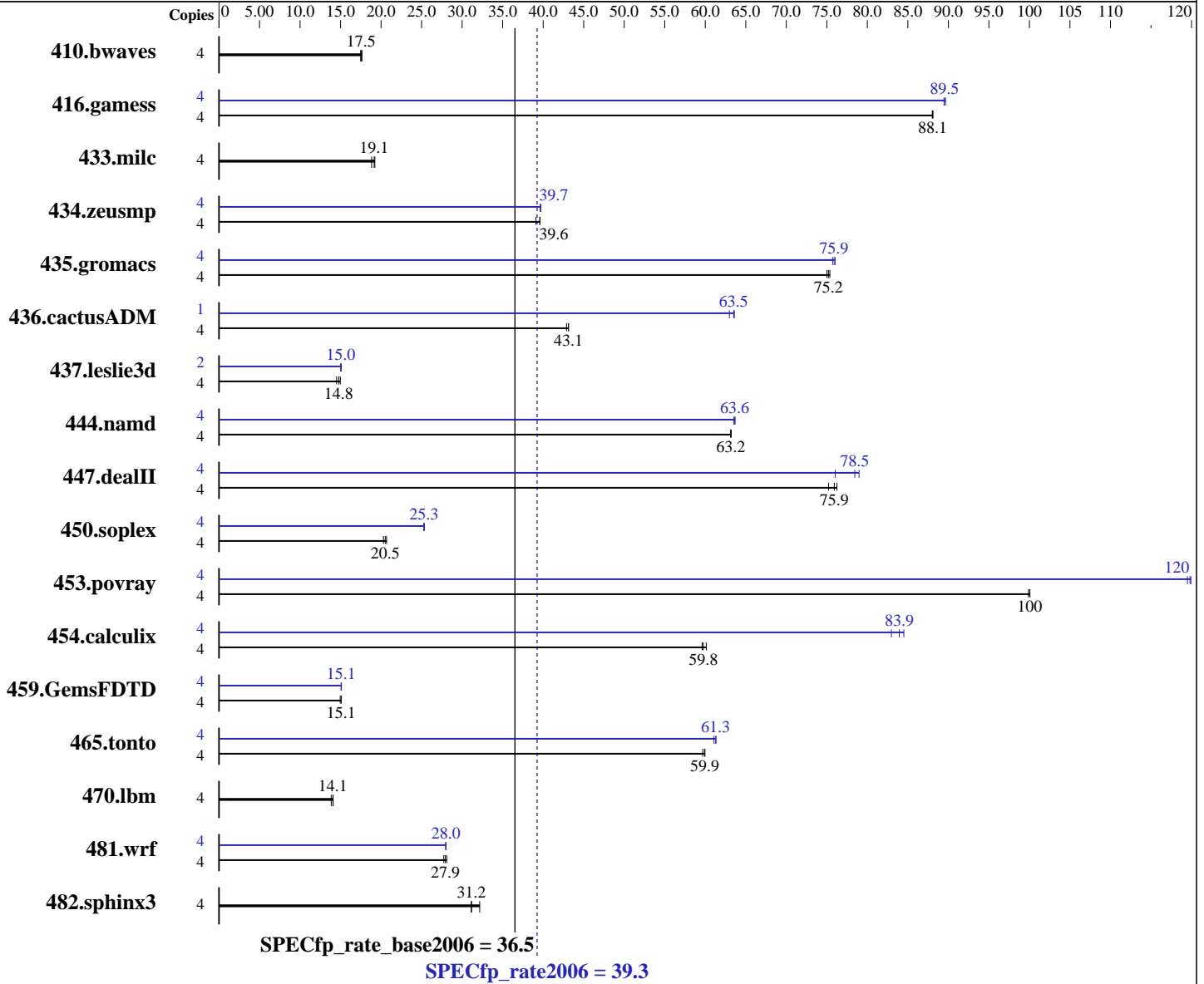
Test date: May-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5450
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler for Linux version 10.1
 Build 20080112 Package ID: l_cc_p_10.1.012, l_fc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)

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L3 Cache: None
Other Cache: None
Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.10.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	3109	17.5	3100	17.5	3081	17.6	4	3109	17.5	3100	17.5	3081	17.6
416.gamess	4	889	88.1	890	88.0	889	88.1	4	875	89.5	876	89.5	874	89.6
433.milc	4	1950	18.8	1909	19.2	1918	19.1	4	1950	18.8	1909	19.2	1918	19.1
434.zeusmp	4	931	39.1	920	39.6	919	39.6	4	918	39.6	917	39.7	917	39.7
435.gromacs	4	381	75.0	379	75.4	380	75.2	4	377	75.7	376	76.0	376	75.9
436.cactusADM	4	1115	42.9	1108	43.1	1109	43.1	1	190	63.0	188	63.6	188	63.5
437.leslie3d	4	2591	14.5	2512	15.0	2544	14.8	2	1247	15.1	1250	15.0	1253	15.0
444.namd	4	508	63.2	508	63.1	508	63.2	4	505	63.5	504	63.7	504	63.6
447.dealII	4	608	75.2	603	75.9	600	76.3	4	579	79.0	583	78.5	602	76.0
450.soplex	4	1644	20.3	1627	20.5	1613	20.7	4	1316	25.3	1319	25.3	1320	25.3
453.povray	4	213	99.9	213	100	213	100	4	177	120	178	120	178	119
454.calculix	4	549	60.1	552	59.8	554	59.6	4	398	83.0	391	84.5	393	83.9
459.GemsFDTD	4	2838	15.0	2812	15.1	2812	15.1	4	2816	15.1	2814	15.1	2809	15.1
465.tonto	4	657	59.9	659	59.7	657	59.9	4	642	61.3	642	61.3	644	61.1
470.lbm	4	3969	13.8	3910	14.1	3902	14.1	4	3969	13.8	3910	14.1	3902	14.1
481.wrf	4	1611	27.7	1600	27.9	1590	28.1	4	1595	28.0	1598	28.0	1596	28.0
482.sphinx3	4	2505	31.1	2424	32.2	2502	31.2	4	2505	31.1	2424	32.2	2502	31.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 436.cactusADM at peak.



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Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

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Base Optimization Flags (Continued)

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/10.1.012/bin/icpc -L/opt/intel/cc/10.1.012/lib
-I/opt/intel/cc/10.1.012/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /opt/intel/fc/10.1.012/bin/ifort -L/opt/intel/fc/10.1.012/lib
-I/opt/intel/fc/10.1.012/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-FP-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-FP-intel64-linux-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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