



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

## SPECint®\_rate2006 = Not Run

ProLiant DL380 G5  
(3.33 GHz, Intel Xeon X5470)

## SPECint\_rate\_base2006 = 139

CPU2006 license: 3

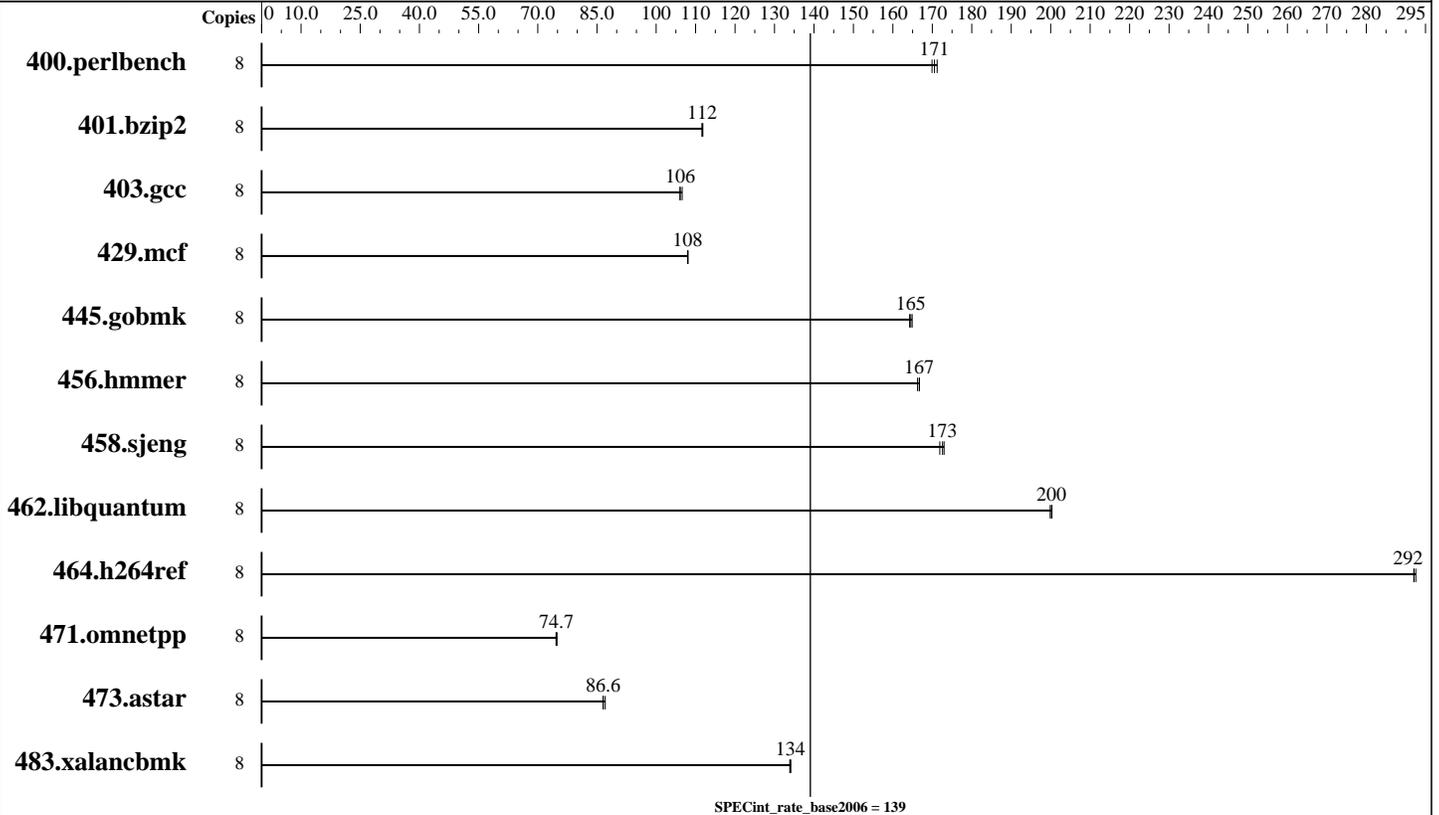
Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5470  
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant DL380 G5  
(3.33 GHz, Intel Xeon X5470)

SPECint\_rate2006 = Not Run

SPECint\_rate\_base2006 = 139

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Aug-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	456	171	<b>458</b>	<b>171</b>	460	170							
401.bzip2	8	<b>691</b>	<b>112</b>	691	112	690	112							
403.gcc	8	<b>607</b>	<b>106</b>	604	107	607	106							
429.mcf	8	<b>676</b>	<b>108</b>	676	108	675	108							
445.gobmk	8	<b>510</b>	<b>165</b>	509	165	511	164							
456.hammer	8	448	167	<b>448</b>	<b>167</b>	449	166							
458.sjeng	8	<b>561</b>	<b>173</b>	563	172	560	173							
462.libquantum	8	829	200	<b>828</b>	<b>200</b>	827	200							
464.h264ref	8	<b>606</b>	<b>292</b>	606	292	605	293							
471.omnetpp	8	670	74.6	<b>669</b>	<b>74.7</b>	668	74.9							
473.astar	8	645	87.1	649	86.5	<b>648</b>	<b>86.6</b>							
483.xalancbmk	8	412	134	412	134	<b>412</b>	<b>134</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except  
for 462.libquantum peak

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled  
Hardware Prefetcher Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = Not Run**

ProLiant DL380 G5  
(3.33 GHz, Intel Xeon X5470)

**SPECint\_rate\_base2006 = 139**

**CPU2006 license:** 3

**Test date:** Aug-2008

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Sep-2008

**Tested by:** Hewlett-Packard Company

**Software Availability:** Nov-2008

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 19:41:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 16 September 2008.