



# SPEC® CINT2006 Result

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## Bull SAS

NovaScale B260  
(Intel Xeon E5440, 2.83 GHz)

SPECint®2006 = 25.1

SPECint\_base2006 = 22.1

CPU2006 license: 20

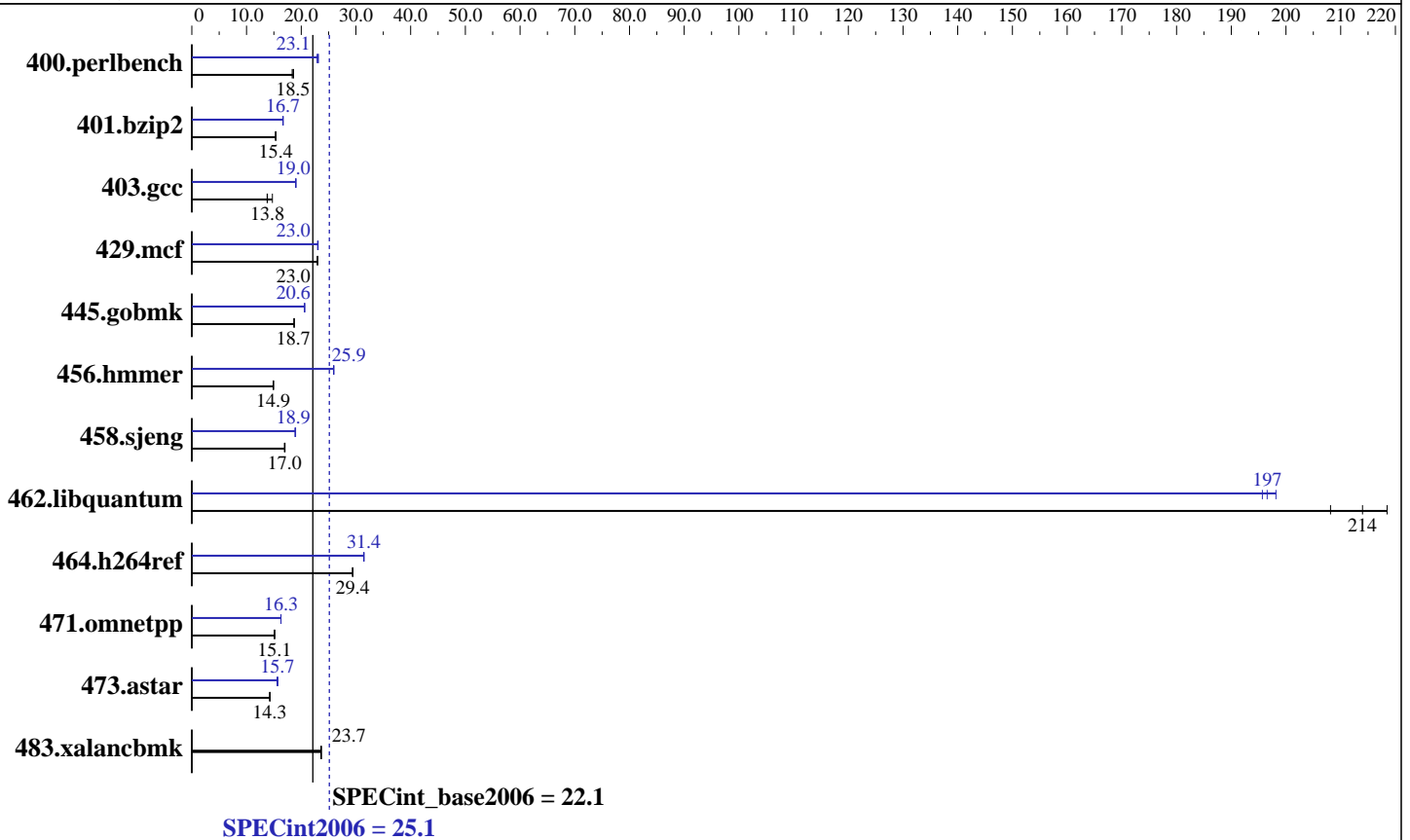
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Sep-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5440  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2833  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4x4 GB) FB-DIMM PC2-5300F ECC CL5  
 Disk Subsystem: 1x73 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1  
 Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux  
 Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.50.0.15  
 SmartHeap library V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	525	18.6	533	18.3	<u>528</u>	<u>18.5</u>	427	22.9	423	23.1	<u>423</u>	<u>23.1</u>
401.bzip2	631	15.3	628	15.4	<u>628</u>	<u>15.4</u>	578	16.7	580	16.6	<u>579</u>	<u>16.7</u>
403.gcc	585	13.8	547	14.7	<u>582</u>	<u>13.8</u>	423	19.0	424	19.0	<u>423</u>	<u>19.0</u>
429.mcf	396	23.0	398	22.9	<u>397</u>	<u>23.0</u>	396	23.0	395	23.1	<u>396</u>	<u>23.0</u>
445.gobmk	561	18.7	561	18.7	<u>561</u>	<u>18.7</u>	509	20.6	509	20.6	<u>509</u>	<u>20.6</u>
456.hmmer	625	14.9	<u>626</u>	<u>14.9</u>	626	14.9	<u>360</u>	<u>25.9</u>	360	25.9	359	26.0
458.sjeng	710	17.0	714	16.9	<u>713</u>	<u>17.0</u>	642	18.9	638	19.0	<u>639</u>	<u>18.9</u>
462.libquantum	94.8	218	99.5	208	<u>96.8</u>	<u>214</u>	106	196	105	198	<u>105</u>	<u>197</u>
464.h264ref	751	29.5	<u>753</u>	<u>29.4</u>	754	29.4	<u>704</u>	<u>31.4</u>	705	31.4	703	31.5
471.omnetpp	413	15.1	<u>413</u>	<u>15.1</u>	414	15.1	384	16.3	<u>384</u>	<u>16.3</u>	384	16.3
473.astar	493	14.2	491	14.3	<u>492</u>	<u>14.3</u>	<u>448</u>	<u>15.7</u>	450	15.6	447	15.7
483.xalancbmk	<u>292</u>	<u>23.7</u>	292	23.6	291	23.7	<u>292</u>	<u>23.7</u>	292	23.6	291	23.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null
```

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

### C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

### C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at  
[http://www.spec.org/cpu2006/flags/EM64T\\_Intel101\\_int\\_flags.20090713.html](http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.html)

You can also download the XML flags source by saving the following link:  
[http://www.spec.org/cpu2006/flags/EM64T\\_Intel101\\_int\\_flags.20090713.xml](http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.xml)

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