



SPEC[®] CINT2006 Result

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NTT System S. A.
NTT Tytan S8 Series

SPECint[®]_rate2006 = 105
SPECint_rate_base2006 = 100

CPU2006 license: 9013

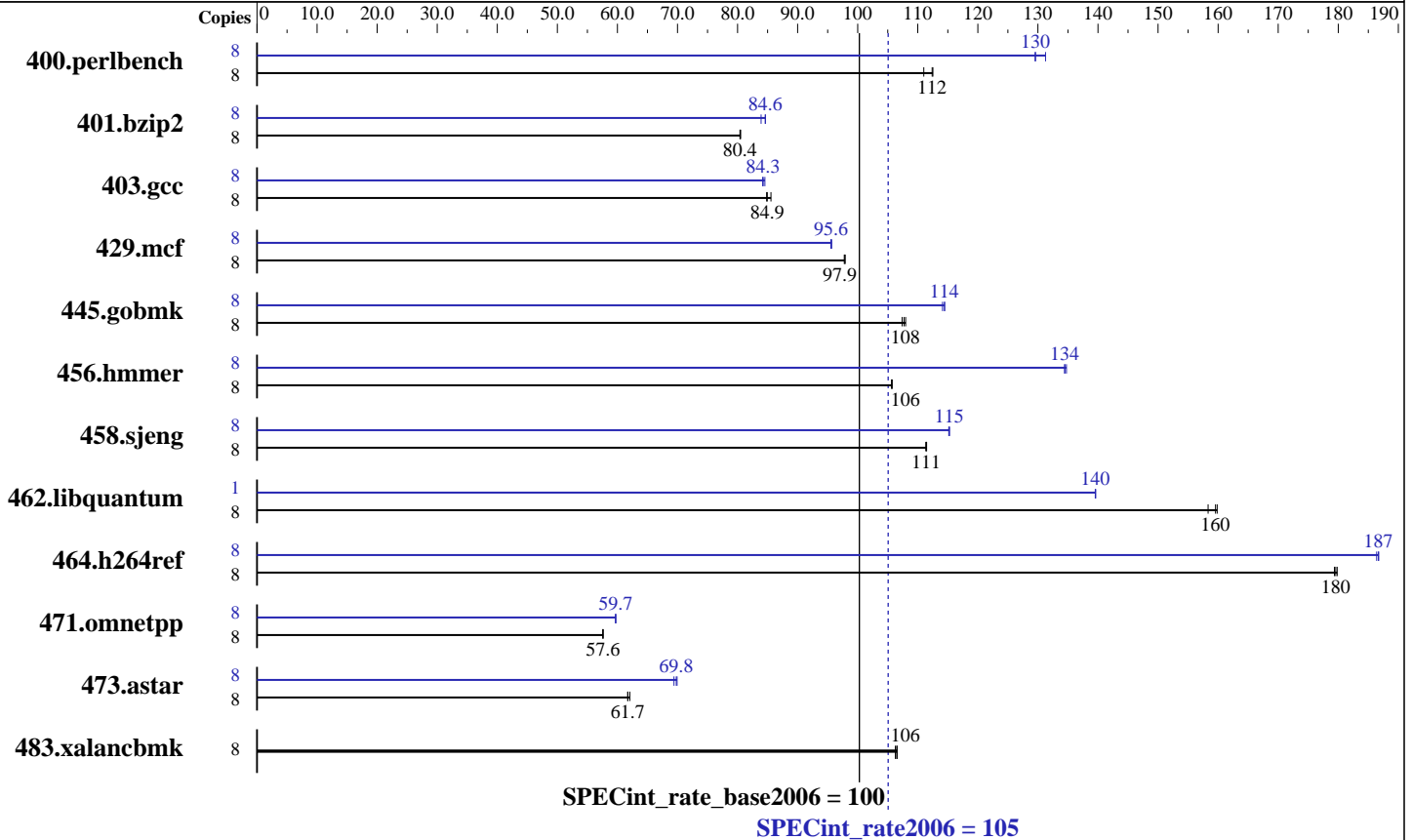
Test sponsor: NTT System S. A.

Tested by: NTT System S. A.

Test date: Dec-2008

Hardware Availability: Dec-2008

Software Availability: Dec-2008



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 2 GHz, 2x6 MB P2 shared, 1333 MHz System Bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4GB)
 Disk Subsystem: 300 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSe Linux SLES10 SP1, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	704	111	695	113	695	112	8	595	131	603	130	603	130
401.bzip2	8	960	80.4	959	80.5	960	80.4	8	920	83.9	912	84.6	913	84.6
403.gcc	8	753	85.6	758	84.9	759	84.8	8	765	84.2	764	84.3	762	84.5
429.mcf	8	746	97.8	745	97.9	745	97.9	8	763	95.6	764	95.5	763	95.7
445.gobmk	8	777	108	779	108	781	107	8	733	114	735	114	733	114
456.hammer	8	707	106	706	106	706	106	8	555	134	554	135	555	134
458.sjeng	8	869	111	869	111	868	111	8	840	115	840	115	841	115
462.libquantum	8	1039	160	1037	160	1047	158	1	148	140	148	140	148	140
464.h264ref	8	987	179	986	180	985	180	8	950	186	948	187	948	187
471.omnetpp	8	868	57.6	868	57.6	868	57.6	8	838	59.7	838	59.7	837	59.7
473.astar	8	910	61.7	905	62.1	910	61.7	8	809	69.4	805	69.8	803	69.9
483.xalancbmk	8	519	106	518	107	518	106	8	519	106	518	107	518	106

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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