



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

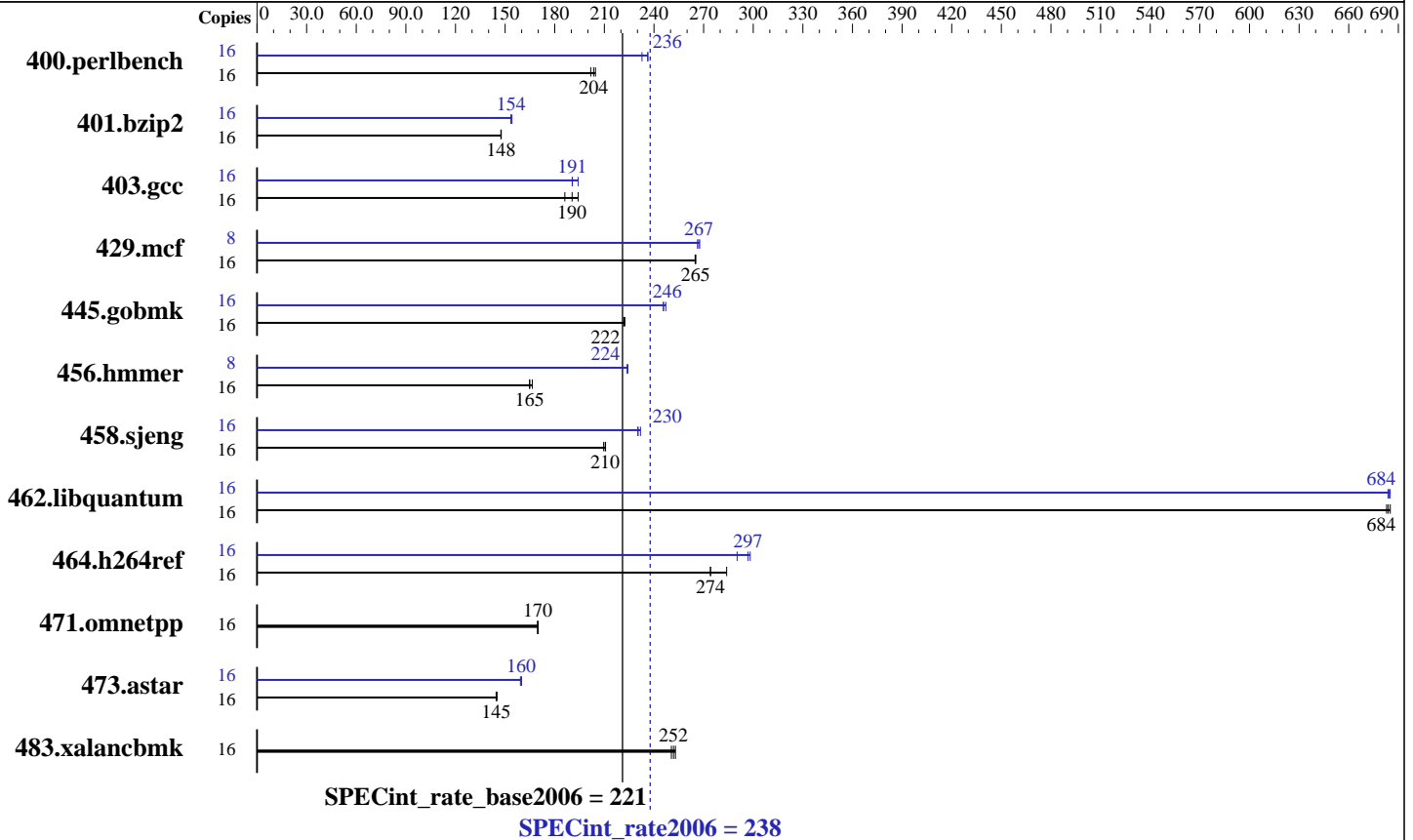
NovaScale R460 E2  
(Intel Xeon X5550, 2.66 GHz)

SPECint®\_rate2006 = 238

SPECint\_rate\_base2006 = 221

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: NEC Corporation

Test date: Apr-2009  
Hardware Availability: Apr-2009  
Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)  
 Disk Subsystem: 1x146.5 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R460 E2  
(Intel Xeon X5550, 2.66 GHz)

SPECint\_rate2006 = 238

SPECint\_rate\_base2006 = 221

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: NEC Corporation

Test date: Apr-2009  
Hardware Availability: Apr-2009  
Software Availability: Feb-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	764	205	775	202	<b>768</b>	<b>204</b>	16	<b>662</b>	<b>236</b>	661	236	672	233
401.bzip2	16	1047	147	1046	148	<b>1046</b>	<b>148</b>	16	<b>1005</b>	<b>154</b>	1002	154	1006	154
403.gcc	16	692	186	<b>676</b>	<b>190</b>	663	194	16	676	191	<b>675</b>	<b>191</b>	664	194
429.mcf	16	550	265	<b>550</b>	<b>265</b>	551	265	8	274	266	273	268	<b>273</b>	<b>267</b>
445.gobmk	16	757	222	755	222	<b>756</b>	<b>222</b>	16	<b>683</b>	<b>246</b>	683	246	679	247
456.hammer	16	897	166	<b>905</b>	<b>165</b>	906	165	8	333	224	<b>333</b>	<b>224</b>	333	224
458.sjeng	16	924	210	<b>920</b>	<b>210</b>	919	211	16	<b>840</b>	<b>230</b>	841	230	835	232
462.libquantum	16	<b>485</b>	<b>684</b>	485	683	484	685	16	484	685	485	684	<b>484</b>	<b>684</b>
464.h264ref	16	1247	284	<b>1291</b>	<b>274</b>	1292	274	16	1220	290	1188	298	<b>1193</b>	<b>297</b>
471.omnetpp	16	<b>589</b>	<b>170</b>	589	170	590	170	16	<b>589</b>	<b>170</b>	589	170	590	170
473.astar	16	774	145	777	145	<b>777</b>	<b>145</b>	16	<b>703</b>	<b>160</b>	702	160	705	159
483.xalancbmk	16	441	251	436	253	<b>438</b>	<b>252</b>	16	441	251	436	253	<b>438</b>	<b>252</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS setting:  
NUMA configuration: Enabled

## General Notes

The NEC Express5800/R120a-1(Intel Xeon X5550),  
the NEC Express5800/R120a-2(Intel Xeon X5550),  
the Bull NovaScale R440 E2 (Intel Xeon X5550, 2.66 GHz) and  
the Bull NovaScale R460 E2 (Intel Xeon X5550, 2.66 GHz) models are electronically equivalent.  
The results have been measured on a NEC Express5800/R120a-1(Intel Xeon X5550) model.

## Base Compiler Invocation

C benchmarks:  
icc

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R460 E2  
(Intel Xeon X5550, 2.66 GHz)

SPECint\_rate2006 = 238

SPECint\_rate\_base2006 = 221

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: NEC Corporation

Test date: Apr-2009  
Hardware Availability: Apr-2009  
Software Availability: Feb-2009

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc  
458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):  
icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R460 E2  
(Intel Xeon X5550, 2.66 GHz)

SPECint\_rate2006 = 238

SPECint\_rate\_base2006 = 221

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** NEC Corporation

**Test date:** Apr-2009  
**Hardware Availability:** Apr-2009  
**Software Availability:** Feb-2009

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R460 E2  
(Intel Xeon X5550, 2.66 GHz)

SPECint\_rate2006 = 238

SPECint\_rate\_base2006 = 221

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: NEC Corporation

Test date: Apr-2009  
Hardware Availability: Apr-2009  
Software Availability: Feb-2009

## Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
           -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64
```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.20090710.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 00:48:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 5 May 2009.