SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL380 G6
(2.93 GHz, Intel Xeon X5570)

SPECint®_rate2006 =   251

SPECint_rate_base2006 =   233

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Benchmark	Copies	Result
400.perlbench	16	250
400.perlbench	16	214
401.bzip2	16	163
401.bzip2	16	154
403.gcc	16	200
403.gcc	16	201
429.mcf	8	278
429.mcf	16	265
445.gobmk	16	260
445.gobmk	16	236
456.hmmer	8	240
456.hmmer	16	179
458.sjeng	16	245
458.sjeng	16	222
462.libquantum	16	751
462.libquantum	16	750
464.h264ref	16	317
464.h264ref	16	295
471.omnetpp	16	173
473.astar	16	170
473.astar	16	155
483.xalancbmk	16	263

SPECint_rate_base2006 = 233

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Hardware

CPU Name: Intel Xeon X5570
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6x8 GB PC3-10600R CL9)
Disk Subsystem: 1x146 GB 10K RPM SAS
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3
Kernel 2.6.18-128.el5
Compiler: Intel C++ Compiler 11.0 for Linux
Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

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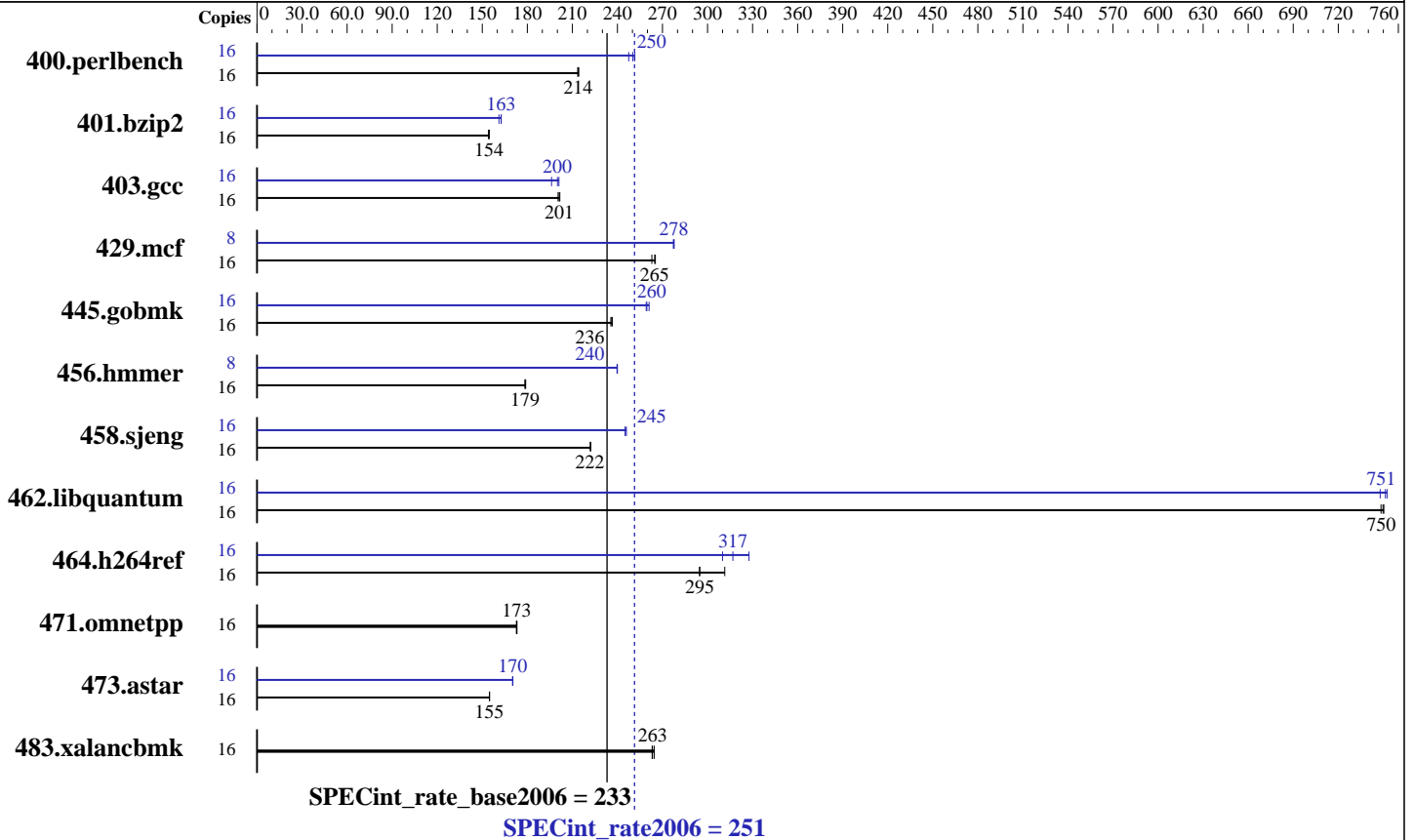|	
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	732	214	730	214	**731**	**214**	16	632	248	**625**	**250**	621	252
401.bzip2	16	**1000**	**154**	1001	154	999	155	16	958	161	950	163	**950**	**163**
403.gcc	16	642	200	639	202	**640**	**201**	16	**644**	**200**	657	196	641	201
429.mcf	16	**551**	**265**	555	263	550	265	8	**263**	**278**	263	278	263	277
445.gobmk	16	**711**	**236**	709	237	712	236	16	643	261	**646**	**260**	648	259
456.hmmer	16	**836**	**179**	834	179	837	178	8	311	240	**311**	**240**	311	240
458.sjeng	16	872	222	**872**	**222**	872	222	16	787	246	790	245	**790**	**245**
462.libquantum	16	**442**	**750**	443	748	442	750	16	443	748	**441**	**751**	441	752
464.h264ref	16	**1201**	**295**	1137	311	1202	295	16	1081	328	1142	310	**1117**	**317**
471.omnetpp	16	579	173	**578**	**173**	578	173	16	579	173	**578**	**173**	578	173
473.astar	16	726	155	**726**	**155**	726	155	16	**660**	**170**	659	170	660	170
483.xalancbmk	16	417	264	**419**	**263**	420	263	16	417	264	**419**	**263**	420	263

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Platform Notes

```
    BIOS configuration:
       Power Regulator set to Static High Performance Mode
       Power Profile set to Maximum Performance
       Thermal Configuration set to Increased Cooling
```

Base Compiler Invocation

```
C benchmarks:
     icc
```

```
C++ benchmarks:
     icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
```

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias -opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc`
`-opt-malloc-options=3`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static`
`-opt-malloc-options=3 -opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.10.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 00:21:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 May 2009.