



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 33.3

IBM System x3650 M2 (Intel Xeon X5550)

SPECint_base2006 = 29.3

CPU2006 license: 11

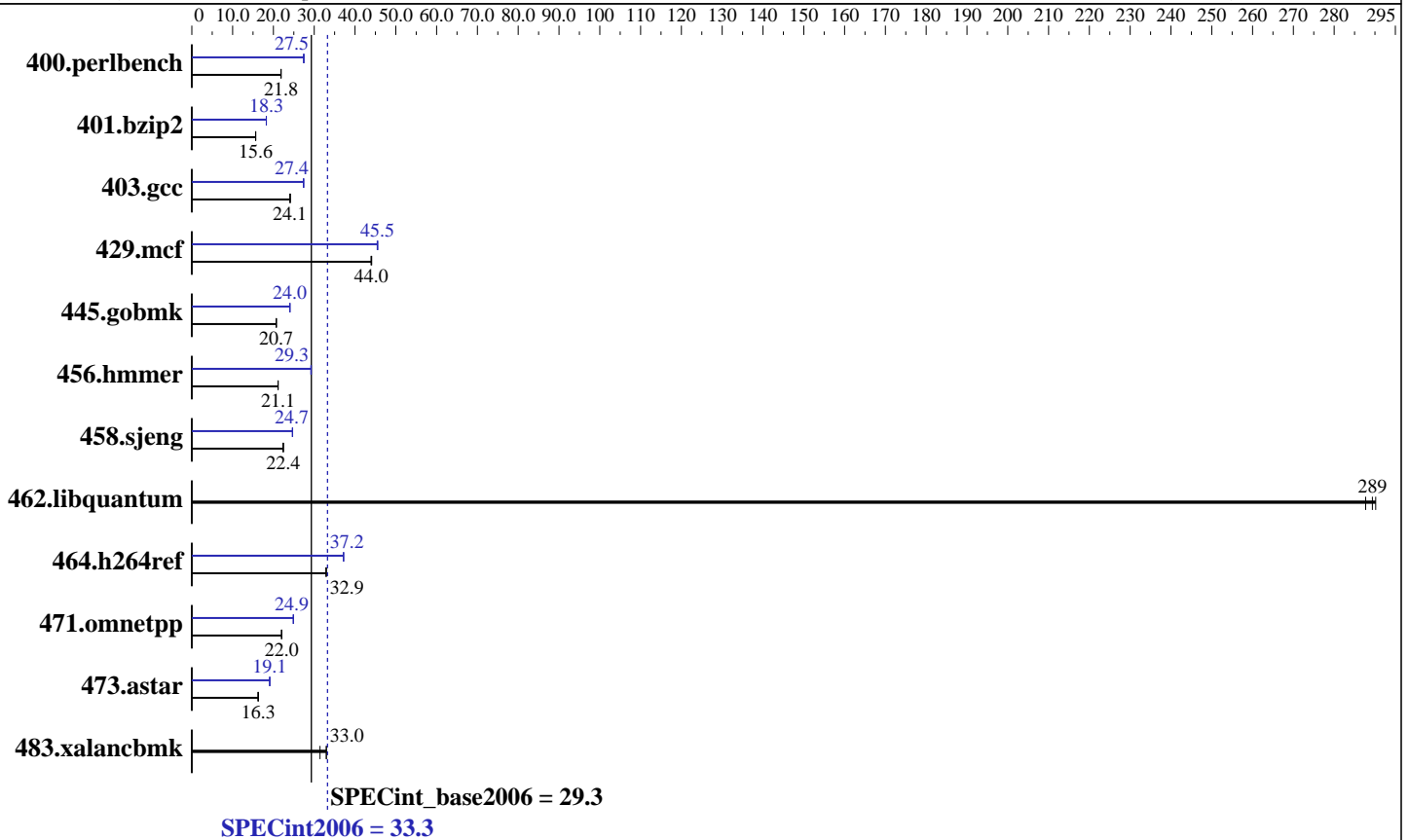
Test date: Jun-2009

Test sponsor: IBM Corporation

Hardware Availability: Apr-2009

Tested by: IBM Corporation

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB PC3-10600R)
 Disk Subsystem: 1 x 73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	21.8	448	21.8	446	21.9	357	27.4	355	27.5	355	27.5
401.bzip2	617	15.6	616	15.7	617	15.6	528	18.3	528	18.3	529	18.2
403.gcc	333	24.2	335	24.1	335	24.1	293	27.5	294	27.4	294	27.3
429.mcf	207	44.1	207	44.0	207	44.0	201	45.4	200	45.5	200	45.6
445.gobmk	507	20.7	507	20.7	507	20.7	437	24.0	436	24.0	436	24.1
456.hammer	441	21.1	441	21.2	441	21.1	319	29.3	319	29.3	320	29.2
458.sjeng	539	22.4	542	22.3	539	22.5	490	24.7	490	24.7	491	24.6
462.libquantum	71.6	289	72.0	288	71.4	290	71.6	289	72.0	288	71.4	290
464.h264ref	671	33.0	673	32.9	672	32.9	596	37.2	594	37.2	595	37.2
471.omnetpp	284	22.0	284	22.0	284	22.0	251	24.9	251	24.9	251	24.9
473.astar	430	16.3	433	16.2	431	16.3	368	19.1	367	19.1	368	19.1
483.xalancbmk	209	33.0	220	31.4	209	33.0	209	33.0	220	31.4	209	33.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Processor CPU C-States Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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