



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 38.5**

Gateway GB2x280 F1 (Intel Xeon X5570, 2.93 GHz)

**SPECint\_base2006 = 35.4**

CPU2006 license: 97

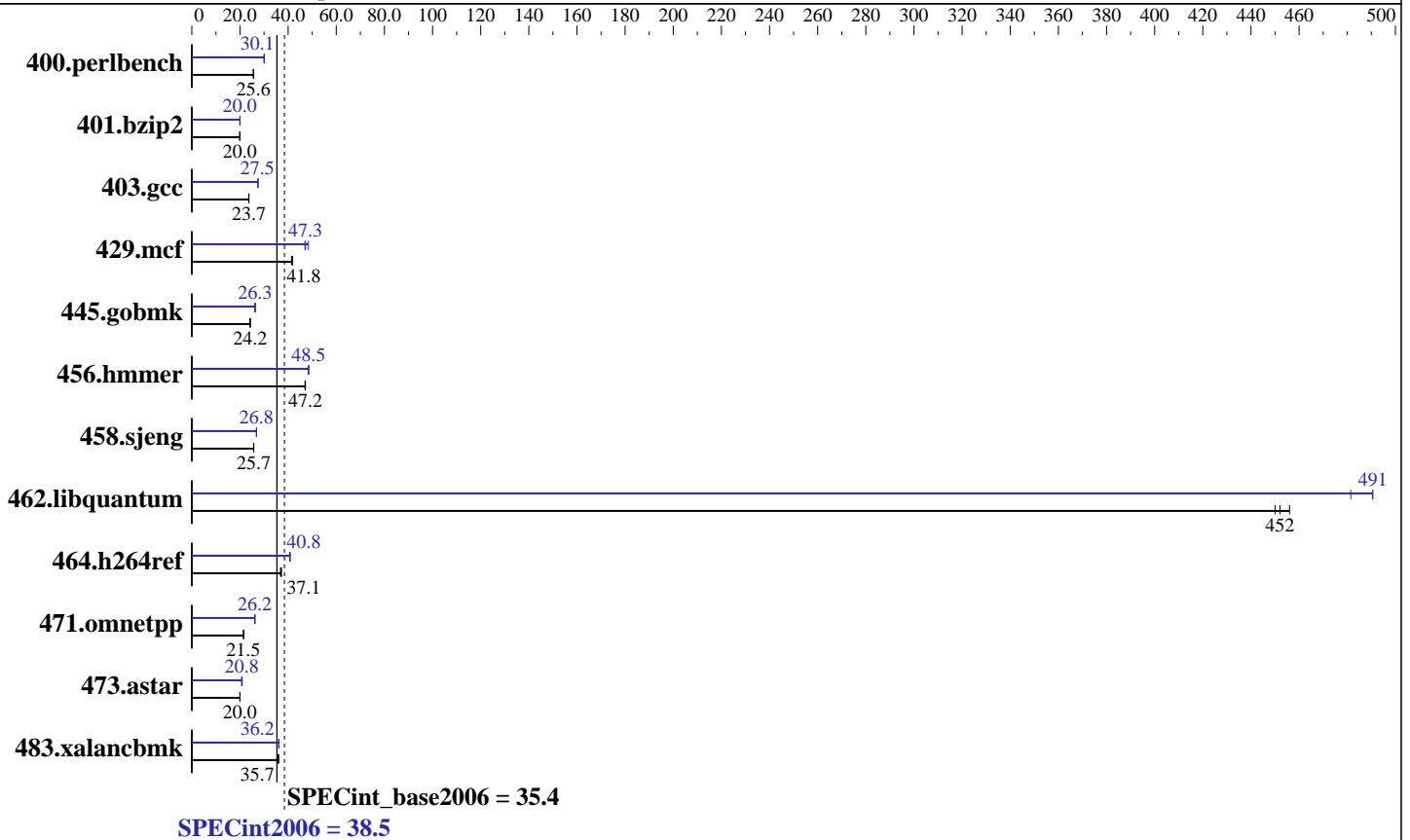
Test date: May-2010

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM, ECC, CL9)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	382	25.6	382	25.6	<b><u>382</u></b>	<b><u>25.6</u></b>	<b><u>325</u></b>	<b><u>30.1</u></b>	325	30.1	325	30.0
401.bzip2	485	19.9	483	20.0	<b><u>484</u></b>	<b><u>20.0</u></b>	<b><u>483</u></b>	<b><u>20.0</u></b>	483	20.0	482	20.0
403.gcc	339	23.7	341	23.6	<b><u>340</u></b>	<b><u>23.7</u></b>	294	27.4	<b><u>293</u></b>	<b><u>27.5</u></b>	293	27.5
429.mcf	220	41.4	218	41.8	<b><u>218</u></b>	<b><u>41.8</u></b>	<b><u>193</u></b>	<b><u>47.3</u></b>	188	48.4	194	47.0
445.gobmk	434	24.2	<b><u>433</u></b>	<b><u>24.2</u></b>	432	24.3	400	26.3	398	26.4	<b><u>398</u></b>	<b><u>26.3</u></b>
456.hmmer	198	47.2	198	47.0	<b><u>198</u></b>	<b><u>47.2</u></b>	192	48.5	<b><u>192</u></b>	<b><u>48.5</u></b>	192	48.6
458.sjeng	<b><u>471</u></b>	<b><u>25.7</u></b>	471	25.7	470	25.7	451	26.8	450	26.9	<b><u>451</u></b>	<b><u>26.8</u></b>
462.libquantum	<b><u>45.8</u></b>	<b><u>452</u></b>	46.0	450	45.4	456	42.2	491	43.0	481	<b><u>42.2</u></b>	<b><u>491</u></b>
464.h264ref	595	37.2	<b><u>596</u></b>	<b><u>37.1</u></b>	601	36.8	543	40.8	543	40.8	<b><u>543</u></b>	<b><u>40.8</u></b>
471.omnetpp	<b><u>291</u></b>	<b><u>21.5</u></b>	290	21.5	291	21.5	<b><u>239</u></b>	<b><u>26.2</u></b>	239	26.1	238	26.3
473.astar	352	20.0	<b><u>351</u></b>	<b><u>20.0</u></b>	350	20.0	<b><u>338</u></b>	<b><u>20.8</u></b>	339	20.7	335	20.9
483.xalancbmk	193	35.7	<b><u>193</u></b>	<b><u>35.7</u></b>	191	36.1	191	36.1	190	36.2	<b><u>191</u></b>	<b><u>36.2</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Platform Notes

Fan speed set to full Speed (ie. Enterprise Blade mode) with Smart Blade Console through CMM (Chassis Management Module)

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AB2x280 F1, and Gateway GB2x280 F1 are electronically equivalent.  
This result was measured on Gateway GB2x280 F1.

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

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## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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