SPEC® CINT2006 Result
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Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5570, 2.93 GHz)

SPECint®_rate2006 = 268

SPECint_rate_base2006 = 253

CPU2006 license: 97	**Test date:** Jul-2010
Test sponsor: Acer Incorporated	**Hardware Availability:** Aug-2010
Tested by: Acer Incorporated	**Software Availability:** Jan-2010

Copies 0 30.0 60.0 90.0 120 150 180 210 240 270 300 330 360 390 420 450 480 510 540 570 600 630 660 690 720 770

Benchmark	Copies	Value
400.perlbench	16	252
	16	215
401.bzip2	16	165
	16	156
403.gcc	16	211
	16	209
429.mcf	8	288
	16	274
445.gobmk	16	269
	16	248
456.hmmer	8	356
	16	340
458.sjeng	16	251
	16	230
462.libquantum	16	763
	16	763
464.h264ref	16	332
	16	319
471.omnetpp	16	191
	16	177
473.astar	16	173
	16	157
483.xalancbmk	16	271

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Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 1000 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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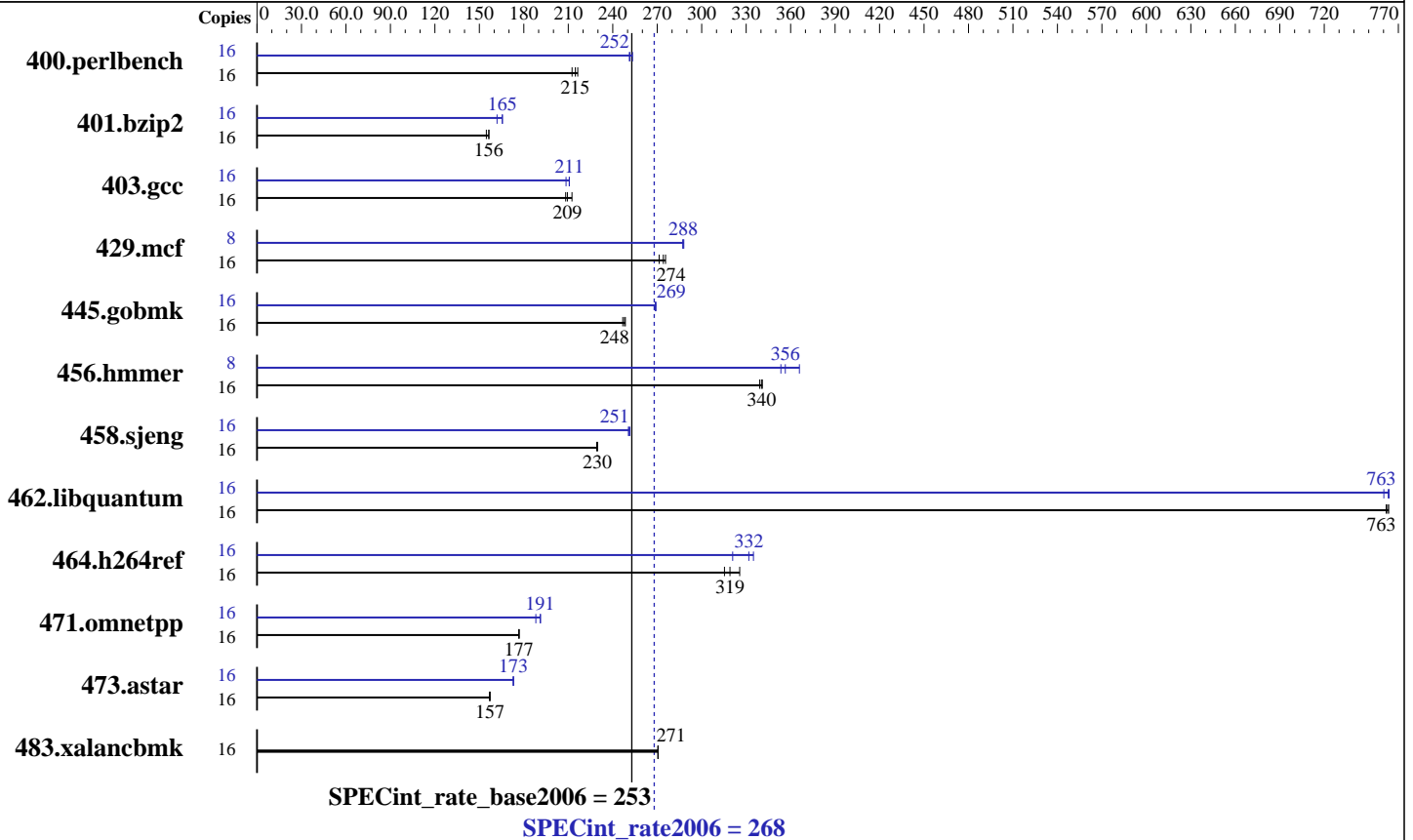
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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	722	216	735	213	**728**	**215**	16	**621**	**252**	622	251	617	253
401.bzip2	16	998	155	987	156	**987**	**156**	16	953	162	933	166	**933**	**165**
403.gcc	16	606	212	618	208	**615**	**209**	16	611	211	618	208	**611**	**211**
429.mcf	16	**532**	**274**	538	271	529	276	8	254	287	**254**	**288**	253	288
445.gobmk	16	680	247	**677**	**248**	675	249	16	625	269	**624**	**269**	624	269
456.hmmer	16	**439**	**340**	438	341	440	339	8	204	366	**209**	**356**	211	353
458.sjeng	16	843	230	845	229	**844**	**230**	16	**772**	**251**	769	252	772	251
462.libquantum	16	435	762	**435**	**763**	434	763	16	436	760	**434**	**763**	434	764
464.h264ref	16	1087	326	1123	315	**1110**	**319**	16	1057	335	**1067**	**332**	1103	321
471.omnetpp	16	566	177	**566**	**177**	565	177	16	523	191	**523**	**191**	532	188
473.astar	16	714	157	**715**	**157**	716	157	16	649	173	650	173	**650**	**173**
483.xalancbmk	16	**408**	**271**	408	271	408	270	16	**408**	**271**	408	271	408	270

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1
and Gateway GW2000ht-GW170ht F1 are electronically equivalent.
This result was measured on GW2000ht-GW170ht F1.
```

Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

    462.libquantum: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
     456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll4 -auto-ilp32

 462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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