



# SPEC<sup>®</sup> CINT2006 Result

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## Itaotec

## SPECint<sup>®</sup>\_rate2006 = 120

### Servidor Itaotec LX113 (Intel Xeon X3470)

## SPECint\_rate\_base2006 = 112

CPU2006 license: 9001

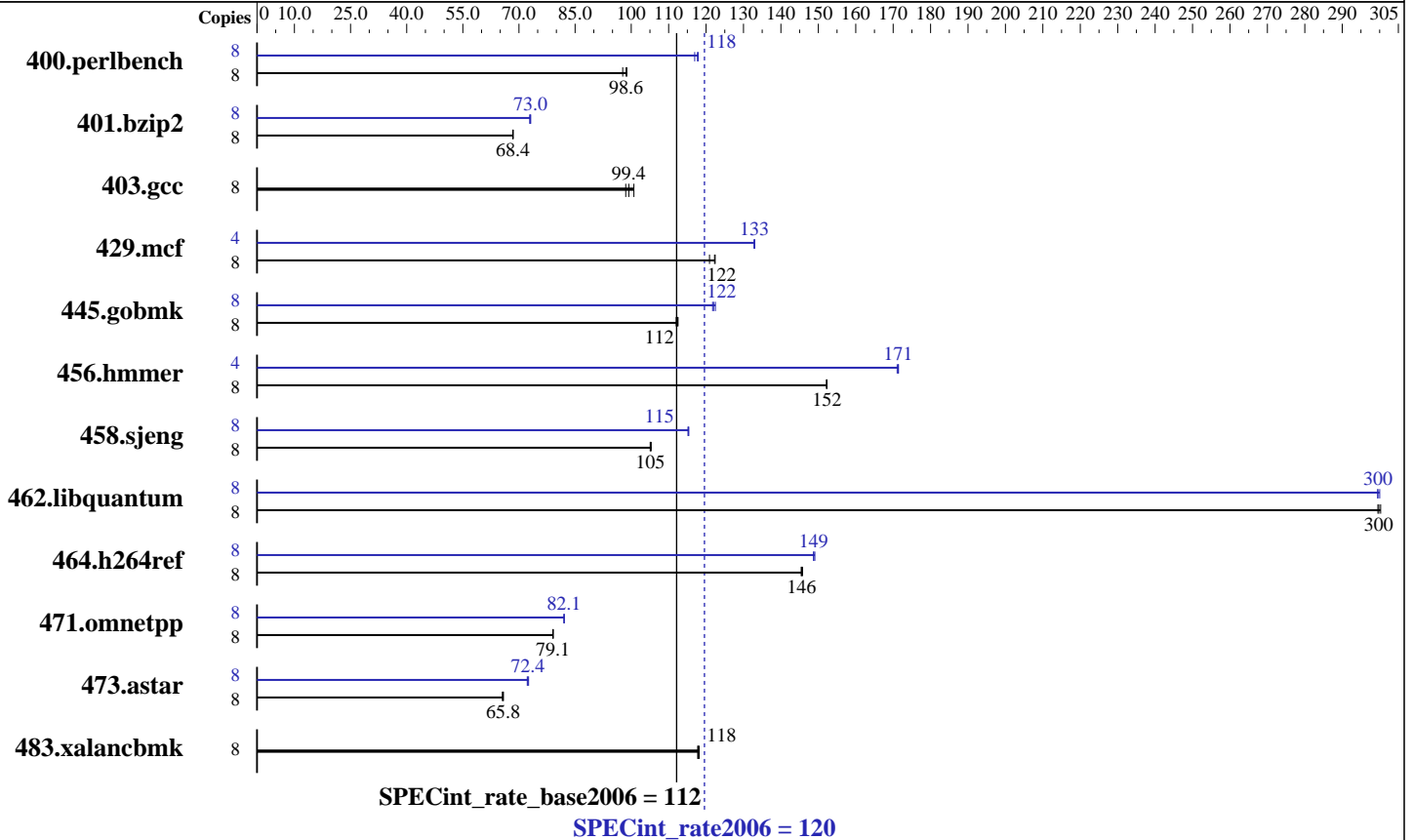
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2011

Hardware Availability: Dec-2009

Software Availability: Apr-2010



### Hardware

CPU Name: Intel Xeon X3470  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (4 x 2 GB 2Rx8 PC3-10600U-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l\_cproc\_p\_11.1.072  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	800	97.7	<u>792</u>	<u>98.6</u>	791	98.8	8	663	118	<u>664</u>	<u>118</u>	668	117
401.bzip2	8	1127	68.5	1130	68.3	<u>1129</u>	<u>68.4</u>	8	<u>1057</u>	<u>73.0</u>	1055	73.1	1060	72.9
403.gcc	8	654	98.5	<u>648</u>	<u>99.4</u>	639	101	8	654	98.5	<u>648</u>	<u>99.4</u>	639	101
429.mcf	8	603	121	596	122	<u>597</u>	<u>122</u>	4	274	133	<u>274</u>	<u>133</u>	275	133
445.gobmk	8	746	112	749	112	<u>746</u>	<u>112</u>	8	689	122	685	122	<u>688</u>	<u>122</u>
456.hammer	8	491	152	<u>490</u>	<u>152</u>	490	152	4	218	171	218	171	<u>218</u>	<u>171</u>
458.sjeng	8	921	105	<u>921</u>	<u>105</u>	919	105	8	840	115	<u>840</u>	<u>115</u>	839	115
462.libquantum	8	552	300	553	300	<u>553</u>	<u>300</u>	8	554	299	553	300	<u>553</u>	<u>300</u>
464.h264ref	8	1217	145	<u>1216</u>	<u>146</u>	1214	146	8	1190	149	<u>1190</u>	<u>149</u>	1188	149
471.omnetpp	8	<u>632</u>	<u>79.1</u>	633	79.0	632	79.1	8	<u>609</u>	<u>82.1</u>	609	82.1	610	82.0
473.astar	8	857	65.6	<u>854</u>	<u>65.8</u>	853	65.8	8	774	72.6	<u>776</u>	<u>72.4</u>	778	72.2
483.xalancbmk	8	<u>468</u>	<u>118</u>	469	118	467	118	8	<u>468</u>	<u>118</u>	469	118	467	118

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec LX114.  
The Servidor Itaotec LX103, Servidor Itaotec LX113 and the Servidor Itaotec LX114 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
462.libquantum: icc -m64  
C++ benchmarks (except as noted below):  
icpc -m32  
473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

-L/opt/sh/SmartHeap\_8/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 19:27:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 April 2011.