



# SPEC® CINT2006 Result

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## Cisco Systems

### SPECint®\_rate2006 = Not Run

Cisco UCS C260 M2 (Intel Xeon E7-2870, 2.40 GHz)

### SPECint\_rate\_base2006 = 526

CPU2006 license: 9019

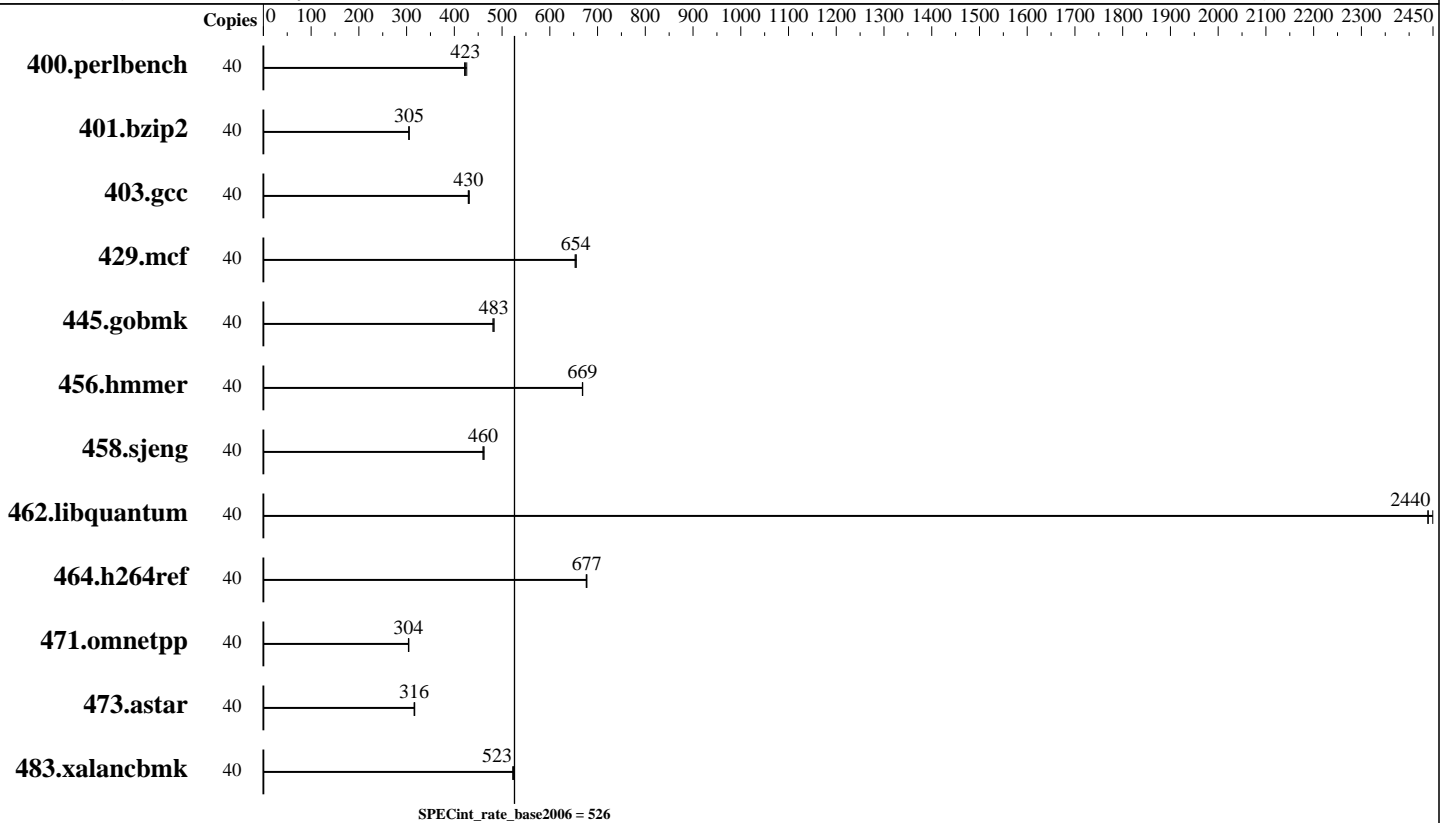
Test date: Mar-2011

Test sponsor: Cisco Systems

Hardware Availability: Jun-2011

Tested by: Cisco Systems

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E7-2870  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 4Rx4 PC3-8500R-9, ECC)  
 Disk Subsystem: 2 x 64 GB SSD, SATA Gen2, 3Gb/s  
 Other Hardware: None

### Software

Operating System: Redhat Enterprise Linux 6.0,  
 Kernel 2.6.32-71.el6.x86\_64  
 Compiler: Intel C++ Compiler XE for applications running  
 on IA-32  
 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	917	426	927	421	<u>924</u>	<u>423</u>							
401.bzip2	40	<u>1265</u>	<u>305</u>	1264	305	1267	305							
403.gcc	40	747	431	750	429	<u>749</u>	<u>430</u>							
429.mcf	40	559	653	<u>557</u>	<u>654</u>	557	655							
445.gobmk	40	<u>869</u>	<u>483</u>	869	483	873	481							
456.hammer	40	558	669	<u>558</u>	<u>669</u>	559	668							
458.sjeng	40	1047	462	1052	460	<u>1051</u>	<u>460</u>							
462.libquantum	40	<u>340</u>	<u>2440</u>	338	2450	340	2440							
464.h264ref	40	<u>1307</u>	<u>677</u>	1307	678	1309	676							
471.omnetpp	40	820	305	823	304	<u>822</u>	<u>304</u>							
473.astar	40	889	316	886	317	<u>888</u>	<u>316</u>							
483.xalancbmk	40	527	524	528	522	<u>527</u>	<u>523</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## Platform Notes

BIOS Configuration : Data Reuse Optimization = Disabled

## General Notes

Binaries compiled on RHEL5.5 with  
binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
icpc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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