



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint®_rate2006 = 232

Gateway GR380 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 220

CPU2006 license: 97

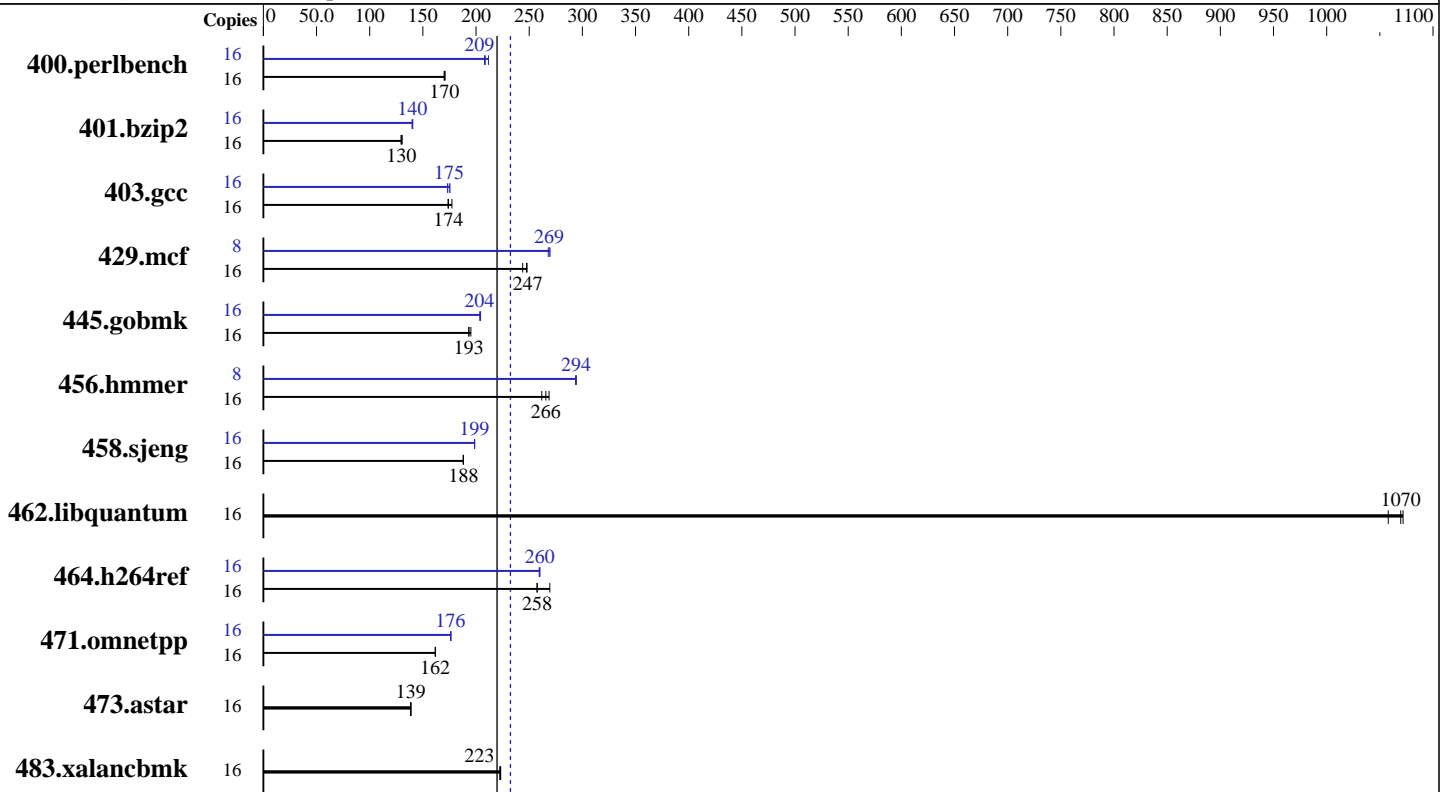
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011



SPECint_rate2006 = 232

SPECint_rate_base2006 = 220

Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 232

Gateway GR380 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	920	170	<u>917</u>	<u>170</u>	915	171	16	738	212	752	208	<u>749</u>	<u>209</u>
401.bzip2	16	1193	129	<u>1188</u>	<u>130</u>	1183	131	16	<u>1102</u>	<u>140</u>	1099	141	1104	140
403.gcc	16	726	177	<u>740</u>	<u>174</u>	742	174	16	744	173	<u>737</u>	<u>175</u>	734	175
429.mcf	16	598	244	588	248	<u>590</u>	<u>247</u>	8	272	268	271	270	<u>271</u>	<u>269</u>
445.gobmk	16	860	195	869	193	<u>868</u>	<u>193</u>	16	824	204	822	204	<u>824</u>	<u>204</u>
456.hammer	16	570	262	<u>562</u>	<u>266</u>	556	269	8	254	294	254	294	<u>254</u>	<u>294</u>
458.sjeng	16	1031	188	1029	188	<u>1029</u>	<u>188</u>	16	974	199	975	199	<u>975</u>	<u>199</u>
462.libquantum	16	309	1070	313	1060	<u>310</u>	<u>1070</u>	16	309	1070	313	1060	<u>310</u>	<u>1070</u>
464.h264ref	16	1314	269	1376	257	<u>1375</u>	<u>258</u>	16	1362	260	1365	259	<u>1362</u>	<u>260</u>
471.omnetpp	16	618	162	619	162	<u>618</u>	<u>162</u>	16	568	176	567	176	<u>568</u>	<u>176</u>
473.astar	16	809	139	810	139	<u>810</u>	<u>139</u>	16	809	139	810	139	<u>810</u>	<u>139</u>
483.xalancbmk	16	495	223	497	222	<u>495</u>	<u>223</u>	16	495	223	497	222	<u>495</u>	<u>223</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Balanced)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
This result was measured on the Gateway GR380 F1.
The Acer AR360 F1, AR380 F1, Gateway GR360 F1 are electronically equivalent

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 232

Gateway GR380 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 232

Gateway GR380 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 232

Gateway GR380 F1 (Intel Xeon E5620)

SPECint_rate_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 20:49:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 May 2011.