



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 103

PRIMERGY TX140 S1, Intel Xeon E3-1260L, 2.40 GHz

SPECfp_rate_base2006 = 101

CPU2006 license: 19

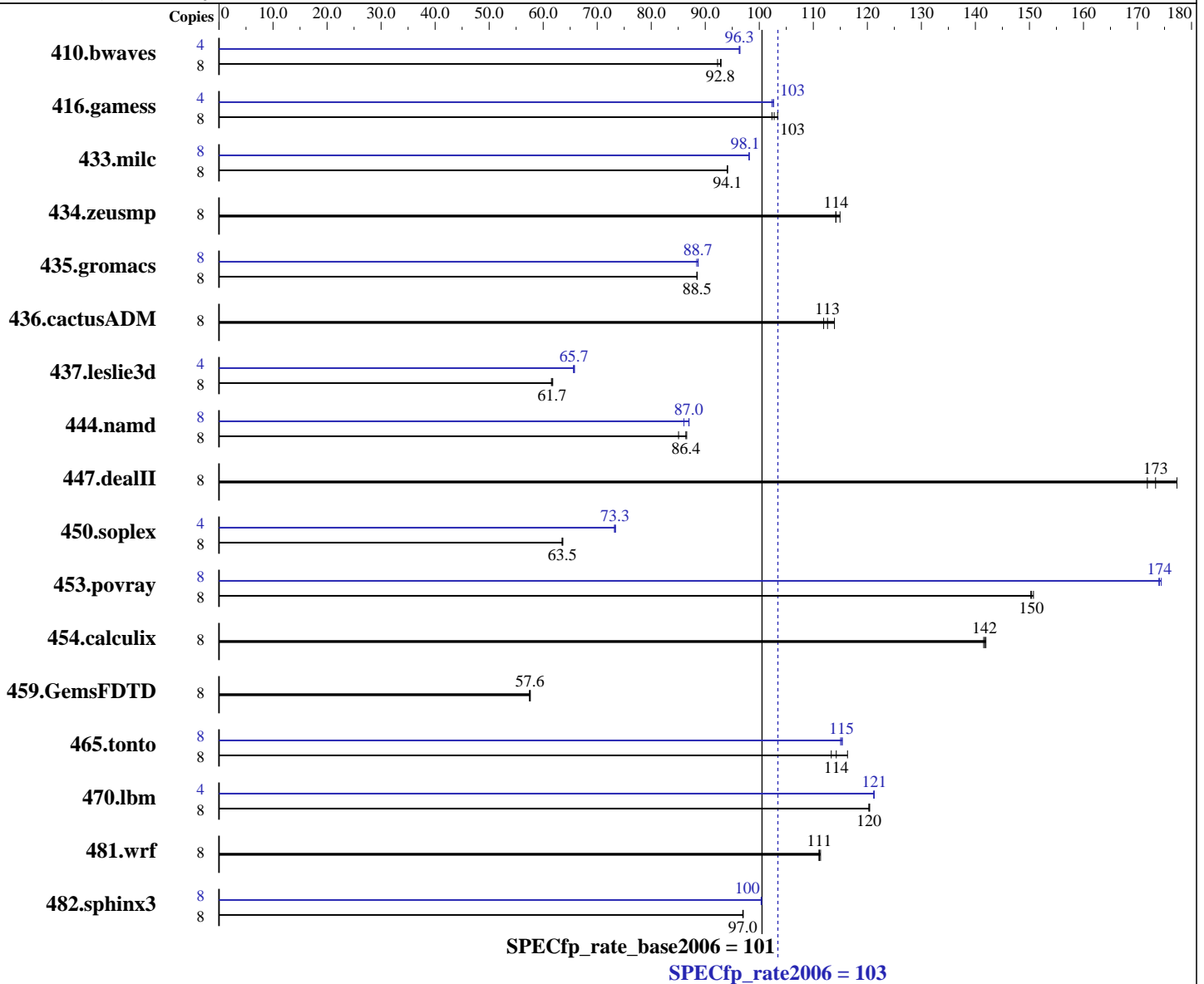
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2011

Hardware Availability: Jun-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1178	92.3	1170	92.9	<u>1171</u>	<u>92.8</u>	4	<u>564</u>	<u>96.3</u>	564	96.4	564	96.3
416.gamess	8	1531	102	1515	103	<u>1524</u>	<u>103</u>	4	<u>763</u>	<u>103</u>	765	102	763	103
433.milc	8	780	94.1	<u>780</u>	<u>94.1</u>	781	94.1	8	748	98.1	<u>748</u>	<u>98.1</u>	749	98.1
434.zeusmp	8	638	114	633	115	<u>637</u>	<u>114</u>	8	638	114	633	115	<u>637</u>	<u>114</u>
435.gromacs	8	<u>646</u>	<u>88.5</u>	645	88.5	646	88.5	8	644	88.7	<u>644</u>	<u>88.7</u>	646	88.4
436.cactusADM	8	<u>849</u>	<u>113</u>	839	114	854	112	8	<u>849</u>	<u>113</u>	839	114	854	112
437.leslie3d	8	<u>1220</u>	<u>61.7</u>	1222	61.5	1217	61.8	4	<u>572</u>	<u>65.7</u>	573	65.6	571	65.8
444.namd	8	754	85.1	<u>743</u>	<u>86.4</u>	741	86.6	8	737	87.0	746	86.1	<u>738</u>	<u>87.0</u>
447.dealII	8	516	177	533	172	<u>528</u>	<u>173</u>	8	516	177	533	172	<u>528</u>	<u>173</u>
450.soplex	8	1048	63.6	<u>1050</u>	<u>63.5</u>	1051	63.5	4	455	73.4	456	73.2	<u>455</u>	<u>73.3</u>
453.povray	8	282	151	<u>283</u>	<u>150</u>	283	150	8	<u>244</u>	<u>174</u>	245	174	244	174
454.calculix	8	465	142	<u>466</u>	<u>142</u>	466	142	8	465	142	<u>466</u>	<u>142</u>	466	142
459.GemsFDTD	8	1473	57.6	1477	57.5	<u>1475</u>	<u>57.6</u>	8	1473	57.6	1477	57.5	<u>1475</u>	<u>57.6</u>
465.tonto	8	677	116	695	113	<u>689</u>	<u>114</u>	8	684	115	682	115	<u>683</u>	<u>115</u>
470.lbm	8	913	120	<u>913</u>	<u>120</u>	914	120	4	<u>453</u>	<u>121</u>	454	121	453	121
481.wrf	8	<u>804</u>	<u>111</u>	805	111	803	111	8	<u>804</u>	<u>111</u>	805	111	803	111
482.sphinx3	8	<u>1608</u>	<u>97.0</u>	1607	97.0	1608	97.0	8	<u>1553</u>	<u>100</u>	1552	100	1554	100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5
This result was measured on the PRIMERGY TX120 S3. The PRIMERGY TX120 S3

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General Notes (Continued)

and the PRIMERGY TX140 S1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`482.sphinx3: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex: icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.deallI: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml>

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For other inquiries, please contact webmaster@spec.org.

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