



# SPEC® CINT2006 Result

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## Bull SAS

**SPECint®2006 = 36.0**

NovaScale T810B F2 (Intel Core i3-2100, 3.10 GHz)

**SPECint\_base2006 = 34.1**

CPU2006 license: 20

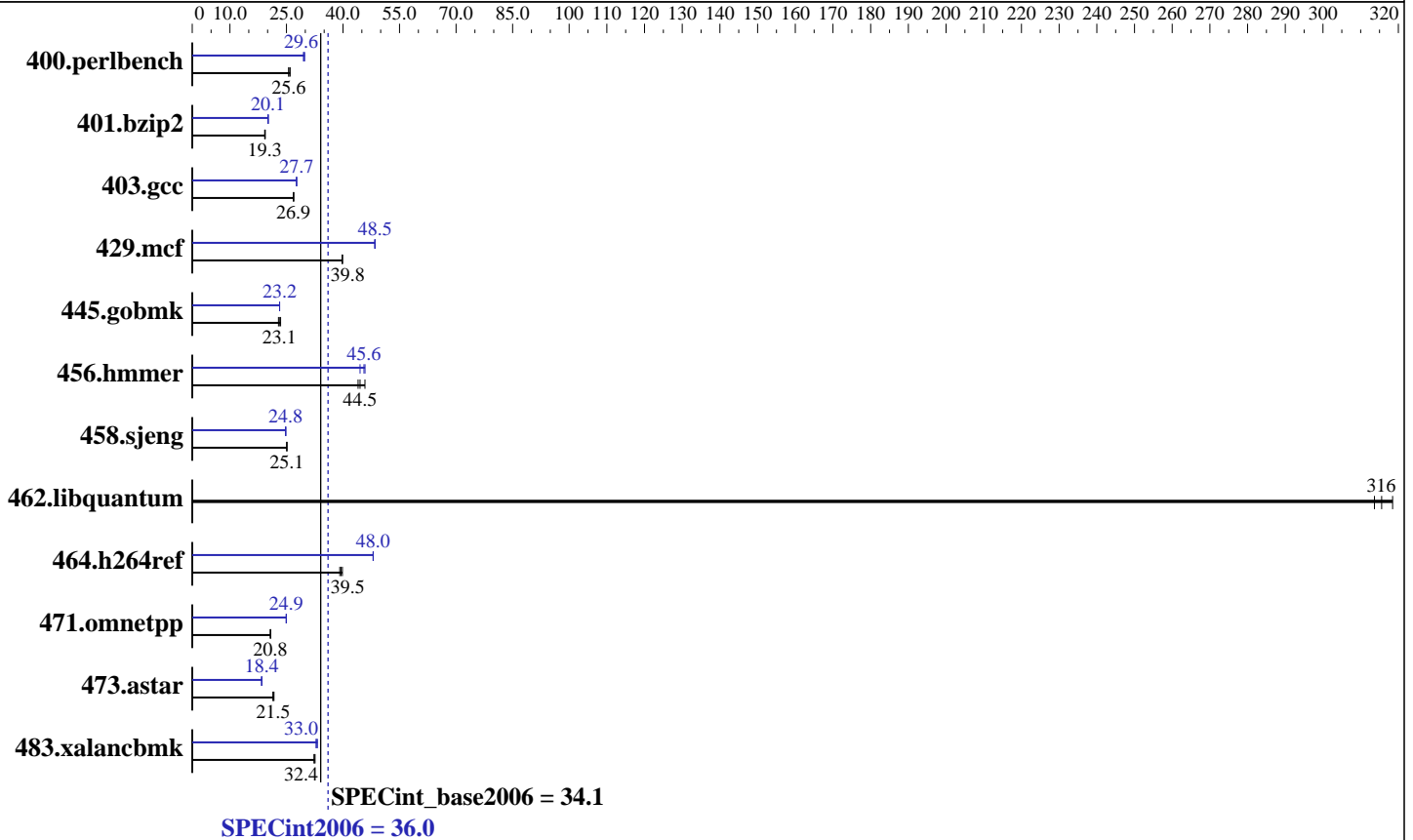
Test date: Mar-2011

Test sponsor: Bull SAS

Hardware Availability: May-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i3-2100  
 CPU Characteristics:  
 CPU MHz: 3100  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 3 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (4 x 2 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	376	26.0	382	25.6	<b>381</b>	<b>25.6</b>	331	29.5	<b>330</b>	<b>29.6</b>	328	29.8
401.bzip2	498	19.4	502	19.2	<b>501</b>	<b>19.3</b>	478	20.2	<b>480</b>	<b>20.1</b>	480	20.1
403.gcc	299	26.9	<b>299</b>	<b>26.9</b>	300	26.9	291	27.7	291	27.7	<b>291</b>	<b>27.7</b>
429.mcf	229	39.9	<b>229</b>	<b>39.8</b>	229	39.8	<b>188</b>	<b>48.5</b>	188	48.5	188	48.4
445.gobmk	447	23.4	<b>454</b>	<b>23.1</b>	459	22.9	453	23.2	<b>453</b>	<b>23.2</b>	452	23.2
456.hammer	204	45.8	212	44.0	<b>210</b>	<b>44.5</b>	204	45.8	210	44.5	<b>205</b>	<b>45.6</b>
458.sjeng	<b>482</b>	<b>25.1</b>	482	25.1	482	25.1	487	24.8	489	24.8	<b>488</b>	<b>24.8</b>
462.libquantum	66.1	314	<b>65.7</b>	<b>316</b>	65.1	318	66.1	314	<b>65.7</b>	<b>316</b>	65.1	318
464.h264ref	565	39.1	555	39.8	<b>561</b>	<b>39.5</b>	461	48.0	461	48.0	<b>461</b>	<b>48.0</b>
471.omnetpp	301	20.7	301	20.8	<b>301</b>	<b>20.8</b>	<b>251</b>	<b>24.9</b>	251	24.9	250	25.0
473.astar	324	21.7	328	21.4	<b>326</b>	<b>21.5</b>	380	18.5	381	18.4	<b>381</b>	<b>18.4</b>
483.xalancbmk	<b>213</b>	<b>32.4</b>	212	32.6	213	32.3	210	32.9	<b>209</b>	<b>33.0</b>	208	33.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Logical Processor = Disabled (Default = Enabled)

## General Notes

OMP\_NUM\_THREADS set to number of cores  
The Dell PowerEdge T110 II and  
the Bull NovaScale T810B F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge T110 II model  
Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32

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## Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`

`-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch`

`-ansi-alias`

`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`

`-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch`

`-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`

`-opt-malloc-options=3 -auto-ilp32`

`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`

`-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

`-ansi-alias`

`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`

`-auto-ilp32 -ansi-alias`

`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

456.hmmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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