



# SPEC<sup>®</sup> CINT2006 Result

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Dell Inc.

SPECint<sup>®</sup>\_rate2006 = Not Run

PowerEdge R810 (Intel Xeon E7-4850, 2.00 GHz)

SPECint\_rate\_base2006 = 423

CPU2006 license: 55

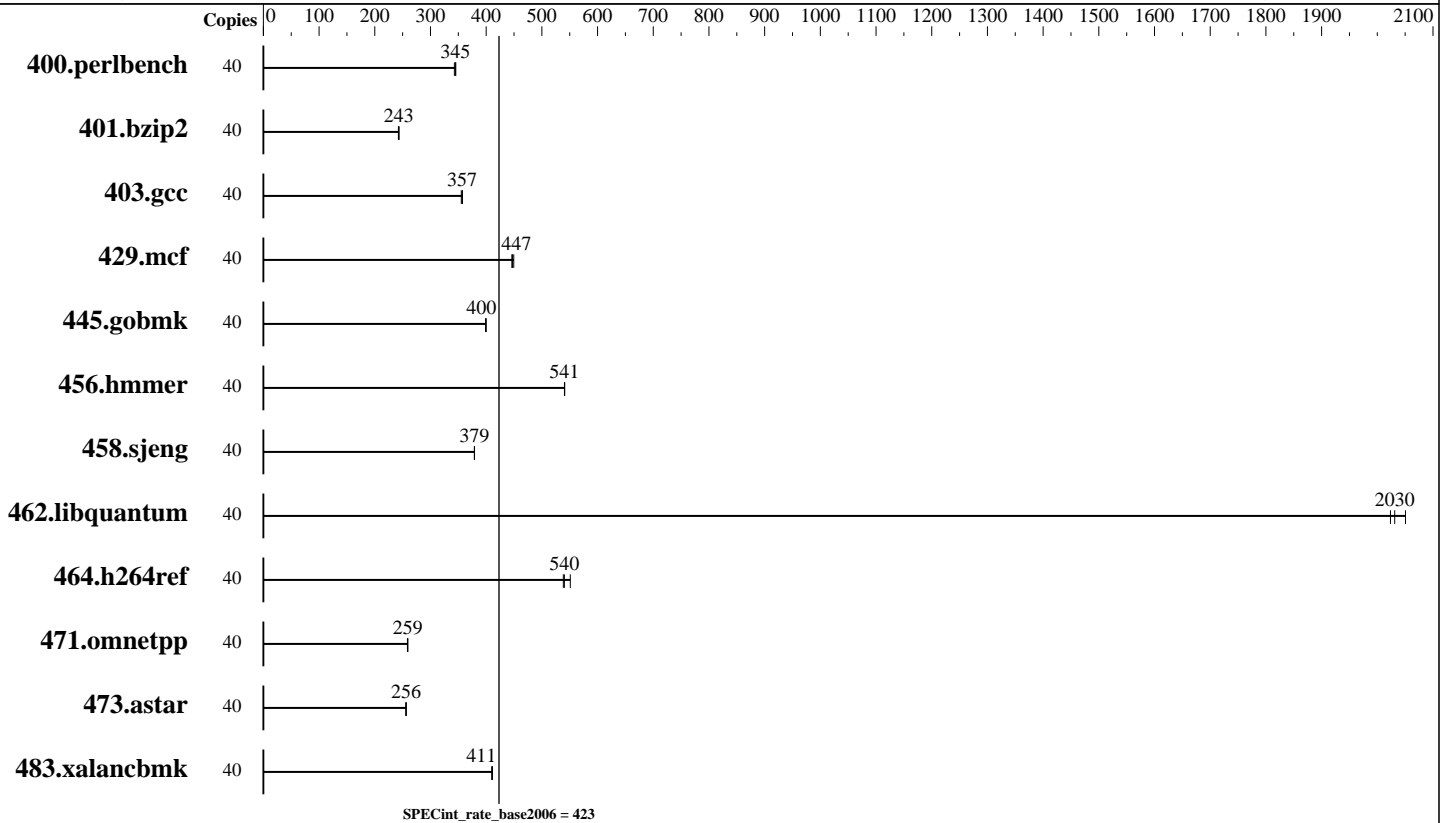
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



## Hardware

CPU Name: Intel Xeon E7-4850  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC)  
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	1132	345	<b><u>1132</u></b>	<b><u>345</u></b>	1140	343							
401.bzip2	40	1586	243	1592	242	<b><u>1587</u></b>	<b><u>243</u></b>							
403.gcc	40	906	355	901	357	<b><u>902</u></b>	<b><u>357</u></b>							
429.mcf	40	818	446	811	450	<b><u>816</u></b>	<b><u>447</u></b>							
445.gobmk	40	1050	400	<b><u>1050</u></b>	<b><u>400</u></b>	1052	399							
456.hammer	40	690	541	<b><u>690</u></b>	<b><u>541</u></b>	690	541							
458.sjeng	40	<b><u>1277</u></b>	<b><u>379</u></b>	1277	379	1278	379							
462.libquantum	40	<b><u>408</u></b>	<b><u>2030</u></b>	410	2020	404	2050							
464.h264ref	40	1644	539	<b><u>1638</u></b>	<b><u>540</u></b>	1606	551							
471.omnetpp	40	<b><u>965</u></b>	<b><u>259</u></b>	967	259	964	259							
473.astar	40	<b><u>1096</u></b>	<b><u>256</u></b>	1098	256	1095	257							
483.xalancbmk	40	673	410	<b><u>672</u></b>	<b><u>411</u></b>	671	411							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 18000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)

## General Notes

Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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