



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 235

PowerEdge R710 (Intel Xeon E5620, 2.40 GHz)

SPECint_rate_base2006 = 221

CPU2006 license: 55

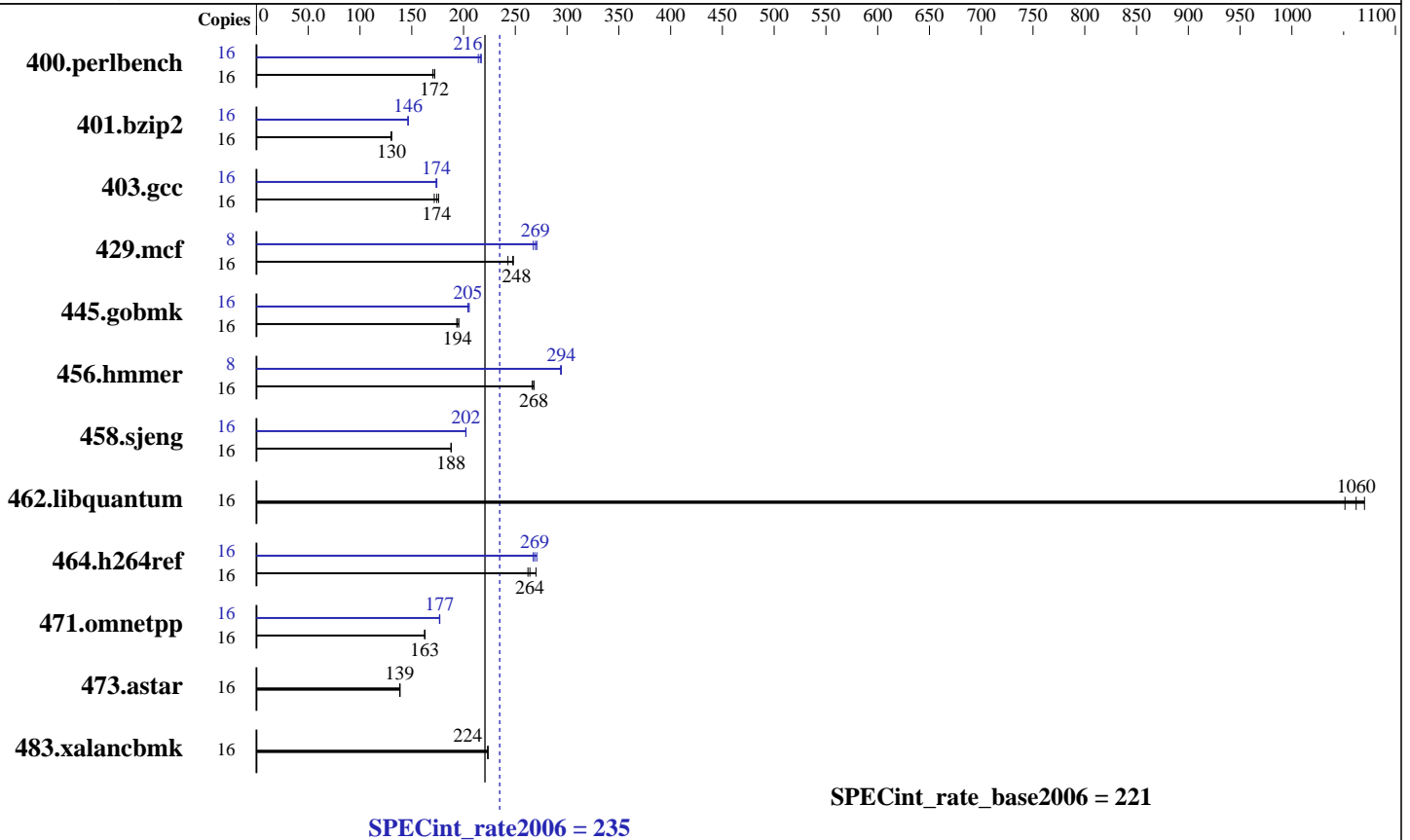
Test date: Apr-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E5620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 235

PowerEdge R710 (Intel Xeon E5620, 2.40 GHz)

SPECint_rate_base2006 = 221

CPU2006 license: 55

Test date: Apr-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	908	172	918	170	<u>909</u>	<u>172</u>	16	730	214	<u>723</u>	<u>216</u>	720	217
401.bzip2	16	<u>1184</u>	<u>130</u>	1187	130	1184	130	16	1054	147	<u>1054</u>	<u>146</u>	1055	146
403.gcc	16	732	176	<u>739</u>	<u>174</u>	750	172	16	739	174	742	174	<u>741</u>	<u>174</u>
429.mcf	16	601	243	<u>589</u>	<u>248</u>	588	248	8	273	267	269	271	<u>271</u>	<u>269</u>
445.gobmk	16	<u>864</u>	<u>194</u>	858	196	868	193	16	817	206	822	204	<u>820</u>	<u>205</u>
456.hammer	16	560	266	557	268	<u>558</u>	<u>268</u>	8	<u>254</u>	<u>294</u>	253	295	254	294
458.sjeng	16	<u>1030</u>	<u>188</u>	1029	188	1030	188	16	958	202	956	202	<u>957</u>	<u>202</u>
462.libquantum	16	315	1050	<u>312</u>	<u>1060</u>	310	1070	16	315	1050	<u>312</u>	<u>1060</u>	310	1070
464.h264ref	16	1311	270	1350	262	<u>1341</u>	<u>264</u>	16	1308	271	<u>1317</u>	<u>269</u>	1325	267
471.omnetpp	16	<u>615</u>	<u>163</u>	616	162	614	163	16	<u>565</u>	<u>177</u>	566	177	565	177
473.astar	16	<u>811</u>	<u>139</u>	811	138	811	139	16	<u>811</u>	<u>139</u>	811	138	811	139
483.xalancbmk	16	494	224	494	223	<u>494</u>	<u>224</u>	16	494	224	494	223	<u>494</u>	<u>224</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge R710 and the Bull NovaScale R460 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R710 model. Binaries were compiled on RHEL5.5



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 235

PowerEdge R710 (Intel Xeon E5620, 2.40 GHz)

SPECint_rate_base2006 = 221

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2011

Hardware Availability: Mar-2010

Software Availability: Jan-2011

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 235

PowerEdge R710 (Intel Xeon E5620, 2.40 GHz)

SPECint_rate_base2006 = 221

CPU2006 license: 55

Test date: Apr-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Jan-2011

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 235

PowerEdge R710 (Intel Xeon E5620, 2.40 GHz)

SPECint_rate_base2006 = 221

CPU2006 license: 55

Test date: Apr-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 22:48:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 16 August 2011.