



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 42.0

Intel DH61WW motherboard (Intel Core i5-2400)

SPECint_base2006 = 39.9

CPU2006 license: 13

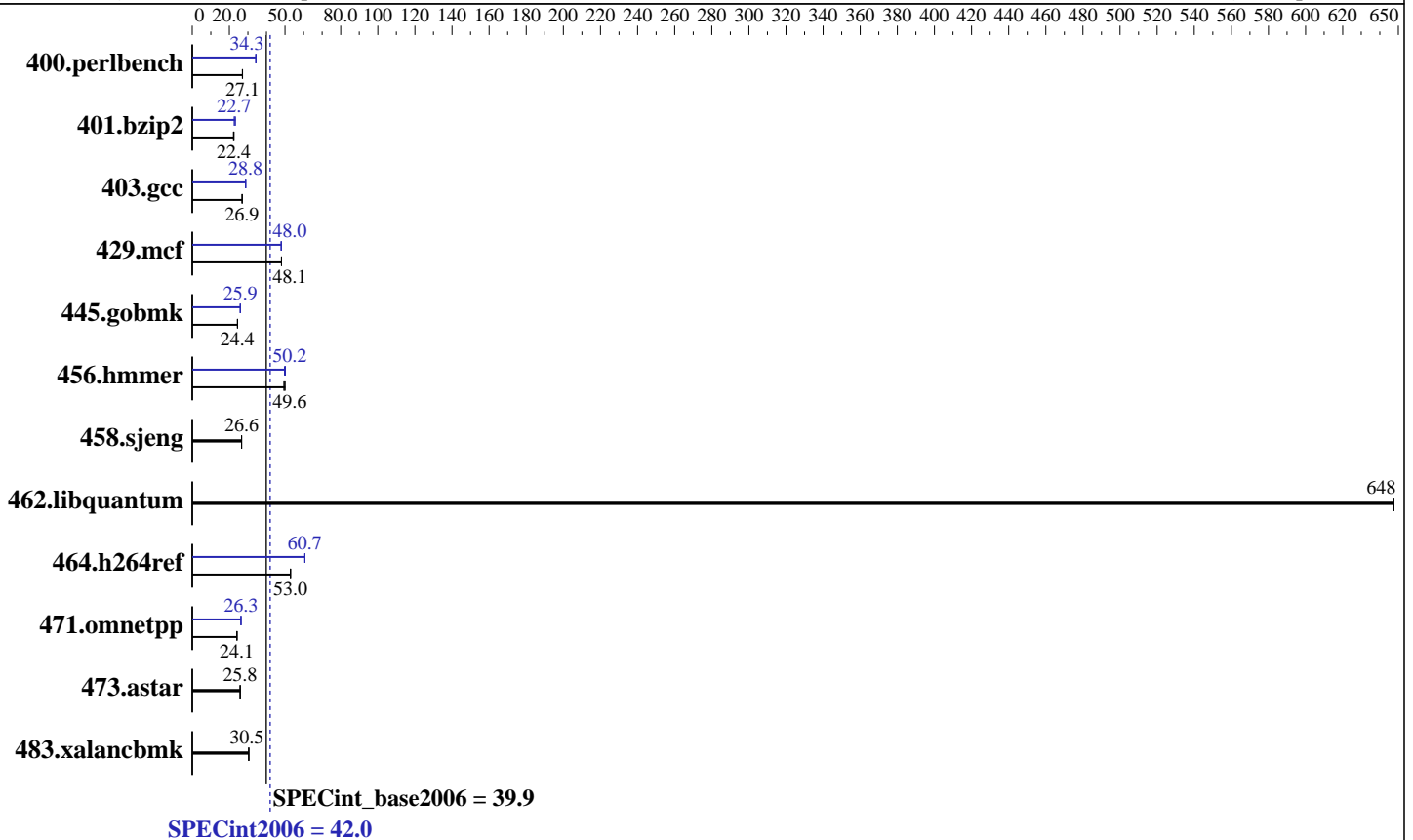
Test date: Oct-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i5-2400
 CPU Characteristics: Intel Turbo Boost Technology up to 3.4 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 250GB HDD, 7200 rpm
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate SP1 (64-bit)
 Compiler: C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows;
 Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	361	27.0	361	27.1	361	27.1	285	34.3	285	34.3	285	34.3
401.bzip2	436	22.2	430	22.5	431	22.4	425	22.7	425	22.7	414	23.3
403.gcc	300	26.9	300	26.9	300	26.9	280	28.8	279	28.8	280	28.8
429.mcf	190	48.1	190	48.1	190	48.1	190	47.9	190	48.0	190	48.0
445.gobmk	429	24.4	429	24.4	429	24.4	404	25.9	405	25.9	404	25.9
456.hammer	189	49.4	186	50.1	188	49.6	186	50.2	188	49.7	186	50.2
458.sjeng	455	26.6	455	26.6	455	26.6	455	26.6	455	26.6	455	26.6
462.libquantum	32.0	648	32.0	648	32.0	648	32.0	648	32.0	648	32.0	648
464.h264ref	418	52.9	417	53.0	417	53.0	364	60.7	364	60.7	364	60.7
471.omnetpp	259	24.1	260	24.1	259	24.1	238	26.3	238	26.3	238	26.2
473.astar	272	25.8	272	25.9	272	25.8	272	25.8	272	25.9	272	25.8
483.xalancbmk	227	30.5	227	30.5	227	30.4	227	30.5	227	30.5	227	30.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Component Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

General Notes

OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
```

```
-DSPEC_CPU_NO_NEED_VA_COPY
```

```
401.bzip2: -DSPEC_CPU_P64
```

```
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
```

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Base Portability Flags (Continued)

```

429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

```

Base Optimization Flags

C benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000

```

C++ benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlw64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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