



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 195

PRIMERGY TX300 S7, Intel Xeon E5-2637, 3.0 GHz

SPECint_rate_base2006 = 187

CPU2006 license: 19

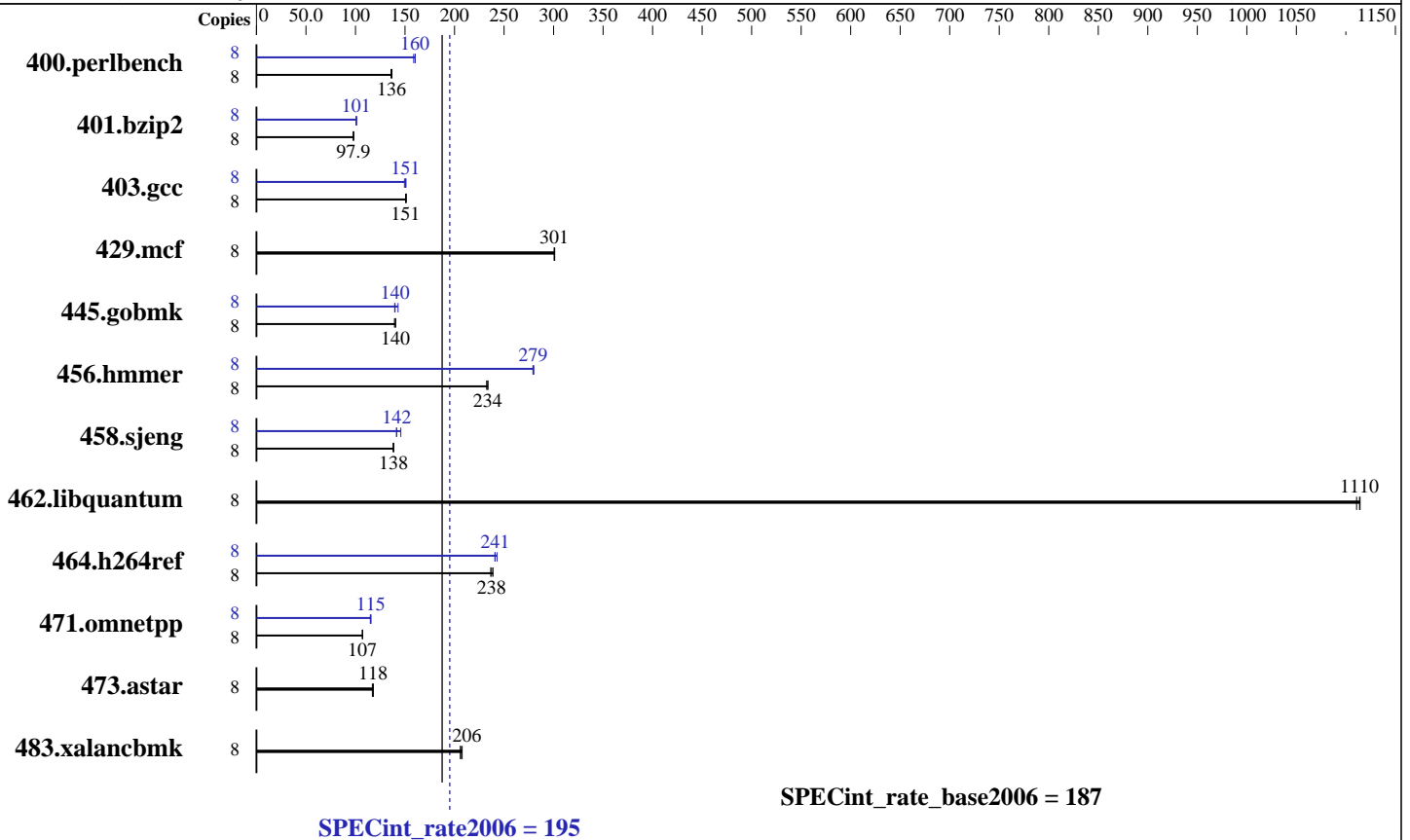
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2637
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 5 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	575	136	<u>573</u>	<u>136</u>	571	137	8	492	159	488	160	<u>488</u>	<u>160</u>
401.bzip2	8	<u>789</u>	<u>97.9</u>	787	98.1	790	97.8	8	768	101	<u>766</u>	<u>101</u>	764	101
403.gcc	8	427	151	<u>427</u>	<u>151</u>	425	151	8	426	151	<u>428</u>	<u>151</u>	431	149
429.mcf	8	243	301	242	301	<u>243</u>	<u>301</u>	8	243	301	242	301	<u>243</u>	<u>301</u>
445.gobmk	8	597	140	<u>598</u>	<u>140</u>	602	139	8	587	143	<u>600</u>	<u>140</u>	601	140
456.hammer	8	319	234	<u>320</u>	<u>234</u>	321	232	8	267	279	<u>267</u>	<u>279</u>	267	280
458.sjeng	8	<u>699</u>	<u>138</u>	701	138	699	138	8	<u>683</u>	<u>142</u>	664	146	685	141
462.libquantum	8	<u>149</u>	<u>1110</u>	149	1110	149	1110	8	<u>149</u>	<u>1110</u>	149	1110	149	1110
464.h264ref	8	748	237	741	239	<u>745</u>	<u>238</u>	8	735	241	729	243	<u>735</u>	<u>241</u>
471.omnetpp	8	467	107	467	107	<u>467</u>	<u>107</u>	8	433	116	435	115	<u>433</u>	<u>115</u>
473.astar	8	<u>477</u>	<u>118</u>	480	117	476	118	8	<u>477</u>	<u>118</u>	480	117	476	118
483.xalancbmk	8	<u>268</u>	<u>206</u>	268	206	266	208	8	<u>268</u>	<u>206</u>	268	206	266	208

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7 and the PRIMERGY TX300 S7 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 07:19:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 April 2012.