



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint®2006 = 37.3

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = 34.8

CPU2006 license: 20

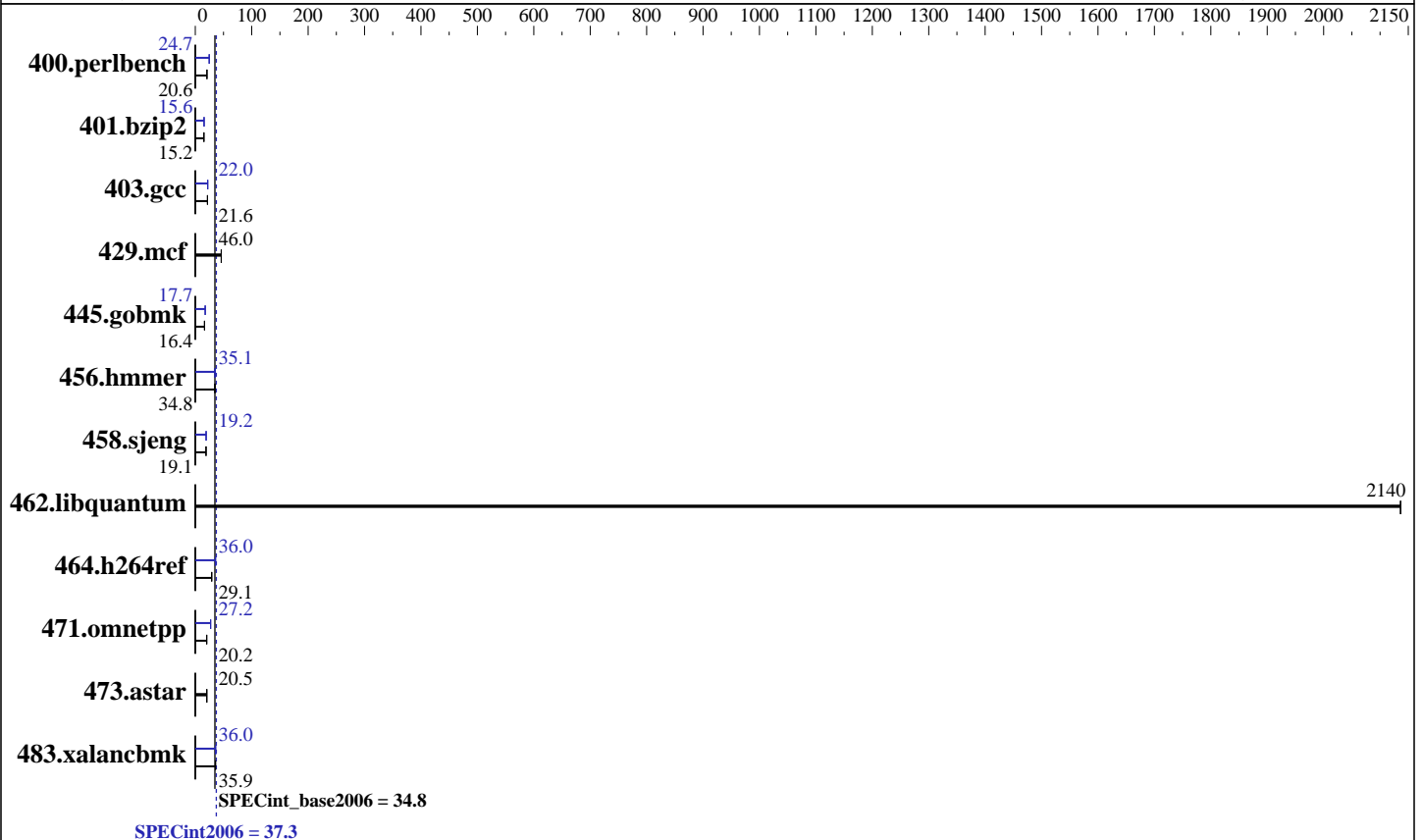
Test date: Mar-2012

Test sponsor: Bull SAS

Hardware Availability: May-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2450L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 2 x 300 GB 15000 RPM SAS, RAID 1
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (add definition here)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = **37.3**

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = **34.8**

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Dell Inc.

Test date: Mar-2012
Hardware Availability: May-2012
Software Availability: Feb-2012

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	473	20.6	473	20.6	475	20.6	395	24.7	396	24.7	396	24.7
401.bzip2	633	15.2	633	15.2	633	15.2	620	15.6	620	15.6	620	15.6
403.gcc	372	21.6	373	21.6	373	21.6	365	22.0	365	22.0	365	22.0
429.mcf	198	46.0	198	46.0	198	46.0	198	46.0	198	46.0	198	46.0
445.gobmk	641	16.4	643	16.3	641	16.4	594	17.7	594	17.7	602	17.4
456.hammer	268	34.9	268	34.8	268	34.8	268	34.9	266	35.1	266	35.1
458.sjeng	641	18.9	634	19.1	633	19.1	630	19.2	630	19.2	630	19.2
462.libquantum	9.70	2140	9.70	2140	9.70	2140	9.70	2140	9.70	2140	9.70	2140
464.h264ref	760	29.1	762	29.0	762	29.1	615	36.0	623	35.5	614	36.0
471.omnetpp	308	20.3	309	20.2	309	20.2	230	27.2	230	27.2	230	27.2
473.astar	350	20.1	342	20.5	343	20.5	350	20.1	342	20.5	343	20.5
483.xalancbmk	192	35.9	192	35.9	193	35.8	192	36.0	191	36.1	192	35.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```

CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Slik Tue Mar 6 17:11:45 2012

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2450L 0 @ 1.80GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 37.3

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = 34.8

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2012

Hardware Availability: May-2012

Software Availability: Feb-2012

Platform Notes (Continued)

cache size : 20480 KB

From /proc/meminfo

MemTotal: 49381468 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 11 (x86_64)

VERSION = 11

PATCHLEVEL = 2

uname -a:

Linux Slik 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012 (54ddfaf)

x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 6 17:08 last=S

SPEC is set to: /root/CPU2006-1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext3	266G	7.4G	245G	3%	/

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

The Dell PowerEdge R420 and

the Bull NovaScale R430 F3 models are electronically equivalent.

The results have been measured on a Dell PowerEdge R420 model



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 37.3

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = 34.8

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Dell Inc.

Test date: Mar-2012
Hardware Availability: May-2012
Software Availability: Feb-2012

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 37.3

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = 34.8

CPU2006 license: 20

Test date: Mar-2012

Test sponsor: Bull SAS

Hardware Availability: May-2012

Tested by: Dell Inc.

Software Availability: Feb-2012

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

SPECint2006 = 37.3

NovaScale R430 F3 (Intel Xeon E5-2450L, 1.80 GHz)

SPECint_base2006 = 34.8

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2012

Hardware Availability: May-2012

Software Availability: Feb-2012

Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 06:03:55 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 5 June 2012.