



# SPEC® CINT2006 Result

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## IBM Corporation

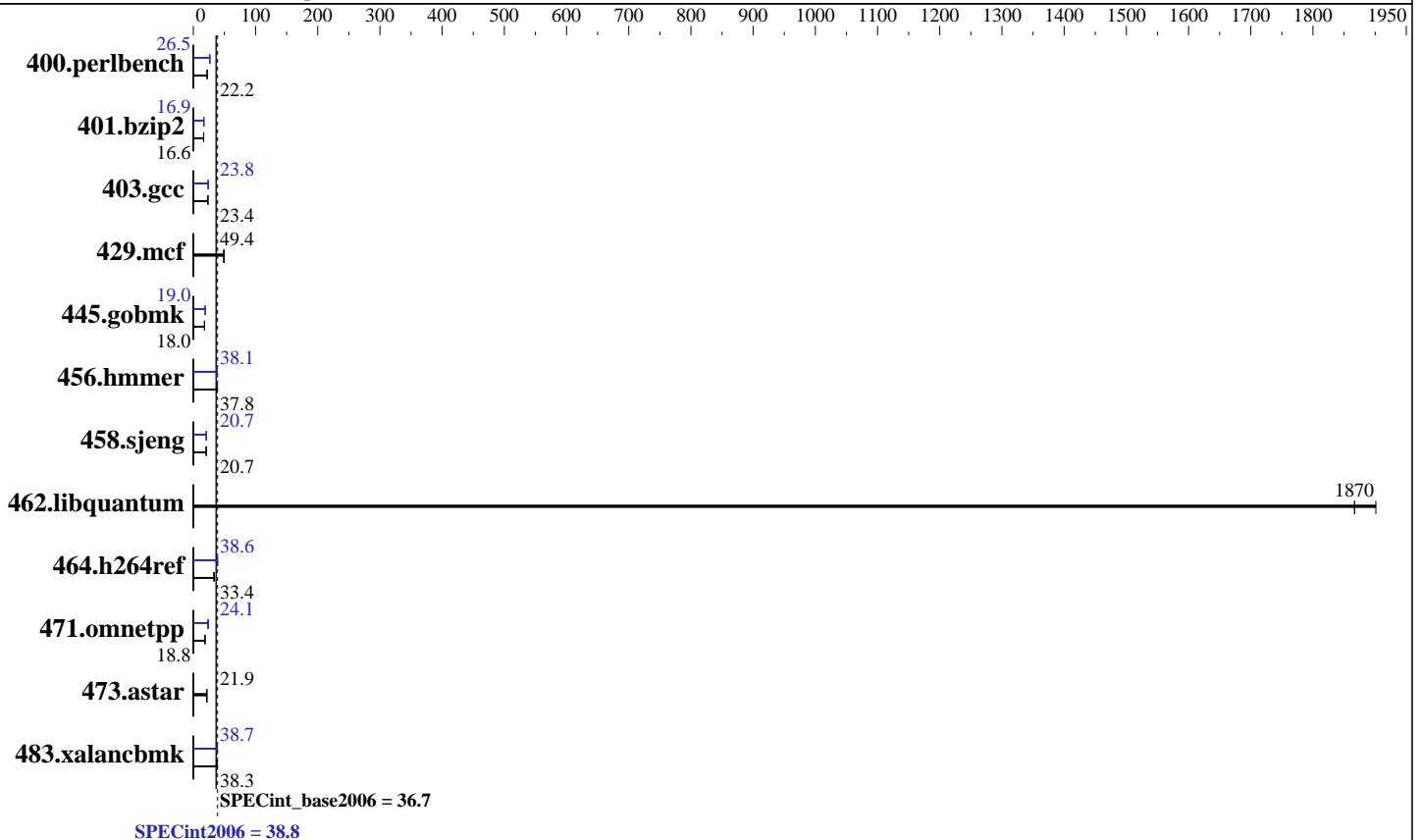
IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2620)

**SPECint®2006 = 38.8**

**SPECint\_base2006 = 36.7**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** Jun-2012  
**Hardware Availability:** Mar-2012  
**Software Availability:** Dec-2011



### Hardware

**CPU Name:** Intel Xeon E5-2620  
**CPU Characteristics:** Intel Turbo Boost Technology up to 2.50 GHz  
**CPU MHz:** 2000  
**FPU:** Integrated  
**CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1,2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 15 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
**Disk Subsystem:** 1 x 250 GB SATA, 7200 RPM  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
**Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>439</b>	<b>22.2</b>	439	22.3	440	22.2	<b>368</b>	<b>26.5</b>	368	26.6	371	26.3
401.bzip2	581	16.6	581	16.6	<b>581</b>	<b>16.6</b>	570	16.9	570	16.9	<b>570</b>	<b>16.9</b>
403.gcc	<b>344</b>	<b>23.4</b>	343	23.4	344	23.4	338	23.8	338	23.8	<b>338</b>	<b>23.8</b>
429.mcf	<b>185</b>	<b>49.4</b>	188	48.6	184	49.5	<b>185</b>	<b>49.4</b>	188	48.6	184	49.5
445.gobmk	<b>584</b>	<b>18.0</b>	584	18.0	583	18.0	<b>552</b>	<b>19.0</b>	551	19.0	552	19.0
456.hammer	247	37.8	<b>247</b>	<b>37.8</b>	247	37.8	<b>245</b>	<b>38.1</b>	245	38.1	245	38.0
458.sjeng	<b>585</b>	<b>20.7</b>	585	20.7	585	20.7	583	20.7	<b>584</b>	<b>20.7</b>	584	20.7
462.libquantum	10.9	1900	<b>11.1</b>	<b>1870</b>	11.1	1870	10.9	1900	<b>11.1</b>	<b>1870</b>	11.1	1870
464.h264ref	663	33.4	666	33.3	<b>663</b>	<b>33.4</b>	576	38.4	563	39.3	<b>573</b>	<b>38.6</b>
471.omnetpp	331	18.9	<b>332</b>	<b>18.8</b>	334	18.7	270	23.1	259	24.1	<b>259</b>	<b>24.1</b>
473.astar	321	21.9	329	21.3	<b>321</b>	<b>21.9</b>	321	21.9	329	21.3	<b>321</b>	<b>21.9</b>
483.xalancbmk	180	38.4	181	38.1	<b>180</b>	<b>38.3</b>	179	38.5	178	38.8	<b>178</b>	<b>38.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
Operating Mode set to Maximum Performance  
Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on tianden Wed Jun 6 04:44:56 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12  
physical 0: cores 0 1 2 3 4 5  
physical 1: cores 0 1 2 3 4 5

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## Platform Notes (Continued)

cache size : 15360 KB

```
From /proc/meminfo
MemTotal:      132094664 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 4 15:48

```
SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_tianden-lv_root
                ext4      145G   61G   78G  44% /
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"  
OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smartheap -lsmartheap64`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`445.gobmk: icc -m32`

`464.h264ref: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

`473.astar: icpc -m64`



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
          -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
          -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2)
            -opt-ra-region-strategy=block -ansi-alias
            -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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