



# SPEC® CINT2006 Result

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## HITACHI

**SPECint®2006 = 38.8**

BladeSymphony BS520A (Intel Xeon E5-2430L)

**SPECint\_base2006 = 36.6**

CPU2006 license: 35

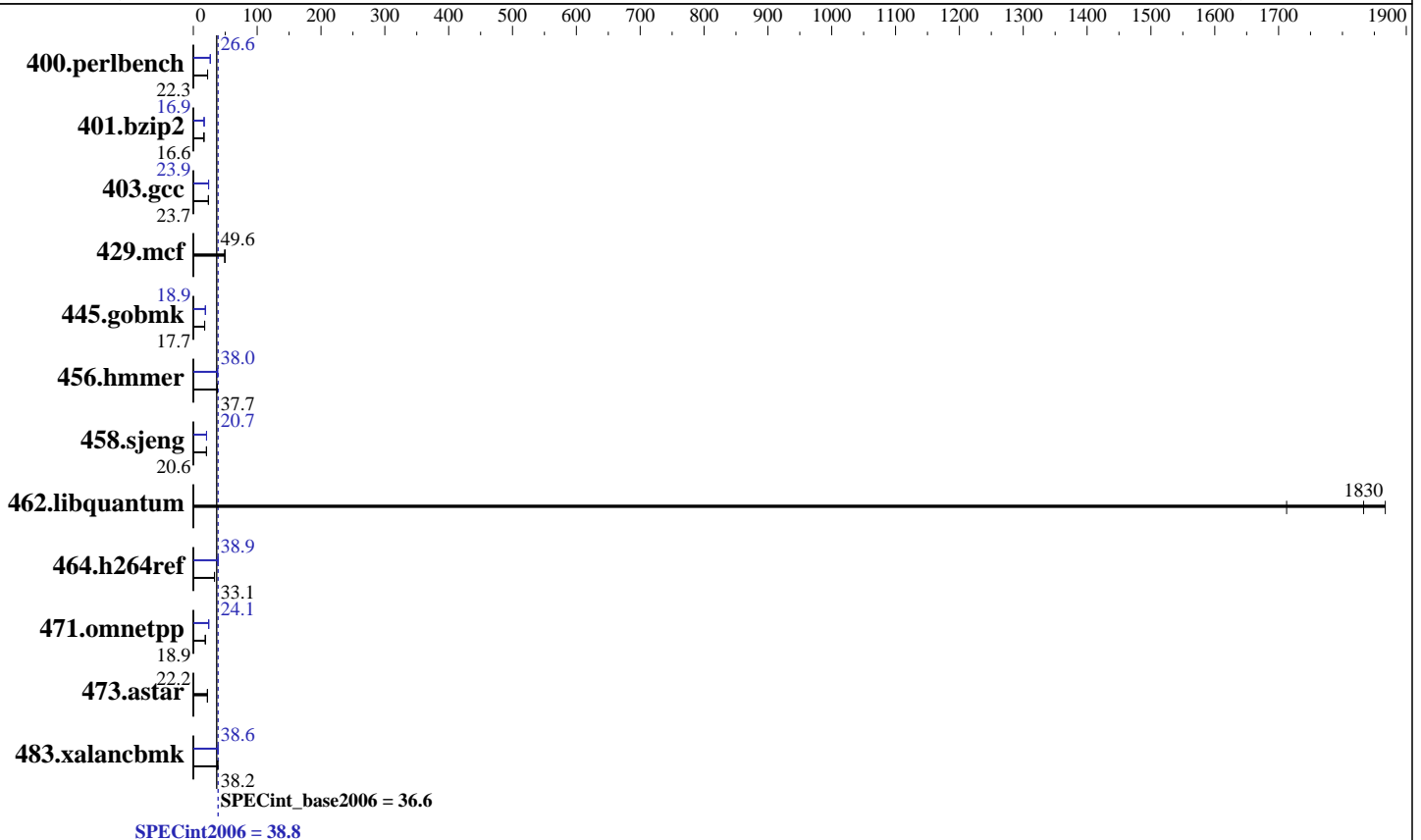
Test date: Jun-2012

Test sponsor: HITACHI

Hardware Availability: Jun-2012

Tested by: HITACHI

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2430L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-10600R-9, ECC)  
 Disk Subsystem: 2 x 147 GB SAS, 15000 RPM RAID1 configuration  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.4.2.el6.x86\_64  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	438	22.3	<b>437</b>	<b>22.3</b>	437	22.4	<b>367</b>	<b>26.6</b>	367	26.6	368	26.6
401.bzip2	580	16.6	<b>581</b>	<b>16.6</b>	582	16.6	<b>570</b>	<b>16.9</b>	570	16.9	570	16.9
403.gcc	<b>340</b>	<b>23.7</b>	340	23.7	341	23.6	<b>337</b>	<b>23.9</b>	336	23.9	337	23.9
429.mcf	<b>184</b>	<b>49.6</b>	186	49.1	184	49.7	<b>184</b>	<b>49.6</b>	186	49.1	184	49.7
445.gobmk	591	17.7	<b>591</b>	<b>17.7</b>	591	17.8	554	18.9	554	18.9	<b>554</b>	<b>18.9</b>
456.hammer	248	37.6	247	37.7	<b>248</b>	<b>37.7</b>	<b>246</b>	<b>38.0</b>	246	38.0	246	37.9
458.sjeng	587	20.6	<b>587</b>	<b>20.6</b>	587	20.6	585	20.7	584	20.7	<b>584</b>	<b>20.7</b>
462.libquantum	11.1	1870	12.1	1710	<b>11.3</b>	<b>1830</b>	11.1	1870	12.1	1710	<b>11.3</b>	<b>1830</b>
464.h264ref	670	33.1	<b>668</b>	<b>33.1</b>	667	33.2	568	39.0	576	38.4	<b>569</b>	<b>38.9</b>
471.omnetpp	332	18.8	331	18.9	<b>331</b>	<b>18.9</b>	260	24.1	259	24.1	<b>260</b>	<b>24.1</b>
473.astar	319	22.0	316	22.2	<b>316</b>	<b>22.2</b>	319	22.0	316	22.2	<b>316</b>	<b>22.2</b>
483.xalancbmk	180	38.3	181	38.1	<b>181</b>	<b>38.2</b>	<b>179</b>	<b>38.6</b>	178	38.7	179	38.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Tue Jun 19 09:06:41 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2430L 0 @ 2.00GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal: 99044504 kB
```

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### Platform Notes (Continued)

HugePages\_Total: 0  
Hugepagesize: 2048 kB

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-220.4.2.el6.x86_64 #1 SMP Mon Feb 6
16:39:28 EST 2012 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 19 09:02
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"  
OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

HITACHI BladeSymphony BS520A and HITACHI Compute Blade 520A are electronically equivalent.  
The results have been measured on a HITACHI BladeSymphony BS520A.

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smartheap -lsmartheap64`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`445.gobmk: icc -m32`

`464.h264ref: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

`473.astar: icpc -m64`



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 12:11:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 August 2012.