



SPEC[®] CINT2006 Result

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IBM Corporation

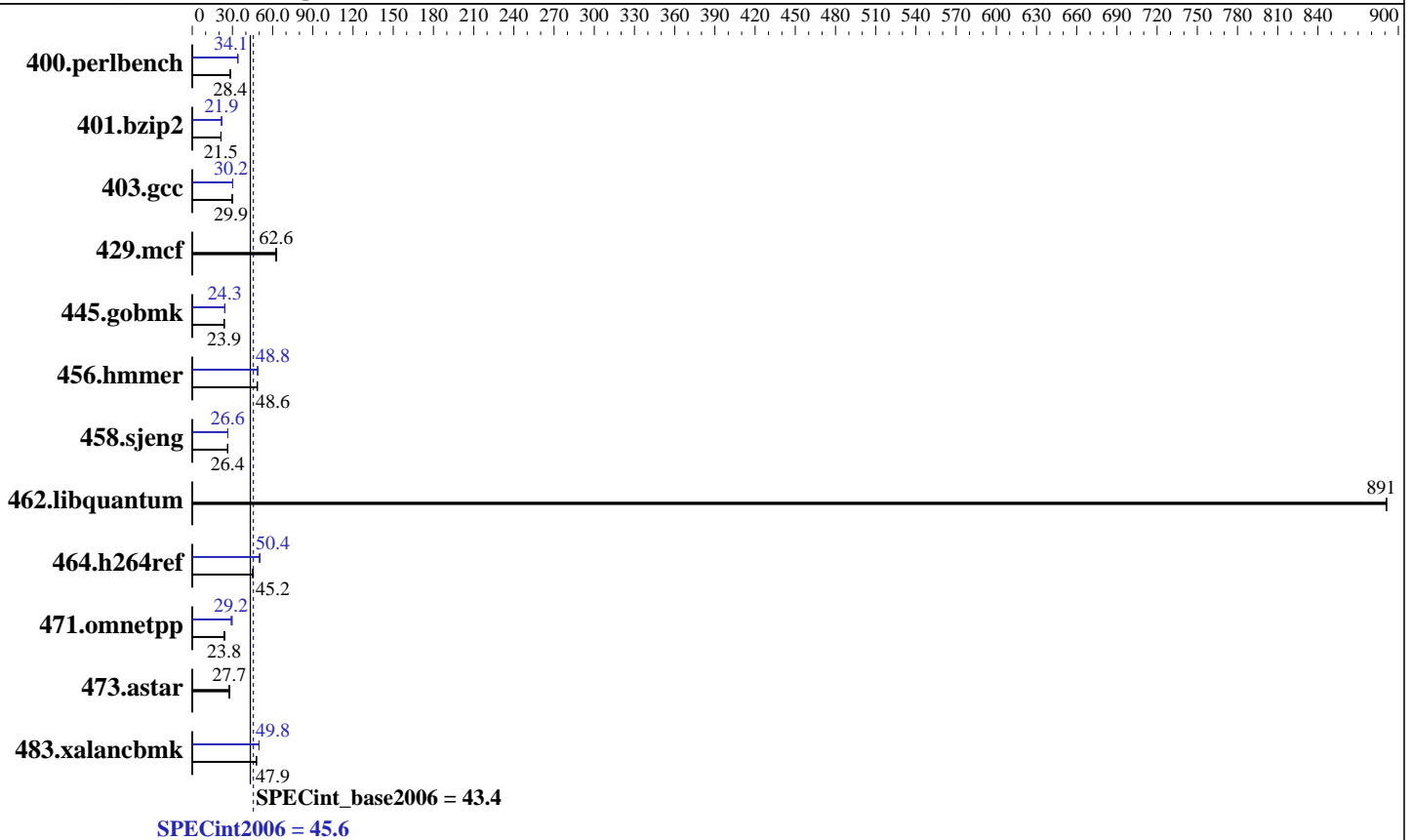
IBM System x3300 M4
(Intel Xeon E5-1410 2.80 GHz)

SPECint[®]2006 = **45.6**

SPECint_base2006 = **43.4**

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Oct-2012
Hardware Availability: Aug-2012
Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-1410
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
 Disk Subsystem: 1 x 2 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	345	28.3	343	28.5	<u>344</u>	<u>28.4</u>	<u>287</u>	<u>34.1</u>	287	34.1	288	33.9
401.bzip2	450	21.5	451	21.4	<u>450</u>	<u>21.5</u>	440	21.9	<u>441</u>	<u>21.9</u>	441	21.9
403.gcc	270	29.8	269	29.9	<u>270</u>	<u>29.9</u>	<u>267</u>	<u>30.2</u>	267	30.2	266	30.2
429.mcf	<u>146</u>	<u>62.6</u>	145	63.0	146	62.4	<u>146</u>	<u>62.6</u>	145	63.0	146	62.4
445.gobmk	439	23.9	440	23.9	<u>439</u>	<u>23.9</u>	<u>431</u>	<u>24.3</u>	431	24.3	432	24.3
456.hammer	192	48.6	<u>192</u>	<u>48.6</u>	192	48.5	<u>191</u>	<u>48.8</u>	191	48.8	191	48.8
458.sjeng	458	26.4	458	26.4	<u>458</u>	<u>26.4</u>	455	26.6	456	26.6	<u>455</u>	<u>26.6</u>
462.libquantum	23.3	891	<u>23.3</u>	<u>891</u>	23.3	891	23.3	891	<u>23.3</u>	<u>891</u>	23.3	891
464.h264ref	489	45.3	<u>489</u>	<u>45.2</u>	489	45.2	442	50.1	439	50.5	<u>439</u>	<u>50.4</u>
471.omnetpp	256	24.4	263	23.8	<u>263</u>	<u>23.8</u>	214	29.1	210	29.8	<u>214</u>	<u>29.2</u>
473.astar	252	27.9	<u>254</u>	<u>27.7</u>	255	27.5	252	27.9	<u>254</u>	<u>27.7</u>	255	27.5
483.xalancbmk	144	48.0	144	47.9	<u>144</u>	<u>47.9</u>	139	49.8	<u>138</u>	<u>49.8</u>	138	49.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpul.2/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on YungAn Tue Oct 9 22:45:47 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-1410 0 @ 2.80GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
cache size : 10240 KB
```

From /proc/meminfo

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Platform Notes (Continued)

MemTotal: 49405796 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

`/usr/bin/lsb_release -d`
Red Hat Enterprise Linux Server release 6.2 (Santiago)

`From /etc/*release* /etc/*version*`
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

`uname -a:`
Linux YungAn 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux

`run-level 3 Oct 9 22:40`

SPEC is set to: /home/SPECcpu1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_yungan-lv_home
ext4 1.7T 65G 1.6T 5% /home

Additional information from dmidecode:
Memory:
6x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/SPECcpu1.2/libs/32:/home/SPECcpu1.2/libs/64"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smarterheap -lsmarterheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact webmaster@spec.org.

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