



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 39.6

ProLiant BL460c Gen8
(2.50 GHz, Intel Xeon E5-2609 v2)

SPECint_base2006 = 37.7

CPU2006 license: 3

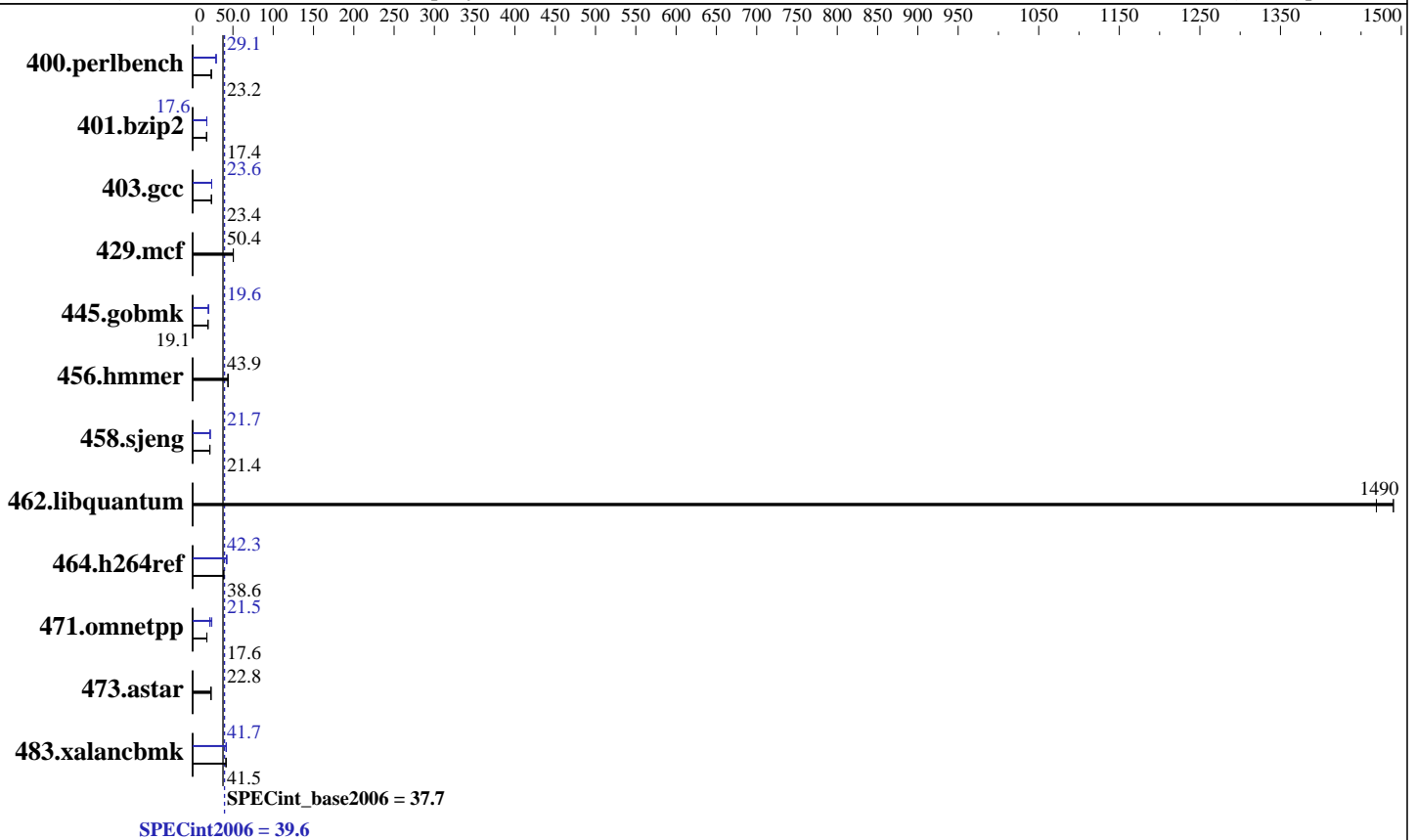
Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2609 v2
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 200 GB SAS SSD, RAID 1
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
 Kernel 3.0.76-0.11-default
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	421	23.2	422	23.2	422	23.2	336	29.1	336	29.1	335	29.1
401.bzip2	555	17.4	556	17.4	555	17.4	549	17.6	549	17.6	549	17.6
403.gcc	343	23.4	345	23.3	344	23.4	341	23.6	341	23.6	340	23.7
429.mcf	181	50.5	181	50.4	181	50.3	181	50.5	181	50.4	181	50.3
445.gobmk	550	19.1	550	19.1	550	19.1	536	19.6	535	19.6	536	19.6
456.hammer	213	43.9	213	43.9	212	43.9	213	43.9	213	43.9	212	43.9
458.sjeng	566	21.4	567	21.3	566	21.4	557	21.7	557	21.7	557	21.7
462.libquantum	13.9	1490	13.9	1490	14.1	1470	13.9	1490	13.9	1490	14.1	1470
464.h264ref	576	38.4	573	38.6	573	38.6	524	42.2	524	42.3	523	42.3
471.omnetpp	354	17.6	353	17.7	355	17.6	290	21.5	291	21.5	263	23.8
473.astar	308	22.8	308	22.8	309	22.8	308	22.8	308	22.8	309	22.8
483.xalancbmk	167	41.4	166	41.5	166	41.5	166	41.6	166	41.7	165	41.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Reclaim mode enabled with:
  echo 1 > /proc/sys/vm/zone_reclaim_mode
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
  echo 1 > /proc/sys/vm/drop_caches
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
HP Power Profile set to Maximum Performance
Minimum Processor Idle Power Core State set to C1E
Minimum Processor Idle Power Package State set to C6 (non-retention)
Energy/Performance Bias is set to Maximum Performance
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x
```

```
Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on bl460cgen8-sys3 Fri Oct 18 17:57:51 2013
```

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2609 v2 @ 2.50GHz
 2 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 4
  siblings     : 4
  physical 0   : cores 0 1 2 3
  physical 1   : cores 0 1 2 3
cache size    : 10240 KB

```

```

From /proc/meminfo
MemTotal:      132130196 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux bl460cgen8-sys3 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 18 17:54 last=S

```

SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext3  176G  9.3G  158G   6% /

```

```

Additional information from dmidecode:
BIOS HP I31 09/08/2013
Memory:
16x HP 689911-071 8 GB 1333 MHz

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmcr: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>



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